

Vishay Siliconix

N-Channel 30-V (D-S) MOSFET

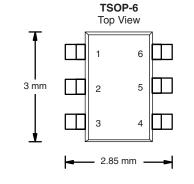
PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)		
30	0.060 at V _{GS} = 10 V	4.5		
	0.085 at V _{GS} = 4.5 V	3.8		



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- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

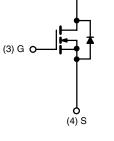




Ordering Information: Si3454ADV-T1-E3 (Lead (Pb)-free) Si3454ADV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Marking Code:

A4xxx



(1, 2, 5, 6) D

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25 \degree C$, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	30		V	
Gate-Source Voltage		V _{GS}	± 20			
	T _A = 25 °C	– I _D	4.5	3.4	•	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		3.6	2.7		
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	20		A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	1.7	1.0		
	T _A = 25 °C	– P _D	2.0	1.14	W	
Maximum Power Dissipation ^a	T _A = 70 °C		1.3	0.73	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 5 s	R _{thJA}	50	62.5		
Maximum Junction-to-Ambient ^a	Steady State		90	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State		30	36		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static			•		•	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.0		3.0	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA
		V_{DS} = 30 V, V_{GS} = 0 V, T_{J} = 70 °C			25	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, $V_{GS} = 10$ V	15			А
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 4.5 A		0.048	0.060	0
		V _{GS} = 4.5 V, I _D = 3.8 A		0.070	0.085	Ω
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 4.5 A		10		S
Diode Forward Voltage ^a	V _{SD}	I _S = 1.7 A, V _{GS} = 0 V		0.8	1.2	V
Dynamic ^b	<u> </u>			1		
Total Gate Charge	Qg			9	15	nC
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 10 V, I_{D} = 4.5 A		2.5		
Gate-Drain Charge	Q _{gd}			1.5		
Gate Resistance	Rg		0.5		2.9	Ω
Turn-On Delay Time	t _{d(on)}			10	20	
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		10	20	ns
Turn-Off Delay Time	t _{d(off)}	${\rm I}_{\rm D}\cong$ 1 A, ${\rm V}_{\rm GEN}$ = 10 V, ${\rm R}_{\rm g}$ = 6 Ω		20	35	
Fall Time	t _f			7	15	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 1.7 A, dI/dt = 100 A/μs		40	80	

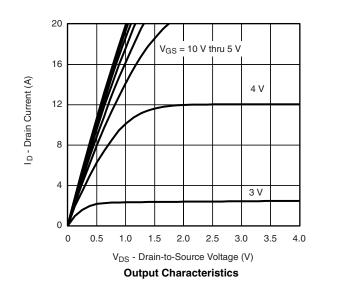
Notes:

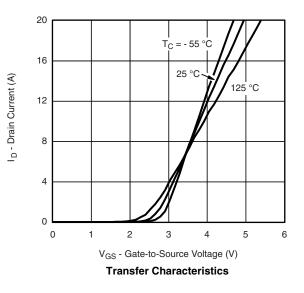
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



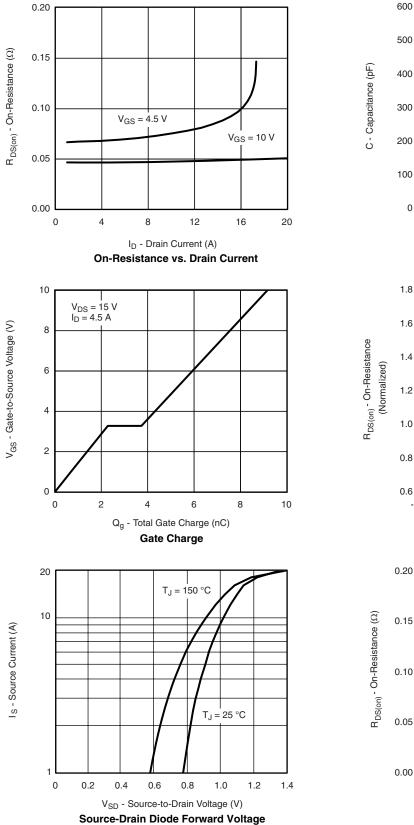


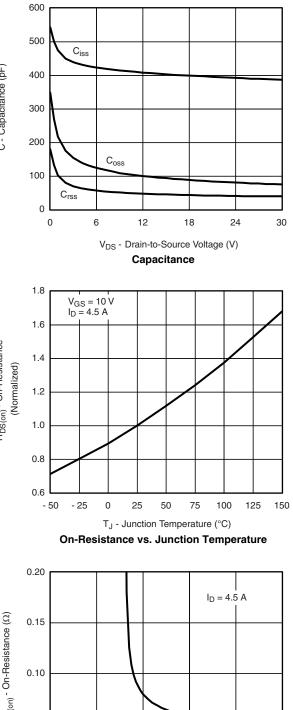


Si3454ADV

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





10

8

0

2

4

V_{GS} - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage

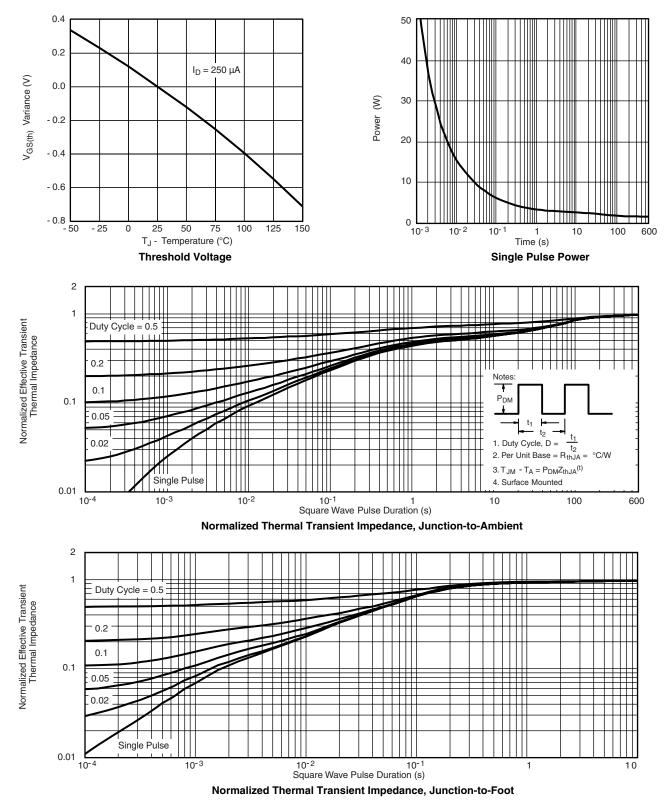
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71108.



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