VS-U5FX240FA120

Vishay Semiconductors



FRED Pt[®] Gen 5 Hyperfast Rectifier Diode, 1200 V, 240 A



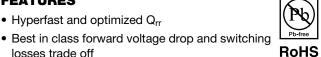
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PRIMARY CHARACTERISTICS						
V _R	1200 V					
V _F (typical) at 120 A, per diode	2.8 V					
t _{rr} (typical) at 120 A, per diode	60 ns					
$I_{F(DC)}$ per module at $T_C = 76 \ ^{\circ}C$	240 A					
Туре	Modules - diode, FRED Pt®					
Package	SOT-227					
Circuit configuration	Two separate diodes, parallel pin-out					

FEATURES

losses trade off

Hyperfast and optimized Q_{rr}



COMPLIANT

- Optimized for high speed operation
- 175 °C maximum operating junction temperature
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- Designed and gualified for industrial level
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, the VS-U5FX240FA120 is the right choice for high frequency converters, both soft switched / resonant. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters, and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V _R		1200	V		
Continuous forward current per diode	I _F	T _C = 59 °C	120	^		
Single pulse forward current per diode	I _{FSM}	$T_J = 25 \ ^{\circ}C$	595	A		
Maximum power dissipation per module	PD	T _C = 59 °C	611	W		
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V		
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 200 μA	1200	-	-	
	V _{FM}	I _F = 120 A	-	2.8	3.52	V
Forward voltage		I _F = 120 A, T _J = 150 °C	-	2.2	-	
		V _R = 1200 V	-	0.9	160	
Reverse leakage current	I _{RM}	T _J = 125 °C, V _R = 1200 V	-	125	-	μA
		T _J = 150 °C, V _R = 1200 V	-	375	-	

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Powerse receivery time	+	T _J = 25 °C		-	60	-	
Reverse recovery time	t _{rr}	T _J = 125 °C		-	134	-	ns
Poole recovery ourrent	I _{RRM} -	T _J = 25 °C	I _F = 120 A, di _F /dt = 1000 A/μs, V _R = 800 V	-	26	-	А
Peak recovery current		T _J = 125 °C		-	56	-	~
Powerse receivery charge	Reverse recovery charge Q _{rr}	T _J = 25 °C		-	1.6	-	μC
neverse recovery charge		T _J = 125 °C		-	6.2	-	μΟ
Junction capacitance	CT	V _R = 1200 V		-	42.4	-	pF

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance junction to case, per diode	P		-	-	0.38	
Thermal resistance junction to case, per module	R _{thJC}		-	-	0.19	°C/W
Thermal resistance case to heatsink, per module	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torgue		Torque per diode	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SOT	Г-227	

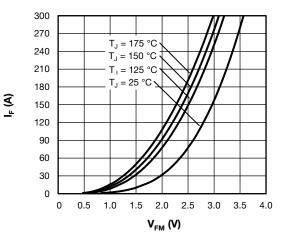


Fig. 1 - Typical Forward Voltage Drop Characteristics

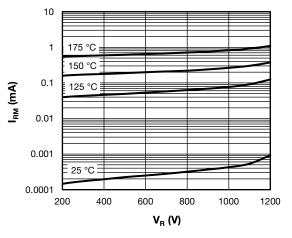


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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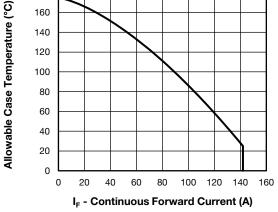


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Diode)

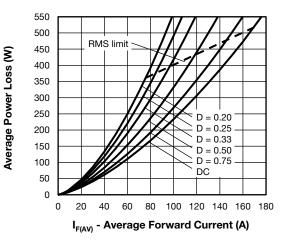


Fig. 4 - Average Power Loss vs Average Forward Current

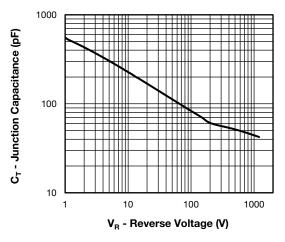


Fig. 5 - Junction Capacitance vs. Reverse Voltage

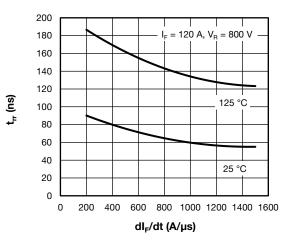


Fig. 6 - Diode Reverse Recovery Time vs. dI_F/dt

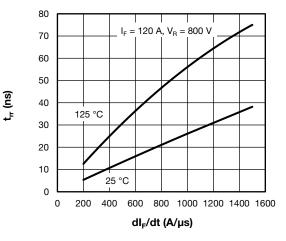


Fig. 7 - Diode Reverse Recovery Current vs. dl_F/dt

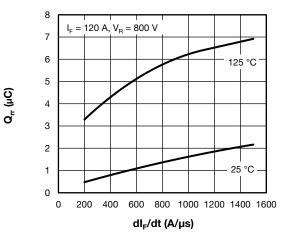


Fig. 8 - Diode Reverse Recovery Charge vs. dl_F/dt

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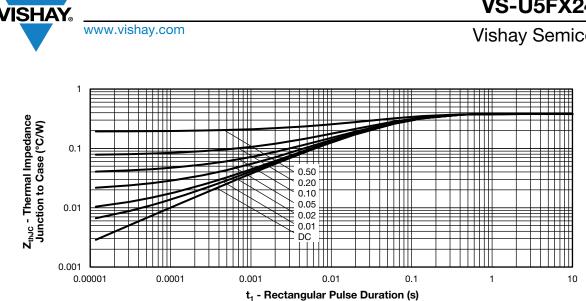


Fig. 9 - Maximum Thermal Impedance Junction to Case

Device code	vs-	U5F	х	240	F	Α	120	
	1	2	3	4	5	6	7	
	 Vishay Semiconductors product U5F = Gen 5 FRED Pt[®] family X = Hyperfast FRED Pt[®] diode 							
	4 · 5 · 6 ·	F = Pac	Current rating per module (240 = 240 A) F = circuit configuration (two separate diodes, parallel pin-out) Package indicator (SOT-227 standard insulated base) Voltage rating (120 = 1200 V)					
	<u> </u>	· voi	laye rali	ng (120	= 1200	V)		

CIRCUIT CONFIGURATION						
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Two separate diodes, parallel pin-out	F	Lead Assignment 4 1 1 1 1 1 1 1 1 1 1 1 1 1				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

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ORDERING INFORMATION TABLE

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SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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