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VS-10ETS08FP-M3, VS-10ETS12FP-M3

Vishay Semiconductors

High Voltage, Input Rectifier Diode, 10 A



2L TO-220 FullPAK

PRIMARY CHARACTERISTICS				
I _{F(AV)}	10 A			
V _R	800 V to 1200 V			
V _F at I _F	1.1 V			
I _{FSM}	160 A			
T _J max.	150 °C			
Package	2L TO-220 FullPAK			
Circuit configuration	Single			

FEATURES

- Very low forward voltage drop
- 150 °C max. operating junction temperature
- Glass passivated pellet chip junction
- Designed and qualified according to JEDEC[®]-JESD 47



COMPLIANT HALOGEN

- Fully isolated package (V_{INS} = 2500 V_{RMS})
- UL pending
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS	
Capacitive input filter $T_A = 55 \text{ °C}$, $T_J = 125 \text{ °C}$ common heatsink of 1 °C/W	12.0	16.0	A	

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Sinusoidal waveform	10	А		
V _{RRM}	Range	800, 1200	V		
I _{FSM}		160	A		
V _F	10 A, T _J = 25 °C	1.1	V		
TJ		-40 to +150	C°		

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA			
VS-10ETS08FP-M3	800	900	0.5			
VS-10ETS12FP-M3	1200	1300	0.5			

 Revision: 15-Sep-17
 1
 Document Number: 96295

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ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	I _{F(AV)}	T_{C} = 105 °C, 180° conduction half sine wave	10	
Maximum peak one cycle non-repetitive surge current		10 ms sine pulse, rated V_{RRM} applied	135	A
	IFSM	10 ms sine pulse, no voltage reapplied	160	
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	91	A ² s
	1-1	10 ms sine pulse, no voltage reapplied	130	A-5
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	1300	A²√s

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM}	10 A, T _J = 25 °C		1.1	V
Forward slope resistance	rt	T,₁ = 150 °C		20	mΩ
Threshold voltage	V _{F(TO)}	1j = 150 C 0.82 V		V	
Maximum reverse leakage current		T _J = 25 °C	V - Reted V	0.05	mA
	IRM	T _J = 150 °C	$V_{R} = Rated V_{RRM}$	0.50	ША

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and stor temperature range	age	T _J , T _{Stg}		-40 to +150	°C
Maximum thermal resistance	ce,	R _{thJC}	DC operation	2.5	
Maximum thermal resistance	ce,	R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	0.5	
Approximate weight				2	g
Approximate weight				0.07	oz.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf · in)
Marking device				10ETS08FP	
			Case style 2L TO-220 FullPAK	10ETS12FP	

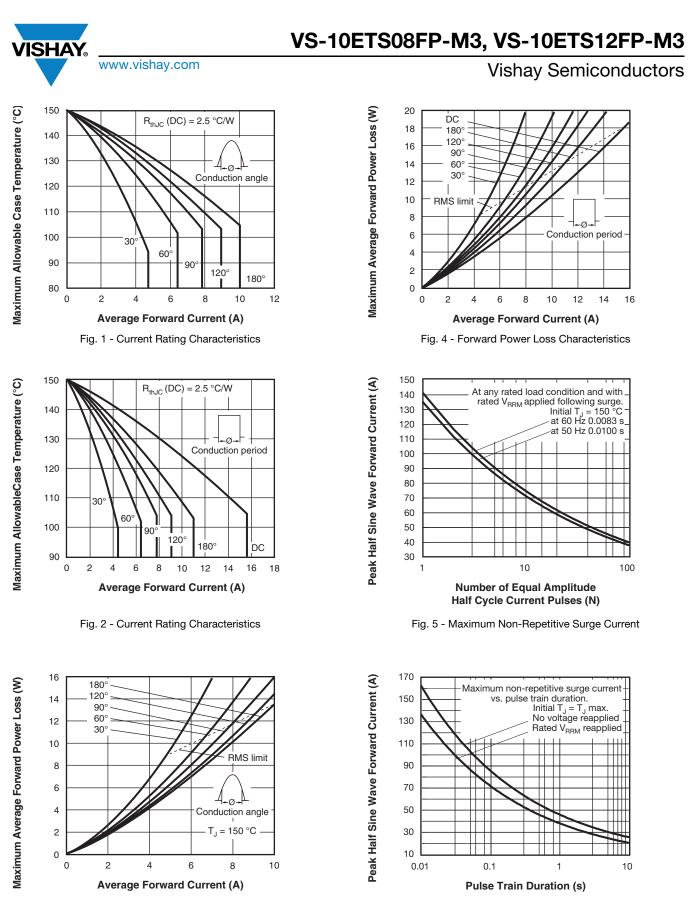


Fig. 3 - Forward Power Loss Characteristics

Fig. 6 - Maximum Non-Repetitive Surge Current

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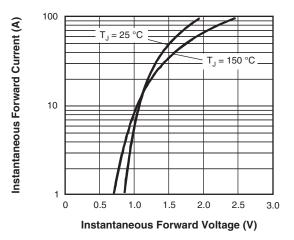


Fig. 7 - Forward Voltage Drop Characteristics

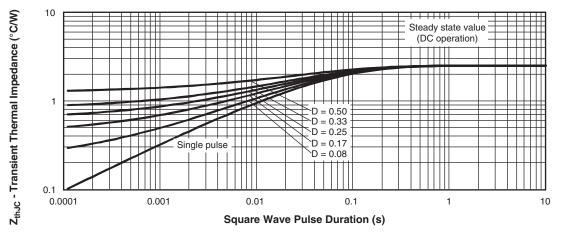


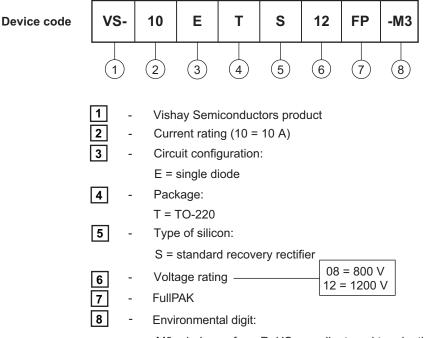
Fig. 8 - Thermal Impedance ZthJC Characteristics



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ORDERING INFORMATION TABLE



-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCR			
VS-10ETS08FP-M3	50	1000	Antistatic plastic tubes	
VS-10ETS12FP-M3	50	1000	Antistatic plastic tubes	

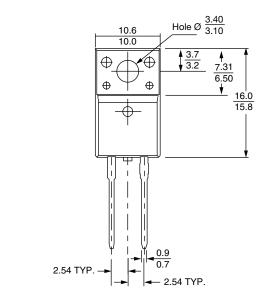
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?96157</u>				
Part marking information	www.vishay.com/doc?95392			

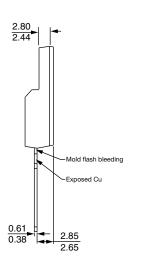


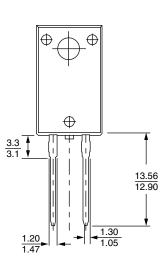
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2L TO-220 FullPAK

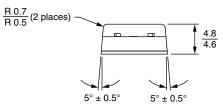
DIMENSIONS in millimeters







Bottom view





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