

# Schottky Rectifier, 1 A

Anode

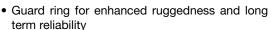


DO-214AC (SMA)

PRODUCT SUMMARY			
Package	DO-214AC (SMA)		
I <sub>F(AV)</sub>	1 A		
$V_{R}$	100 V		
V <sub>F</sub> at I <sub>F</sub>	0.63 V		
I <sub>RM</sub>	1 mA at 125 °C		
T <sub>J</sub> max.	150 °C		
Diode variation	Single die		
E <sub>AS</sub>	1.0 mJ		

#### **FEATURES**

Low forward voltage drop





**FREE** 

- Halogen-free according to IEC 61249-2-21 definition
- · Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS Directive 2002/95/EC

#### **DESCRIPTION**

The VS-10MQ100-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I <sub>F(AV)</sub>	DC	1	А	
$V_{RRM}$		100	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	120	А	
V <sub>F</sub>	1.5 Apk, T <sub>J</sub> = 125 °C	0.68	V	
T <sub>J</sub>	Range	- 55 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-10MQ100-M3	UNITS	
Maximum DC reverse voltage	$V_{R}$	100	V	
Maximum working peak reverse voltage	$V_{RWM}$	100	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDI	TEST CONDITIONS		UNITS
Maximum average forward current		50 % duty cycle at $T_L$ = 126 °C On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad are		1.5	
See fig. 4	I <sub>F(AV)</sub>	50 % duty cycle at $T_L$ = 135 °C On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad are		1	А
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	120	
non-repetitive surge current, T <sub>J</sub> = 25 °C See fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	30	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.5 A, L = 8 mH		1.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		Α	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		1 A	T <sub>.1</sub> = 25 °C	0.78	V
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	1.5 A	1J=25 C	0.85	
See fig. 1	V FM (1)	1 A	T <sub>J</sub> = 125 °C	0.63	
		1.5 A		0.68	
Maximum reverse leakage current		T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.1	A
See fig. 2	I <sub>RM</sub>	T <sub>J</sub> = 125 °C		1	mA
Threshold voltage	V <sub>F(TO)</sub>	$T_{J} = T_{J} \text{ maximum} $ $0.52$ $78.4$		0.52	V
Forward slope resistance	r <sub>t</sub>			mΩ	
Typical junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 10 V <sub>DC</sub> , T <sub>J</sub> = 25 °C, test signal = 1 MHz 38		pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0 nH		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µs		V/µs	

#### Note

 $<sup>^{(1)}</sup>$  Pulse width = 300  $\mu$ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 55 to 150	°C
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (similar D-64)	1.	J

#### Note

$$^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$

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# Vishay Semiconductors

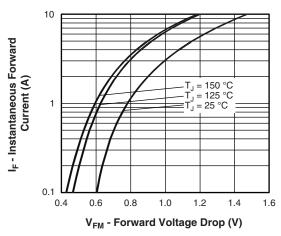


Fig. 1 - Maximum Forward Voltage Drop Characteristics

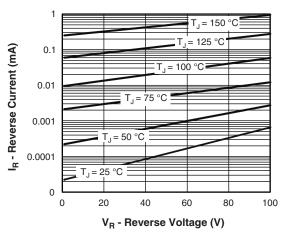


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

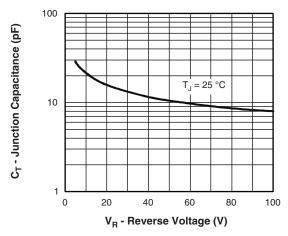
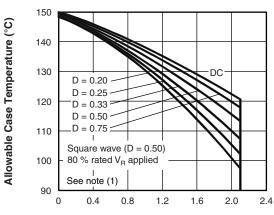
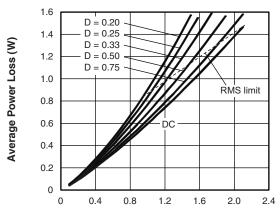


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



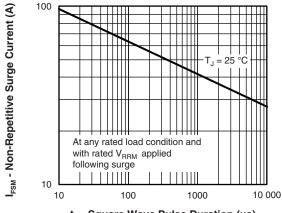
I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature



I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 6 - Maximum Peak Surge Forward Current vs.
Pulse Duration

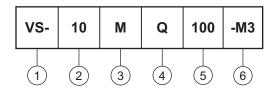
#### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1} = 80$  % rated  $V_R$ 



#### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

2 - Current rating

3 - M = SMA

4 - Q = Schottky "Q" series

5 - Voltage rating (100 = 100 V)

6 - Environmental digit:

-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION				
VS-10MQ100-M3/5AT	5AT	7500	13" diameter plastic tape and reel	

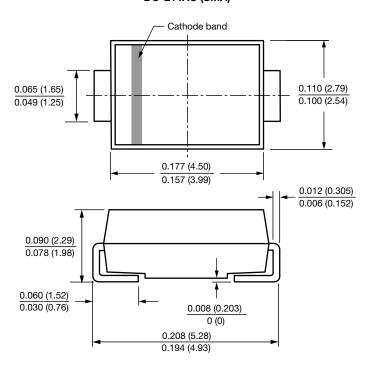
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95400			
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			



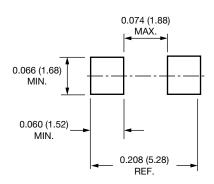
### **SMA**

### **DIMENSIONS** in inches (millimeters)

### DO-214AC (SMA)



#### **Mounting Pad Layout**



### **Legal Disclaimer Notice**



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