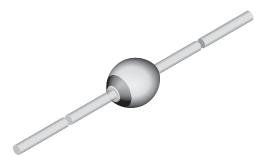
Vishay Semiconductors

Standard Avalanche Sinterglass Diode

949539

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FEATURES

- Glass passivated junction
- Hermetically sealed package
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- High voltage rectification
- · Efficiency diode in horizontal deflection circuits



3D Models Available

MECHANICAL DATA

DESIGN SUPPORT TOOLS

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750, method 2026

Polarity: color band denotes cathode end

Mounting position: any

Weight: approx. 369 mg

ORDERING INFORMATION (Example)					
DEVICE NAME	ORDERING CODE	TAPED UNITS	MINIMUM ORDER QUANTITY		
BY458	BY458TR	5000 per 10" tape and reel	25 000		
BY458	BY458TAP	5000 per ammopack	25 000		

PARTS TABLE		
PART	TYPE DIFFERENTIATION	PACKAGE
BY448	V _R = 1500 V, I _{FAV} = 2 A	SOD-57
BY458	V _R = 1200 V, I _{FAV} = 2 A	SOD-57

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
Poverse veltage	See electrical characteristics	BY448	$V_{R} = V_{RRM}$	1500	V	
Reverse voltage		BY458	$V_{R} = V_{RRM}$	1200	V	
Peak forward surge current	t _p = 10 ms, half sine wave		I _{FSM}	30	А	
Average forward current			I _{FAV}	2	А	
Junction temperature			Tj	140	°C	
Storage temperature range			T _{stg}	-55 to +175	°C	
Non repetitive reverse avalanche energy	I _{(BR)R} = 0.4 A		E _R	10	mJ	

MAXIMUM THERMAL RESISTANCE (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Junction ambient	I = 10 mm, T _L = constant	R _{thJA}	45	K/W		
Sulction ambient	On PC board with spacing 25 mm	R _{thJA}	100	K/W		

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 1
 Document Number: 86006

 For technical questions within your region:
 DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

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BY448, BY458



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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX	UNIT
Forward voltage	I _F = 3 A	V _F	-	-	1.6	V
Reverse current	$V_{R} = V_{RRM}$	I _R	-	-	3	μA
neverse current	$V_R = V_{RRM}, T_j = 140 \ ^\circ C$	I _R	-	-	140	μA
Reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, i_R = 0.25 \text{ A}$	t _{rr}	-	-	2	μs
Total reverse recovery time	I _F = 1 A, - dI _F /dt = 0.05 A/μs	t _{rr}	-	-	20	μs

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

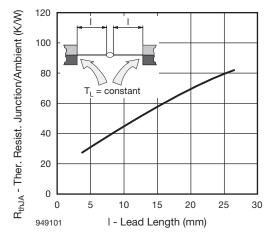


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

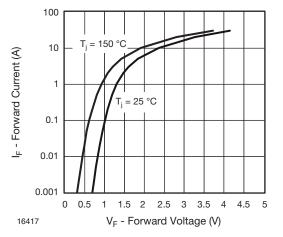


Fig. 2 - Forward Current vs. Forward Voltage

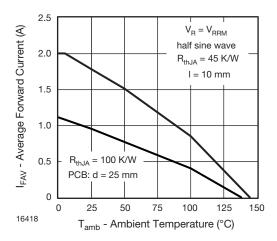


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

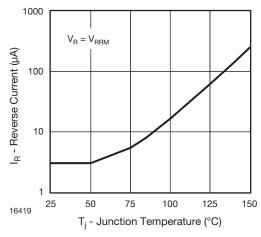


Fig. 4 - Reverse Current vs. Junction Temperature



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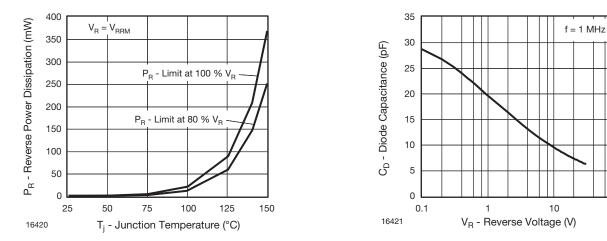


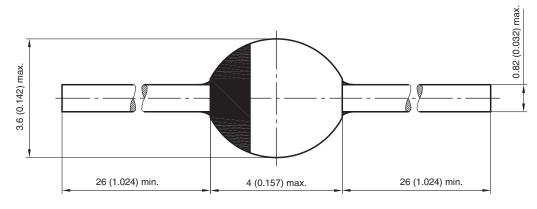
Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

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SHAY



PACKAGE DIMENSIONS in millimeters (inches): SOD-57



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