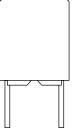


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MKT1817

Metallized Polyester Film Capacitors MKT Radial Potted Types



FEATURES

• Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS



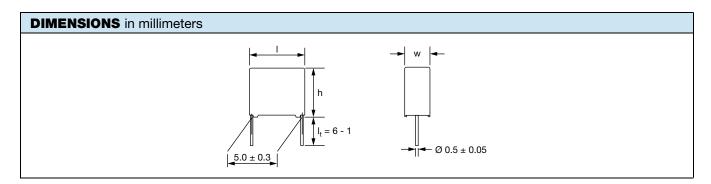
RoHS COMPLIANT HALOGEN FREE GREEN (5-2008)

Blocking, bypassing, filtering and timing, high frequency coupling and decoupling for fast digital and analog ICs, interference suppression in low voltage applications.

Capacitance range	1 nF to 1.0 µF (E12 series)
Capacitance tolerance	± 20 % (M), ± 10 % (K), ± 5 % (J)
Climatic testing class according to IEC 60068-1	55/100/56 for rated voltage 63 V 55/105/56 for rated voltage > 63 V
Reference specifications	IEC 60384-2
Dielectric	Polyester film
Electrodes	Metallized
	Mono construction
Construction	
Encapsulation	Flame retardant plastic case and epoxy resin sealed (UL-class 94 V-0)
Leads	Tinned wire
Marking	Manufacturer's logo/type/C-value/rated voltage/tolerance/date of manufacture
Rated DC voltage	63 V _{DC} , 100 V _{DC} , 250 V _{DC} , 400 V _{DC}
Rated AC voltage	40 V _{AC} , 63 V _{AC} , 160 V _{AC} , 200 V _{AC}
Rated temperature	85 °C
Maximum application temperature	100 °C for rated voltage 63 V 105 °C for rated voltage > 63 V
Performance grade	1 (long life)

Note

For more detailed data and test requirements, contact <u>dc-film@vishay.com</u>

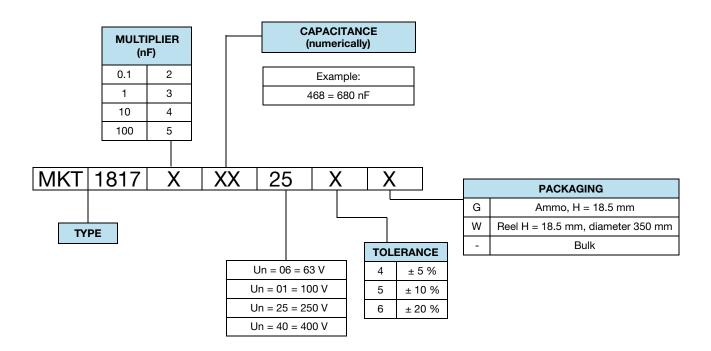


1 For technical questions, contact: dc-film@vishay.com Document Number: 26032



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COMPOSITION OF CATALOG NUMBER



SPECIFIC REFERENCE DATA					
DESCRIPTION		VALUE			
Tangent of loss angle:		at 1 kHz at 10 kHz at 100 kHz			
C ≤ 0.1 µF		≤ 80 x 10 ⁻⁴	≤ 150 x 10 ⁻⁴	≤ 250 x 10 ⁻⁴	
$0.1~\mu F < C \leq 1.0~\mu F$		≤ 80 x 10 ⁻⁴	≤ 150 x 10 ⁻⁴	-	
PITCH		RATED VOLTAGE PUL	SE SLOPE (dU/dt) _R AT		
(mm)	63 V _{DC}	100 V _{DC}	250 V _{DC}	400 V _{DC}	
5	60	110	330	630	
If the maximum pulse volta	age is less than the rated vol	tage higher dV/dt values can	be permitted.		
R between leads, for $C \le C$	0.33 μF and $U_R \leq 100~V$		$>$ 15 000 M Ω		
R between leads, for $C \le C$).33 μF and U _R > 100 V		> 30 000 M Ω		
RC between leads, for C >	\triangleright 0.33 μF and $U_R \leq$ 100 V		> 5000 s		
RC between leads, for C >	\sim 0.33 µF and U _R $>$ 100 V		> 10 000 s		
R between interconnecting (foil method)	g leads and casing 100 V	> 30 000 MΩ			
Withstanding (DC) voltage rise time \leq 1000 V/s	(cut off current 10 mA) ⁽¹⁾ ;	1.6 x U _{RDC} , 1 min			
Withstanding (DC) voltage	between leads and case	2.0 x U _{RDC} , with minimum of 200 V _{DC} ; 1 min			
Maximum application temperature		100 °C for rated voltage 63 V 105 °C for rated voltage > 63 V			

Note

⁽¹⁾ See "Voltage Proof Test for Metalized Film Capacitors": <u>www.vishay.com/doc?28169</u>



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U _{RDC} (V)	CAP. (µF)	CAPACITANCE CODE	VOLTAGE CODE	V _{AC}	DIMENSION w x h x l (mm)
	0.10	-410			2.5 x 6.5 x 7
	0.15	-415			2.5 x 6.5 x 7
	0.22	-422			3.5 x 8.0 x 7
63	0.33	-433	06	40	3.5 x 8.0 x 7
	0.47	-447			3.5 x 8.0 x 7
	0.68	-468			4.5 x 9.0 x 7
	1.0	-510			6.0 x 11.0 x 7
	0.022	-322			2.5 x 6.5 x 7
	0.033	-333			2.5 x 6.5 x 7
	0.047	-347			2.5 x 6.5 x 7
100	0.068	-368	01	<u></u>	2.5 x 6.5 x 7
100	0.10	-410	- 01	63	2.5 x 6.5 x 7
	0.15	-415]		3.5 x 8.0 x 7
	0.22	-422]		4.5 x 9.0 x 7
	0.33	-433			4.5 x 9.0 x 7
	0.0033	-233			2.5 x 6.5 x 7
	0.0047	-247			2.5 x 6.5 x 7
	0.0068	-268			2.5 x 6.5 x 7
	0.010	-310		160	2.5 x 6.5 x 7
250	0.015	-315	25		2.5 x 6.5 x 7
250	0.022	-322	25		3.5 x 8.0 x 7
	0.033	-333			3.5 x 8.0 x 7
	0.047	-347			4.5 x 9.0 x 7
	0.068	-368			6.0 x 11.0 x 7
	0.10	-410			6.0 x 11.0 x 7
	0.0033	-233			2.5 x 6.5 x 7
	0.0047	-247			2.5 x 6.5 x 7
	0.0068	-268			2.5 x 6.5 x 7
	0.010	-310			2.5 x 6.5 x 7
250	0.015	-315	25	160	2.5 x 6.5 x 7
200	0.022	-322	100	3.5 x 8.0 x 7	
	0.033	-333			3.5 x 8.0 x 7
	0.047	-347			4.5 x 9.0 x 7
	0.068	-368			6.0 x 11.0 x 7
	0.10	-410			6.0 x 11.0 x 7
	0.0010	-210			2.5 x 6.5 x 7
	0.0015	-215			2.5 x 6.5 x 7
	0.0022	-222			2.5 x 6.5 x 7
	0.0033	-233			2.5 x 6.5 x 7
400	0.0047	-247	40	200	2.5 x 6.5 x 7
	0.0068	-268			2.5 x 6.5 x 7
	0.010	-310			3.5 x 8.0 x 7
	0.015	-315			3.5 x 8.0 x 7
	0.022	-322			4.5 x 9.0 x 7

RECOMMENDED PACKAGING					
PACKAGING CODE	TYPE OF PACKAGING	HEIGHT (H) (mm)	REEL DIAMETER (mm)	ORDERING CODE EXAMPLES	PITCH 5
G	Ammo	18.5	S ⁽¹⁾	MKT1817233255G	х
W	Reel	18.5	350	MKT1817233255W	х
-	Bulk	-	-	MKT1817233255	Х

Note

(1) S = box size 55 mm x 210 mm x 340 mm (w x h x l)

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MOUNTING

Normal Use

The capacitors are designed for mounting on printed-circuit boards. The capacitors packed in bandoliers are designed for mounting in printed-circuit boards by means of automatic insertion machines.

For detailed tape specifications refer to packaging information www.vishay.com/docs?28139

Specific Method of Mounting to Withstand Vibration and Shock

In order to withstand vibration and shock tests, it must be ensured that the stand-off pips are in good contact with the printed-circuit board.

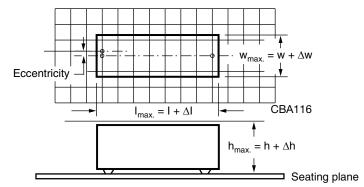
- For pitches \leq 15 mm the capacitors shall be mechanically fixed by the leads
- For larger pitches the capacitors shall be mounted in the same way and the body clamped

Space Requirements on Printed-Circuit Board

The maximum space for length ($I_{max.}$), width ($w_{max.}$) and height ($h_{max.}$) of film capacitors to take in account on the printed-circuit board is shown in the drawings.

• For products with pitch \leq 15 mm, $\Delta w = \Delta I = 0.3$ mm; $\Delta h = 0.1$ mm

Eccentricity defined as in drawing. The maximum eccentricity is smaller than or equal to the lead diameter of the product concerned.



SOLDERING CONDITIONS

For general soldering conditions and wave soldering profile, we refer to the document "Characteristics and Definitions Used for Film Capacitors": <u>www.vishay.com/doc?28147</u>

Storage Temperature

 T_{stg} = -25 °C to +35 °C with RH maximum 75 % without condensation

Ratings and Characteristics Reference Conditions

Unless otherwise specified, all electrical values apply to an ambient free air temperature of 23 °C \pm 1 °C, an atmospheric pressure of 86 kPa to 106 kPa and a relative humidity of 50 % \pm 2 %.

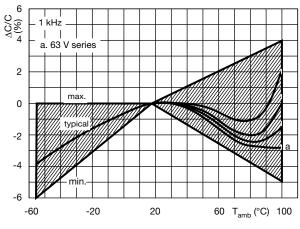
For reference testing, a conditioning period shall be applied over 96 h \pm 4 h by heating the products in a circulating air oven at the rated temperature and a relative humidity not exceeding 20 %.



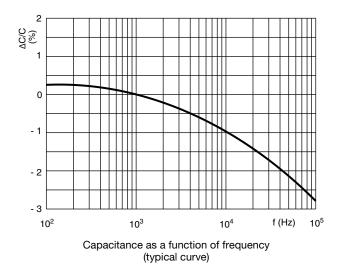
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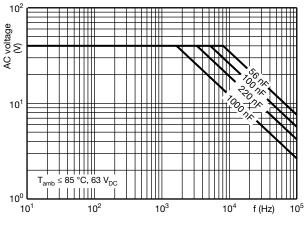
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CHARACTERISTICS

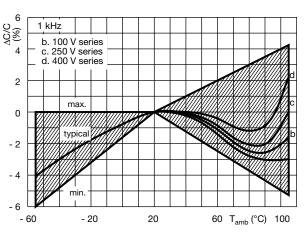


Capacitance as a function of ambient temperature (typical) for voltage 63 V

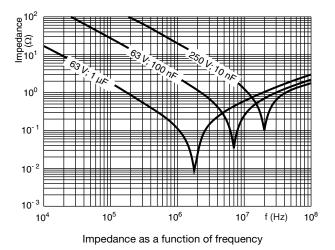


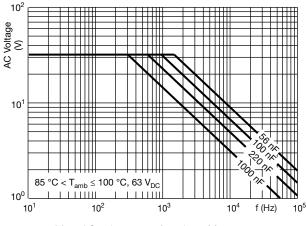


Max. AC voltage as a function of frequency



Capacitance as a function of ambient temperature (typical) for voltages > 63 V





Max. AC voltage as a function of frequency

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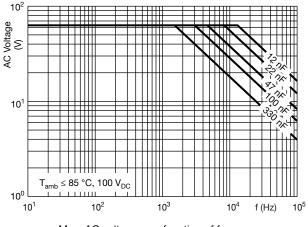
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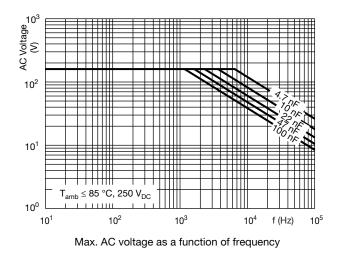


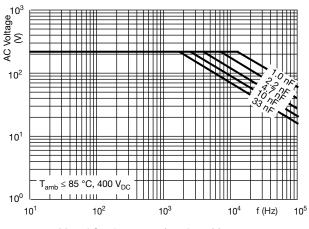
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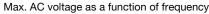
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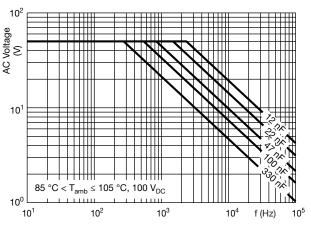


Max. AC voltage as a function of frequency

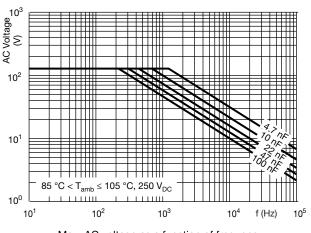


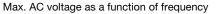


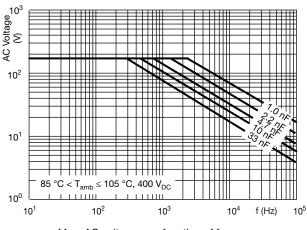




Max. AC voltage as a function of frequency







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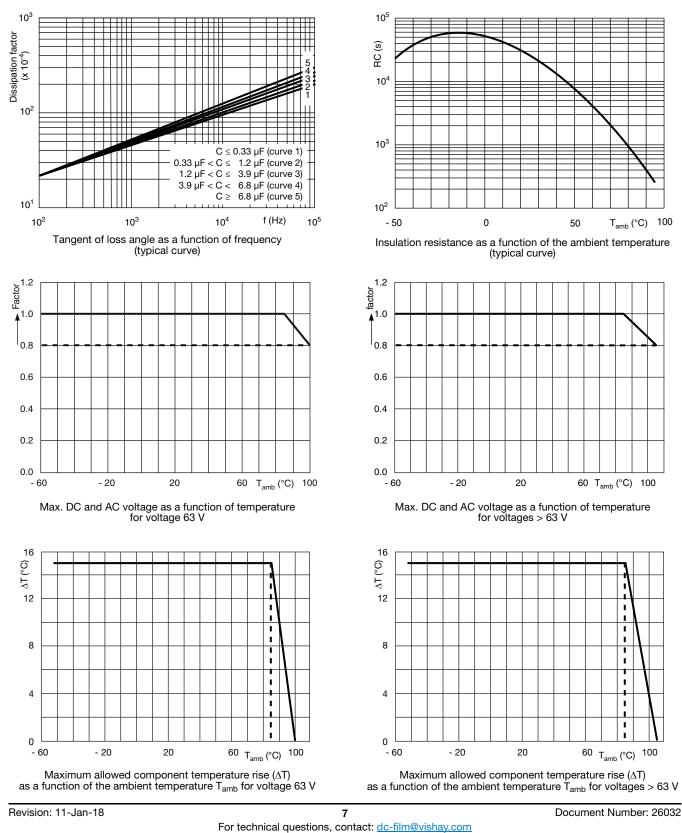


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Maximum RMS Current (Sinewave) as a Function of Frequency

The maximum RMS current is defined by $I_{ac} = \omega \times C \times U_{ac}$.

U_{AC} is the maximum AC voltage depending on the ambient temperature in the curves "Max. RMS voltage and AC current as a function of frequency".



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HEAT CONDUCTIVITY (G) AS A FUNCTION OF (ORIGINAL) PITCH AND CAPACITOR BODY THICKNESS IN $mW/^{\circ}C$		
W _{max.}	HEAT CONDUCTIVITY (mW/°C)	
(mm)	PITCH 5 mm	
2.5	2.5	
3.0	3.0	
4.5	4.0	
6.0	5.5	

POWER DISSIPATION AND MAXIMUM COMPONENT TEMPERATURE RISE

The power dissipation must be limited in order not to exceed the maximum allowed component temperature rise as a function of the free ambient temperature.

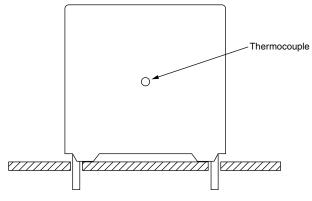
The power dissipation can be calculated according type detail specification "HQN-384-01/101: Technical Information Film Capacitors".

The component temperature rise (Δ T) can be measured (see section "Measuring the Component Temperature" for more details) or calculated by Δ T = P/G:

- ΔT = component temperature rise (°C)
- P = power dissipation of the component (mW)
- G = heat conductivity of the component (mW/°C)

MEASURING THE COMPONENT TEMPERATURE

A thermocouple must be attached to the capacitor body as in:



The temperature is measured in unloaded (T_{amb}) and maximum loaded condition (T_C). The temperature rise is given by $\Delta T = T_C - T_{amb}$.

To avoid radiation or convection, the capacitor should be tested in a wind-free box.



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APPLICATION NOTE AND LIMITING CONDITIONS

These capacitors are not suitable for mains applications as across-the-line capacitors without additional protection, as described hereunder. These mains applications are strictly regulated in safety standards and therefore electromagnetic interference suppression capacitors conforming the standards must be used.

For capacitors connected in parallel, normally the proof voltage and possibly the rated voltage must be reduced. For information depending of the capacitance value and the number of parallel connections contact: dc-film@vishay.com

- To select the capacitor for a certain application, the following conditions must be checked:
- 1. The peak voltage (U_P) shall not be greater than the rated DC voltage (U_{RDC})
- 2. The peak-to-peak voltage (UP-P) shall not be greater than 2\2 x U_{BAC} to avoid the ionization inception level
- 3. The voltage peak slope (dU/dt) shall not exceed the rated voltage pulse slope in an RC-circuit at rated voltage and without ringing. If the pulse voltage is lower than the rated DC voltage, the rated voltage pulse slope may be multiplied by U_{RDC} and divided by the applied voltage.

For all other pulses following equation must be fulfilled:

$$2 x \int_{0}^{1} \left(\frac{dU}{dt}\right)^{2} x dt < U_{RDC} x \left(\frac{dU}{dt}\right)_{rated}$$

T is the pulse duration.

- 4. The maximum component surface temperature rise must be lower than the limits (see graph "Max. allowed component temperature rise").
- 5. Since in circuits used at voltages over 280 V peak-to-peak the risk for an intrinsically active flammability after a capacitor breakdown (short circuit) increases, it is recommended that the power to the component is limited to 100 times the values mentioned in the table: "Heat Conductivity"
- 6. When using these capacitors as across-the-line capacitor in the input filter for mains applications or as series connected with an impedance to the mains the applicant must guarantee that the following conditions are fulfilled in any case (spikes and surge voltages from the mains included).

VOLTAGE CONDITIONS FOR 6 ABOVE				
ALLOWED VOLTAGES	T _{amb} ≤ 85 °C	85 °C < T _{amb} \leq 100 °C FOR 63 V		
ALLOWED VOLTAGES		85 °C < T _{amb} \leq 100 °C FOR > 63 V		
Maximum continuous RMS voltage	U _{RAC}	See "Max. AC voltage as function of temperature" per characteristics		
Maximum temperature RMS-overvoltage (< 24 h)	1.25 x U _{RAC}	U _{RAC}		
Maximum peak voltage (V _{O-P}) (< 2 s)	1.6 x U _{RDC}	1.3 x U _{RDC}		

Example

C = 330 nF - 63 V used for the voltage signal shown in next drawing. $U_{P-P} = 40 \text{ V}; U_P = 35 \text{ V}; T_1 = 100 \text{ }\mu\text{s}; T_2 = 200 \text{ }\mu\text{s}$ The ambient temperature is 35 °C

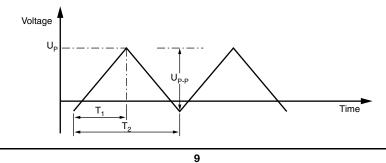
Checking conditions:

- 1. The peak voltage $U_P = 35$ V is lower than 63 V_{DC}
- 2. The peak-to-peak voltage 40 V is lower than $2\sqrt{2} \times 40 V_{AC} = 113 U_{P-P}$
- 3. The voltage pulse slope (dU/dt) = 40 V/100 μ s = 0.4 V/ μ s
- This is lower than 60 V/µs (see specific reference data for each version) 4. The dissipated power is 16.2 mW as calculated with fourier terms
 - The temperature rise for w_{max} = 3.5 mm and pitch = 5 mm will be 16.2 mW/3.0 mW/°C = 5.4 °C

This is lower than 15 °C temperature rise at 35 °C, according figure "Max. allowed component temperature rise"

- 5. Not applicable
- 6. Not applicable

Voltage Signal



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INSPECTION REQUIREMENTS

General Notes

Sub-clause numbers of tests and performance requirements refer to the "Sectional Specification, Publication IEC 60384-2 and Specific Reference Data".

GROUP C INSPECTION REQUI		T
SUB-CLAUSE NUMBER AND TEST	CONDITIONS	PERFORMANCE REQUIREMENTS
SUB-GROUP C1A PART OF SAMPLE OF SUB-GROUP C1		
4.1 Dimensions (detail)		As specified in chapters "MKT370 General Data" of this specification
4.3.1 Initial measurements	Capacitance Tangent of loss angle: for C \leq 470 nF at 100 kHz for C > 470 nF at 10 kHz	
4.3 Robustness of terminations	Tensile and bending	No visible damage
4.4 Resistance to soldering heat	Method: 1A Solder bath: 280 °C ± 5 °C Duration: 10 s	
4.14 Component solvent resistance	Isopropylalcohol at room temperature Method: 2 Immersion time: 5 min ± 0.5 min Recovery time: min. 1 h, max. 2 h	
4.4.2 Final measurements	Visual examination	No visible damage Legible marking
	Capacitance	$\left \Delta C/C \right \leq 2$ % of the value measured initially
	Tangent of loss angle	Increase of tan δ : ≤ 0.005 for: C ≤ 100 nF or ≤ 0.010 for: 100 nF < C ≤ 220 nF or ≤ 0.015 for: 220 nF < C ≤ 470 nF or ≤ 0.003 for: C > 470 nF Compared to values measured in 4.3.1
SUB-GROUP C1B OTHER PART OF SAMPLE OF SUB-GROUP C1		
4.6.1 Initial measurements	Capacitance Tangent of loss angle: for C \leq 470 nF at 100 kHz for C > 470 nF at 10 kHz	
4.6 Rapid change of temperature	$\theta A = -55 \ ^{\circ}C$ $\theta B = +100 \ ^{\circ}C$ 5 cycles Duration t = 30 min	
4.7 Vibration	Visual examination Mounting: see section "Mounting" of this specification Procedure B4 Frequency range: 10 Hz to 55 Hz Amplitude: 0.75 mm or Acceleration 98 m/s ² (whichever is less severe) Total duration 6 h	No visible damage



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GROUP C INSPECTION REQUIREMENTS					
SUB-CI	LAUSE NUMBER AND TEST	CONDITIONS	PERFORMANCE REQUIREMENTS		
	ROUP C1B OTHER PART OF E OF SUB-GROUP C1				
4.7.2	Final inspection	Visual examination	No visible damage		
4.9	Shock	Mounting: see section "Mounting" of this specification Pulse shape: half sine Acceleration: 490 m/s ² Duration of pulse: 11 ms			
4.9.3	Final measurements	Visual examination	No visible damage		
		Capacitance	$ \Delta C/C \leq 3$ % of the value measured in 4.6.1		
		Tangent of loss angle	Increase of tan δ : ≤ 0.010 for: C ≤ 220 nF or ≤ 0.015 for: 220 nF < C ≤ 470 nF or ≤ 0.003 for: C > 470 nF Compared to values measured in 4.6.1		
		Insulation resistance	As specified in section "Specific Reference Data 370" of this specification		
	ROUP C1 COMBINED SAMPLE CIMENS OF SUB-GROUPS ID C1B				
4.10	Climatic sequence				
4.10.2	Dry heat	Temperature: +100 °C for rated voltage 63 V +105 °C for rated voltage > 63 V Duration: 16 h			
4.10.3	Damp heat cyclic Test Db, first cycle				
4.10.4	Cold	Temperature: -55 °C Duration: 2 h			
4.10.6	Damp heat cyclic Test Db, remaining cycles	Voltage proof = U_{RDC} for 1 min within 15 min after removal from testchamber	No breakdown or flash-over		
4.10.6.2	P Final measurements	Visual examination	No visible damage Legible marking		
		Capacitance	$ \Delta C/C \le 3$ % of the value measured in 4.4.2 or 4.9.3		
		Tangent of loss angle	Increase of tan δ : ≤ 0.010 for: C ≤ 220 nF or ≤ 0.015 for: 220 nF < C ≤ 470 nF or ≤ 0.005 for: C > 470 nF Compared to values measured in 4.3.1 or 4.6.1		
		Insulation resistance	\geq 50 % of values specified in section "Specific Reference Data 370" of this specification		

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GROUP C INSPECTION REQUIREMENTS					
SUB-CLAUSE NUMBER AND TEST	CONDITIONS	PERFORMANCE REQUIREMENTS			
SUB-GROUP C2					
4.11 Damp heat steady state	56 days, 40 °C, 90 % to 95 % RH				
4.11.1 Initial measurements	Capacitance Tangent of loss angle at 1 kHz				
4.11.3 Final measurements	Voltage proof = U_{RDC} for 1 min within 15 min after removal from testchamber	No breakdown or flash-over			
	Visual examination	No visible damage Legible marking			
	Capacitance	$ \Delta C/C \le 5$ % of the value measured in 4.11.1.			
	Tangent of loss angle	Increase of tan δ : \leq 0.005 Compared to values measured in 4.11.1			
	Insulation resistance	\geq 50 % of values specified in section "Specific Reference Data 370" of this specification			
SUB GROUP C3					
4.12 Endurance	Duration: 2000 h 1.25 x U_{RDC} at 85 °C 0.8 x 1.25 U_{RDC} at 100 °C for rated voltage 63 V 0.8 x 1.25 U_{RDC} at 105 °C for rated voltage > 63 V				
4.12.1 Initial measurements	Capacitance Tangent of loss angle: for C \leq 470 nF at 100 kHz for C > 470 nF at 10 kHz				
4.12.5 Final measurements	Visual examination	No visible damage Legible marking			
	Capacitance	$ \Delta C/C \leq 5$ % compared to values measured in 4.12.1			
	Tangent of loss angle	Increase of tan δ : ≤ 0.005 at 85 °C ≤ 0.010 at 100 °C for: C ≤ 220 nF or ≤ 0.015 for: 220 nF < C ≤ 470 nF or ≤ 0.003 for: C > 470 nF Compared to values measured in 4.12.1			
	Insulation resistance	≥ 50 % of values specified in section "Specific Reference Data 370" of this specification			



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GROUP C INSPECTION REQUIREMENTS				
SUB-CLAUSE NUMBER AND TEST	CONDITIONS	PERFORMANCE REQUIREMENTS		
SUB-GROUP C4				
4.13 Charge and discharge	10 000 cycles Charged to U _{RDC} Discharge resistance:			
	$R = \frac{U_R}{C \times 2.5 \times (dU/dt)_R}$			
4.13.1 Initial measurements	Capacitance Tangent of loss angle: for C \leq 470 nF at 100 kHz for C > 470 nF at 10 kHz			
4.13.3 Final measurements	Capacitance	$ \Delta C/C \leq 3$ % compared to values measured in 4.13.1		
	Tangent of loss angle	$\begin{array}{l} \mbox{Increase of tan δ:} \\ \leq 0.005 \mbox{ for: } C \leq 100 \mbox{ nF or } \\ \leq 0.010 \mbox{ for: } 100 \mbox{ nF < C } \leq 220 \mbox{ nF or } \\ \leq 0.015 \mbox{ for: } 220 \mbox{ nF < C } \leq 470 \mbox{ nF or } \\ \leq 0.003 \mbox{ for: } C > 470 \mbox{ nF } \\ \mbox{ Compared to values measured in 4.13.1} \end{array}$		
	Insulation resistance	≥ 50 % of values specified in section "Specific Reference Data 370" of this specification		



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