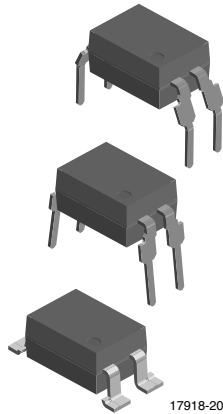


Optocoupler, Phototransistor Output, High Reliability, 5300 V_{RMS}, 110 °C Rated



17918-20



FEATURES

- Operating temperature from -55 °C to +110 °C
- Good CTR linearity depending on forward current
- Isolation test voltage, 5300 V_{RMS}
- High collector emitter voltage, V_{CEO} = 70 V
- Low saturation voltage
- Fast switching times
- Low CTR degradation
- Temperature stable
- Low coupling capacitance
- End stackable, 0.100" (2.54 mm) spacing
- High common mode interference immunity
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



DESCRIPTION

The 110 °C rated SFH617A (DIP) feature a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 package.

The coupling devices are designed for signal transmission between two electrically separated circuits.

The couplers are end-stackable with 2.54 mm spacing.

Creepage and clearance distances of > 8.0 mm are achieved with option 6.

APPLICATIONS

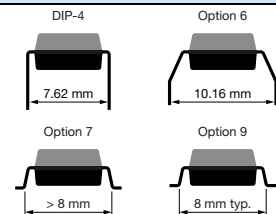
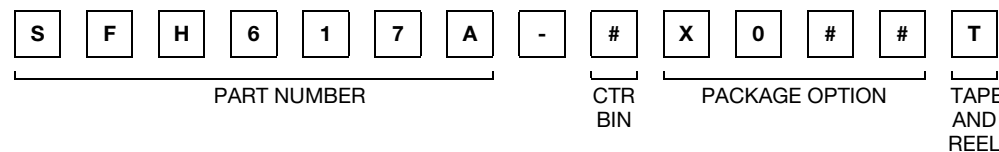
- AC adapter
- SMPS
- PLC
- Factory automation
- Game consoles

AGENCY APPROVALS

The safety application model number covering all products in this datasheet is SFH617A. This model number should be used when consulting safety agency documents.

- UL1577, file no. E52744
- cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-5 (VDE 0884-5) available with option 1
- BSI IEC 60950; IEC 60065
- FIMKO
- CQC

ORDERING INFORMATION



AGENCY CERTIFIED/PACKAGE	CTR (%)			
	40 to 80	63 to 125	100 to 200	160 to 320
UL, BSI, FIMKO, cUL				
DIP-4	SFH617A-1	SFH617A-2	SFH617A-3	SFH617A-4
DIP-4, 400 mil, option 6	SFH617A-1X006	SFH617A-2X006	SFH617A-3X006	SFH617A-4X006
SMD-4, option 7	SFH617A-1X007T	-	SFH617A-3X007T	-
SMD-4, option 9	-	SFH617A-2X009T	-	-
VDE, UL, BSI, FIMKO, cUL				
DIP-4	SFH617A-1X001	SFH617A-2X001	SFH617A-3X001	SFH617A-4X001
DIP-4, 400 mil, option 6	SFH617A-1X016	SFH617A-2X016	SFH617A-3X016	SFH617A-4X016
SMD-4, option 7	-	SFH617A-2X017T	SFH617A-3X017T ⁽¹⁾	-
SMD-4, option 9	-	SFH617A-2X019T ⁽¹⁾	-	-

Notes

- Additional options may be possible, please contact sales office.
- ⁽¹⁾ Also available in tubes; do not add T to end.



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V _R	6	V
Forward current		I _F	60	mA
Forward surge current	t _p ≤ 10 μs	I _{FSM}	2.5	A
LED power dissipation	at 25 °C	P _{diss}	70	mW
OUTPUT				
Collector emitter voltage		V _{CEO}	70	V
Emitter collector voltage		V _{ECO}	7	V
Collector current		I _C	50	mA
Collector peak current	t _p /T = 0.5, t _p ≤ 10 ms	I _{CM}	100	mA
Output power dissipation	at 25 °C	P _{diss}	150	mW
COUPLER				
Operation temperature		T _{amb}	- 55 to + 110	°C
Storage temperature range		T _{stg}	- 55 to + 150	°C
Soldering temperature ⁽¹⁾	2 mm from case, ≤ 10 s	T _{slid}	260	°C

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I _F = 60 mA		V _F		1.35	1.65	V
Reverse current	V _R = 6 V		I _R		0.01	10	μA
Capacitance	V _R = 0 V, f = 1 MHz		C _O		13		pF
OUTPUT							
Collector emitter capacitance	V _{CE} = 5 V, f = 1 MHz		C _{CE}		5.2		pF
Collector emitter leakage current	V _{CE} = 10 V	SFH617A-1	I _{CEO}		2	50	nA
		SFH617A-2	I _{CEO}		2	50	nA
		SFH617A-3	I _{CEO}		5	100	nA
		SFH617A-4	I _{CEO}		5	100	nA
COUPLER							
Collector emitter saturation voltage	I _F = 10 mA, f = 1 MHz		V _{CEsat}		0.25	0.4	V
Coupling capacitance			C _C		0.4		pF

Note

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.



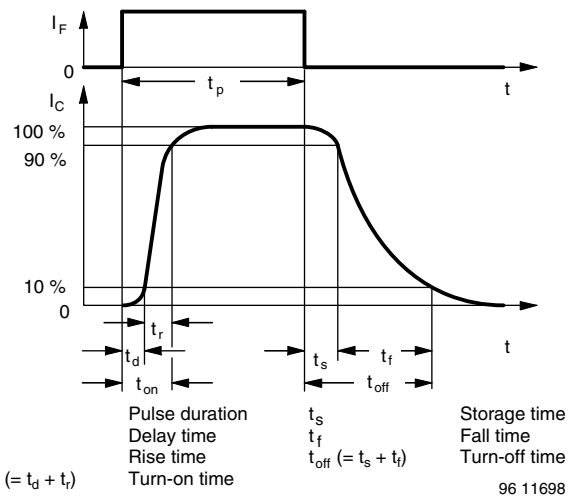
CURRENT TRANSFER RATIO ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I_C/I_F	$I_F = 10\text{ mA}$, $V_{CE} = 5\text{ V}$	SFH617A-1	CTR	40		80	%
		SFH617A-2	CTR	63		125	%
		SFH617A-3	CTR	100		200	%
		SFH617A-4	CTR	160		320	%
	$I_F = 1\text{ mA}$, $V_{CE} = 5\text{ V}$	SFH617A-1	CTR	13	30		%
		SFH617A-2	CTR	22	45		%
		SFH617A-3	CTR	34	70		%
		SFH617A-4	CTR	56	90		%

SWITCHING CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED							
Turn-on time	$I_F = 10\text{ mA}$, $V_{CC} = 5\text{ V}$, $R_L = 75\text{ }\Omega$		t_{on}		3		μs
Rise time	$I_F = 10\text{ mA}$, $V_{CC} = 5\text{ V}$, $R_L = 75\text{ }\Omega$		t_r		2		μs
Turn-off time	$I_F = 10\text{ mA}$, $V_{CC} = 5\text{ V}$, $R_L = 75\text{ }\Omega$		t_{off}		2.3		μs
Fall time	$I_F = 10\text{ mA}$, $V_{CC} = 5\text{ V}$, $R_L = 75\text{ }\Omega$		t_f		2		μs
Cut-off frequency	$I_F = 10\text{ mA}$, $V_{CC} = 5\text{ V}$		f_{CO}		100		kHz
SATURATED							
Turn-on time	$I_F = 20\text{ mA}$	SFH617A-1	t_{on}		3		μs
	$I_F = 10\text{ mA}$	SFH617A-2	t_{on}		4.2		μs
		SFH617A-3	t_{on}		4.2		μs
	$I_F = 5\text{ mA}$	SFH617A-4	t_{on}		6		μs
Rise time	$I_F = 20\text{ mA}$	SFH617A-1	t_r		2		μs
	$I_F = 10\text{ mA}$	SFH617A-2	t_r		3		μs
		SFH617A-3	t_r		3		μs
	$I_F = 5\text{ mA}$	SFH617A-4	t_r		4.6		μs
Turn-off time	$I_F = 20\text{ mA}$	SFH617A-1	t_{off}		18		μs
	$I_F = 10\text{ mA}$	SFH617A-2	t_{off}		23		μs
		SFH617A-3	t_{off}		23		μs
	$I_F = 5\text{ mA}$	SFH617A-4	t_{off}		25		μs
Fall time	$I_F = 20\text{ mA}$	SFH617A-1	t_f		11		μs
	$I_F = 10\text{ mA}$	SFH617A-2	t_f		14		μs
		SFH617A-3	t_f		14		μs
	$I_F = 5\text{ mA}$	SFH617A-4	t_f		15		μs



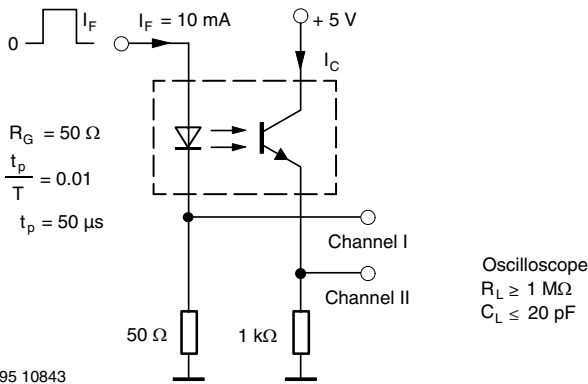
95 10804-3

Fig. 1 - Test Circuit, Non-Saturated Operation



t_p Pulse duration
 t_d Delay time
 t_r Rise time
 $t_{on} (= t_d + t_r)$ Turn-on time
 t_s Storage time
 t_f Fall time
 $t_{off} (= t_s + t_f)$ Turn-off time
 Storage time
 Fall time
 Turn-off time
 96 11698

Fig. 3 - Switching Times



95 10843

Fig. 2 - Test Circuit, Saturated Operation

SAFETY AND INSULATION RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55/115/21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	4470	V _{RMS}
Tested withstanding isolation voltage	According to UL1577, t = 1 s	V _{ISO}	5300	V _{RMS}
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V _{IOTM}	8000	V _{peak}
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V _{IORM}	890	V _{peak}
Isolation resistance	T _{amb} = 25 °C, V _{IO} = 500 V	R _{IO}	≥ 10 ¹²	Ω
	T _{amb} = 100 °C, V _{IO} = 500 V	R _{IO}	≥ 10 ¹¹	Ω
Output safety power		P _{SO}	700	mW
Input safety current		I _{SI}	400	mA
Input safety temperature		T _S	175	°C
Creepage distance	DIP-4		≥ 7	mm
Clearance distance	DIP-4		≥ 7	mm
Creepage distance	DIP-4, 400 mil, option 6		≥ 8	mm
Clearance distance	DIP-4, 400 mil, option 6		≥ 8	mm
Creepage distance	SMD-4, option 7 and option 9		≥ 7	mm
Clearance distance	SMD-4, option 7 and option 9		≥ 7	mm
Insulation thickness		DTI	≥ 0.4	mm

Note

- As per DIN EN 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for “safe electrical insulation” only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

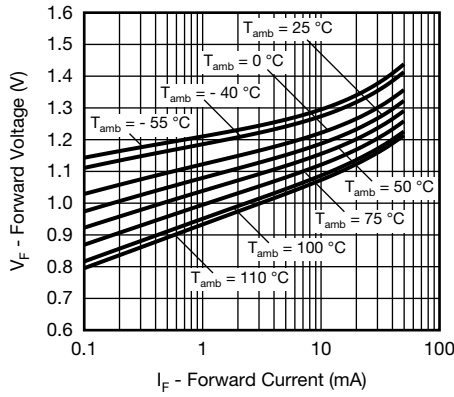


Fig. 4 - Forward Voltage vs. Forward Current

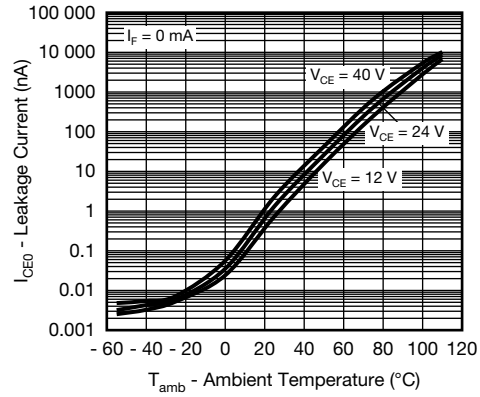


Fig. 7 - Leakage Current vs. Ambient Temperature

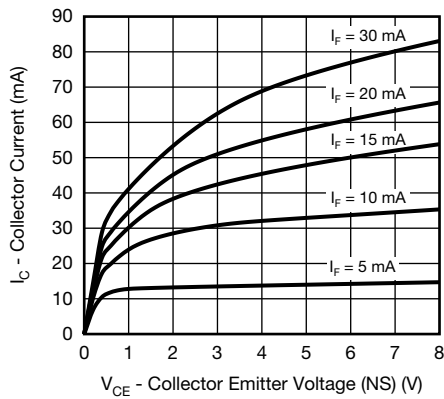


Fig. 5 - Collector Current vs. Collector Emitter Voltage (NS)



Fig. 8 - Normalized CTR (NS) vs. Ambient Temperature

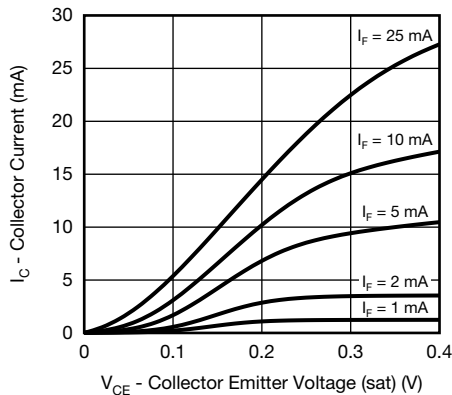


Fig. 6 - Collector Current vs. Collector Emitter Voltage (sat)

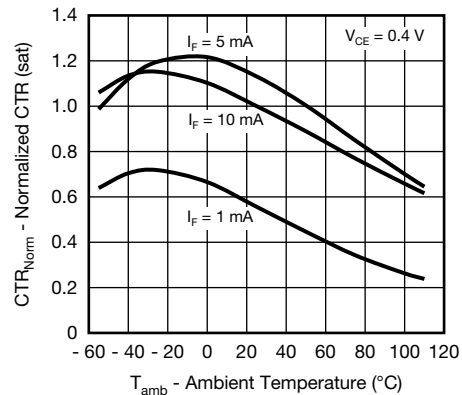


Fig. 9 - Normalized CTR (sat) vs. Ambient Temperature

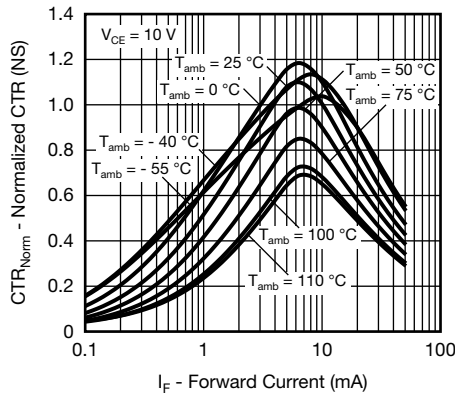


Fig. 10 - Normalized CTR (NS) vs. Forward Current



Fig. 13 - CTR Frequency vs. Collector Current

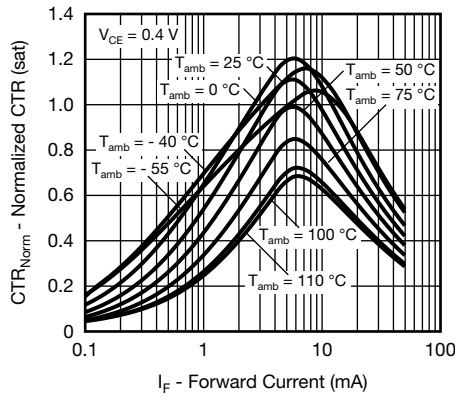


Fig. 11 - Normalized CTR (sat) vs. Forward Current

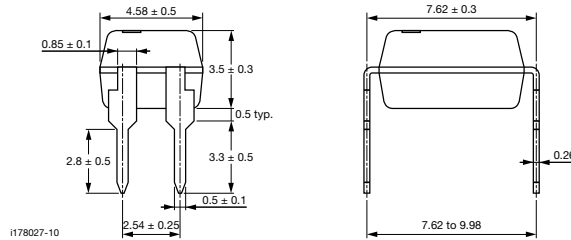
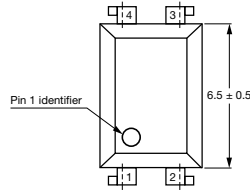


Fig. 14 - Switching Time vs. Load Resistance



Fig. 12 - CTR Frequency vs. Phase Angle

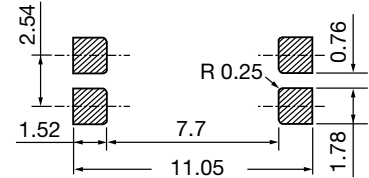
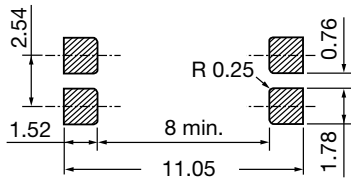
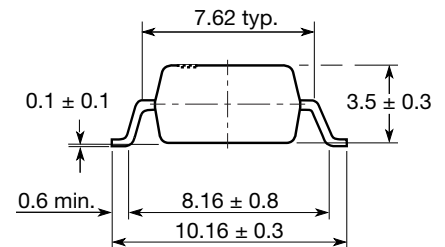
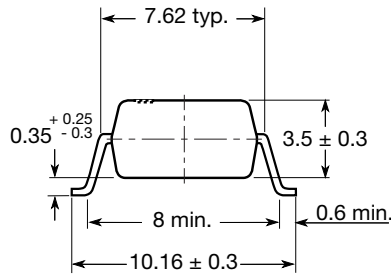
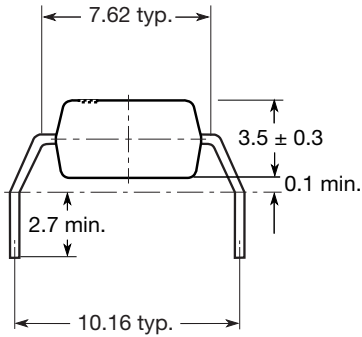
PACKAGE DIMENSIONS in millimeters



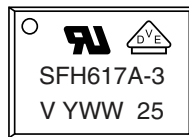
Option 6

Option 7

Option 9



PACKAGE MARKING



Notes

- VDE logo is only marked on option 1 parts. Option information is not marked on the part.
- Tape and reel suffix (T) is not part of the package marking.

SOLDER PROFILES

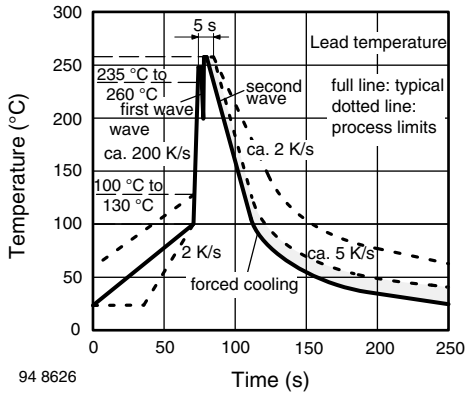


Fig. 15 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP-8 Devices

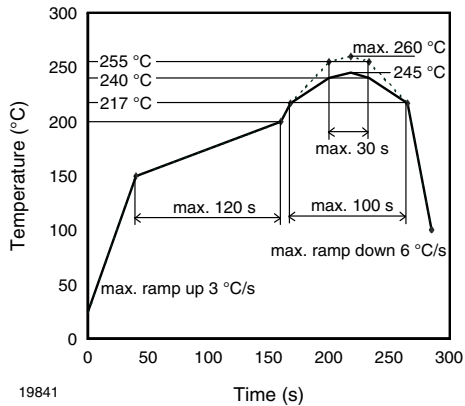


Fig. 16 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD-8 Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2

Floor life: unlimited

Conditions: $T_{amb} < 30\text{ °C}$, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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