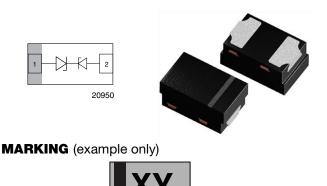
# VCUT0714BHD1

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**Vishay Semiconductors** 

## Bidirectional Asymmetrical (BiAs) Single Line ESD Protection Diode in DFN1006-2A



21121

Bar = pin 1 marking Y = type code (see table below) X = date code

#### LINKS TO ADDITIONAL RESOURCES



SPICE Application Notes

#### FEATURES

- Ultra compact DFN1006-2A
- AEC-Q101 qualified available
- Low package height
- 1-line ESD protection
- Working range -7 V up to +14 V or -14 V up to +7 V
- Low leakage current < 0.1 μA
- Low load capacitance typical C<sub>D</sub> = 8 pF
- ESD immunity acc. IEC 61000-4-2 ± 25 kV contact discharge ± 30 kV air discharge
- e3 Sn
  - Tin plated exposed side wall of lead frame
  - Soldering can be checked by standard vision inspection
  - AOI = automated optical inspection
  - No X-ray necessary
- PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

| ORDERING INFORMATION                  |   |   |               |                                |                    |  |  |
|---------------------------------------|---|---|---------------|--------------------------------|--------------------|--|--|
|                                       |   | ENVIRONMENTAL AND QUALITY                       | CODE          | PACKAGING CODE                 |                    |  |  |
| PART NUMBER AEC-Q<br>(EXAMPLE) QUALIF |   | RoHS-COMPLIANT +<br>LEAD (Pb)-FREE TERMINATIONS | TIN<br>PLATED | 10K PER 7" REEL<br>(8 mm TAPE) | ORDERING CODE      |  |  |
|                                       |   | GREEN   | FLATED        | MOQ = 10K                      |                    |  |  |
| VCUT0714BHD1                          | - | G   | 3             | -08                            | VCUT0714BHD1-G3-08 |  |  |
| VCUT0714BHD1                          | Н | G   | 3             | -08                            | VCUT0714BHD1HG3-08 |  |  |

| PACKAGE DATA |                 |                |              |         |           |                                      |                                 |  |
|--------------|-----------------|----------------|--------------|---------|-----------|--------------------------------------|---------------------------------|--|
| DEVICE NAME  | PACKAGE<br>NAME | PIN<br>Plating | TYPE<br>CODE |         |           | SOLDERING<br>CONDITIONS              |                                 |  |
| VCUT0714BHD1 | DFN1006-2A      | e3             | :A           | 0.83 mg | UL 94 V-0 | MSL level 1<br>(according J-STD-020) | Peak temperature<br>max. 260 °C |  |

| ABSOLUTE MAXIMUM RATINGS  |   |                   |             |      |  |  |
|---------------------------|---|-------------------|-------------|------|--|--|
| PARAMETER TEST CONDITIONS |   |                   | VALUE       | UNIT |  |  |
| Peak pulse current        | Pin 1 to pin 2, acc. IEC 61000-4-5, 8/20 µs/single shot | lanu.             | 3.6         | А    |  |  |
|                           | Pin 2 to pin 1, acc. IEC 61000-4-5, 8/20 µs/single shot | IPPM              | 2           | А    |  |  |
| Peak pulse power          | Pin 1 to pin 2, acc. IEC 61000-4-5, 8/20 µs/single shot | - P <sub>PP</sub> | 50          | W    |  |  |
|                           | Pin 2 to pin 1, acc. IEC 61000-4-5, 8/20 µs/single shot | грр               | 61          | W    |  |  |
|                           | Contact discharge acc. IEC 61000-4-2; 10 pulses         | V                 | ± 25        | kV   |  |  |
| ESD immunity              | Air discharge acc. IEC 61000-4-2; 10 pulses             | V <sub>ESD</sub>  | ± 30        | kV   |  |  |
| Storage temperature       |   | T <sub>STG</sub>  | -55 to +150 | °C   |  |  |
| Operating temperature     | Junction temperature; for AEC-Q101 qualified devices    | TJ                | -55 to +150 | °C   |  |  |

#### PATENT(S): <u>www.vishay.com/patents</u> This Vishay product is protected by one or more United States and international patents.

Rev. 1.3, 16-Mar-2021

1 For technical questions, contact: <u>ESDprotection@vishay.com</u> Document Number: 86177





HALOGEN FREE GREEN

<u>(5-2008)</u>

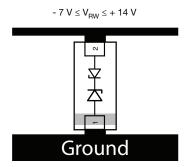
## VCUT0714BHD1

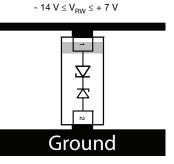


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#### **CUT THE SPIKES**

The VCUT0714BHD1 is a bidirectional but asymmetrical (BiAs) ESD protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VCUT0714BHD1 offers a high isolation (low leakage current, small capacitance) within the specified working range of -7 V to +14 V or -14 V and +7 V. Due to the short leads and small package size of the tiny DFN1006-2A package the line inductance is very low, so that fast transients like an ESD strike can be clamped with minimal over- or undershoots.





22286

| <b>ELECTRICAL CHARACTERISTICS</b> (pin 2 to pin 1)<br>(T <sub>amb</sub> = 25 °C, unless otherwise specified) |  |                      |      |      |      |       |  |
|--|--|----------------------|------|------|------|-------|--|
| PARAMETER  | TEST CONDITIONS/REMARKS                | SYMBOL               | MIN. | TYP. | MAX. | UNIT  |  |
| Protection paths   | Number of lines which can be protected | N <sub>channel</sub> | -    | -    | 1    | lines |  |
| Reverse stand-off voltage  | Max. reverse working voltage           | V <sub>RWM</sub>     | -    | -    | 14   | V     |  |
| Reverse voltage  | At I <sub>R</sub> = 0.1 μA             | V <sub>R</sub>       | 14   | -    | -    | V     |  |
| Reverse current  | At V <sub>RWM</sub> = 14 V             | I <sub>R</sub>       | -    | -    | 0.1  | μA    |  |
| Reverse breakdown voltage  | At I <sub>R</sub> = 1 mA               | V <sub>BR</sub>      | 14.5 | -    | -    | V     |  |
| Reverse clamping voltage   | At I <sub>PP</sub> = 1 A               | V <sub>C</sub>       | -    | -    | 27   | V     |  |
|  | At $I_{PP} = I_{PPM} = 2 A$            | V <sub>C</sub>       | -    | -    | 30   | V     |  |
| 0  | At $V_R = 0$ V; f = 1 MHz              | CD                   | -    | 8    | 8.5  | pF    |  |
| Capacitance  | At $V_R = 7 V$ ; f = 1 MHz             | CD                   | -    | 4    | -    | pF    |  |

| <b>ELECTRICAL CHARACTERISTICS</b> (pin 1 to pin 2)     |  |
|--|--|
| (T <sub>amb</sub> = 25 °C, unless otherwise specified) |  |

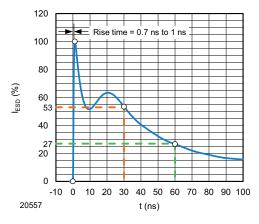
| (Tamb - 20 °C; alloco official de opcontou) |   |                      |      |      |      |       |  |
|---|---|----------------------|------|------|------|-------|--|
| PARAMETER                                   | TEST CONDITIONS/REMARKS                       | SYMBOL               | MIN. | TYP. | MAX. | UNIT  |  |
| Protection paths                            | Number of lines which can be protected        | N <sub>channel</sub> | -    | -    | 1    | lines |  |
| Reverse stand-off voltage                   | Max. reverse working voltage                  | V <sub>RWM</sub>     | -    | -    | 7    | V     |  |
| Reverse voltage                             | At I <sub>R</sub> = 0.1 μA                    | V <sub>R</sub>       | 7    | -    | -    | V     |  |
| Reverse current                             | At V <sub>RWM</sub> = 7 V                     | I <sub>R</sub>       | -    | -    | 0.1  | μA    |  |
| Reverse breakdown voltage                   | At I <sub>R</sub> = 1 mA                      | V <sub>BR</sub>      | 7.3  | -    | -    | V     |  |
| Reverse clamping voltage                    | At I <sub>PP</sub> = 1 A                      | V <sub>C</sub>       | -    | -    | 13   | V     |  |
| Reverse clamping voltage                    | At I <sub>PP</sub> = I <sub>PPM</sub> = 3.6 A | V <sub>C</sub>       | -    | -    | 15   | V     |  |
| Capacitance                                 | At V = 0 V; f = 1 MHz                         | CD                   | -    | 8    | 8.5  | pF    |  |
|   | At V = 3.5 V; f = 1 MHz                       | CD                   | -    | 6.4  | -    | pF    |  |

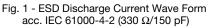
## VCUT0714BHD1



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### **TYPICAL CHARACTERISTICS** ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)





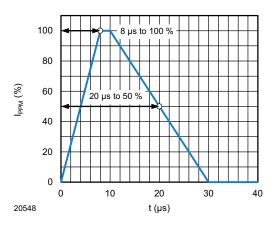


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

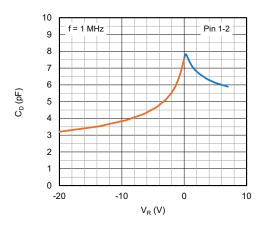


Fig. 3 - Typical Capacitance vs. Reverse Voltage

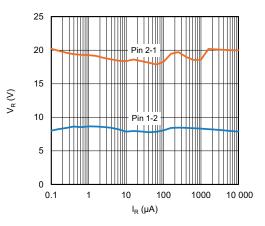


Fig. 4 - Typical Reverse Voltage vs. Reverse Current

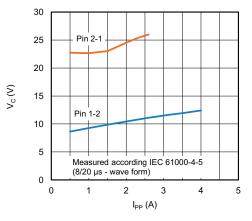


Fig. 5 - Typical Peak Clamping Voltage vs. Peak Pulse Current

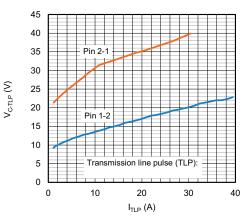


Fig. 6 - Typical Clamping Voltage vs. Peak Pulse Current

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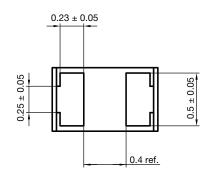
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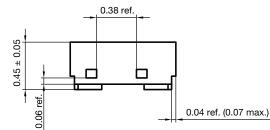
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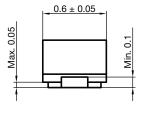


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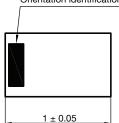
#### PACKAGE DIMENSIONS in millimeters (inches): DFN1006-2A

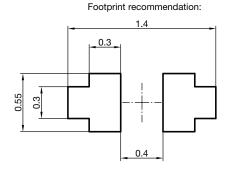






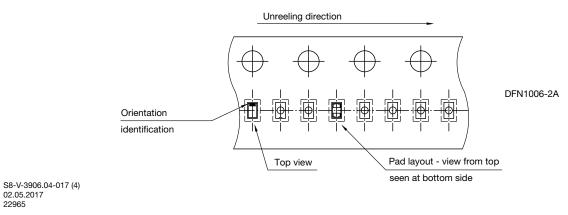
Orientation identification





Document no.: S8-V-3906.04-061 (4) Created - Date: 01. March 2019

#### **ORIENTATION IN CARRIER TAPE: DFN1006-2A**



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