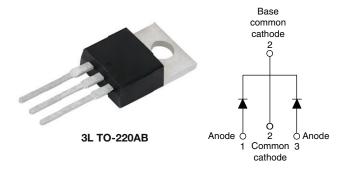


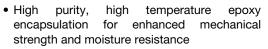
High Performance Schottky Rectifier, 2 x 30 A



PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 30 A							
V_{R}	150 V							
V _F at I _F	0.72 V							
I _{RM} max.	20 mA at 125 °C							
T _J max.	175 °C							
E _{AS}	0.4 mJ							
Package	3L TO-220AB							
Circuit configuration	Common cathode							

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-60CTQ150... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	60	Α					
V _{RRM}		150	V					
I _{FSM}	t _p = 5 μs sine	710	Α					
V _F	30 A _{pk} , T _J = 125 °C (typical, per leg)	0.69	V					
T _J	Range	-55 to +175	°C					

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-60CTQ150-M3	UNITS					
Maximum DC reverse voltage	V_{R}	150	V					
Maximum working peak reverse voltage	V_{RWM}	150	V					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	BOL TEST CONDITIONS			UNITS			
Maximum average forward per leg		50 % duty cycle at T _C = 137 °C, rectangular waveform		30				
current, see fig. 5 per device	I _{F(AV)}	50 % duty cycle at 1 _C = 157 C	60					
Maximum peak one cycle non-repetitive		5 μs sine or 3 μs rect. pulse	Following any rated load condition and	710	A			
surge current per leg, see fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	with rated V _{RRM} applied	270				
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 0.9 A, L = 1 mH		0.4	mJ			
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5$ x V_R typical		0.9	Α			



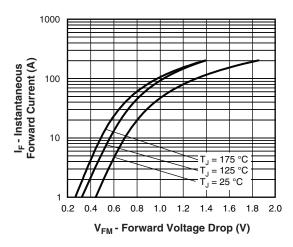
ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TYP	MAX.	UNITS				
		30 A	T _{.1} = 25 °C	0.83	0.88				
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	60 A	1]=25 0	0.98	1.09	V			
See fig. 1		30 A	T _{.1} = 125 °C	0.67	0.72				
		60 A	1J = 125 C	0.82	0.87				
Maximum reverse leakage current per leg	I _{RM}	T _J = 25 °C	V _R = Rated V _R	7	75	μΑ			
See fig. 2		T _J = 125 °C	VR = nateu VR	7.2	20	mA			
Typical junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1	650	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body			7.5	nH			
Maximum voltage rate of change	dV/dt	Rated V _R	- 1	10 000	V/µs				

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C			
Maximum thermal resistance,	per leg	В	DC operation See fig. 4	1.2	°C/W			
junction to case	per package	R_{thJC}	DC operation	0.6				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.25	G ,			
Approximate weight				6	g			
Approximate weight				0.21	OZ.			
Mounting torque	minimum			6 (5)	kgf · cm			
Mounting torque	maximum			12 (10)	(lbf ⋅ in)			
Marking device			Case style 3 L TO-220AB	60CT	Q150			





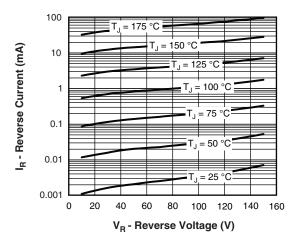


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

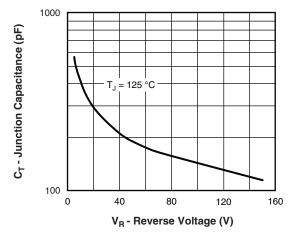


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

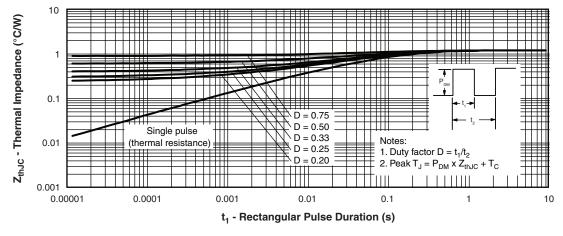
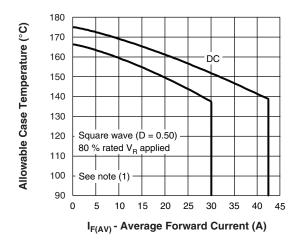
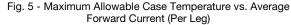


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)







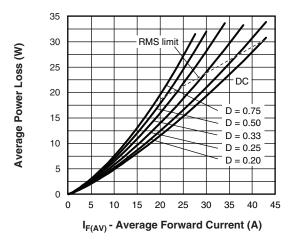


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

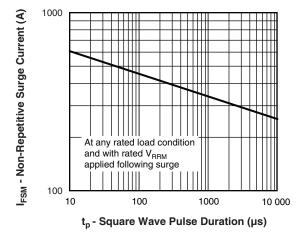


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

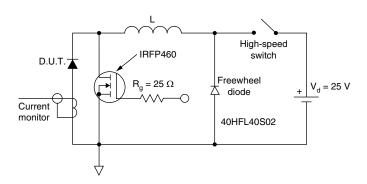


Fig. 8 - Unclamped Inductive Test Circuit

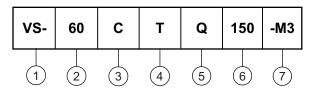
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE





- 1 Vishay Semiconductors product
- 2 Current rating (60 = 60 A)
- Circuit configuration

C = common cathode

4 - Package

T = TO-220

- 5 Schottky "Q" series
- 6 Voltage rating (150 = 150 V)
- 7 Environmental digit

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

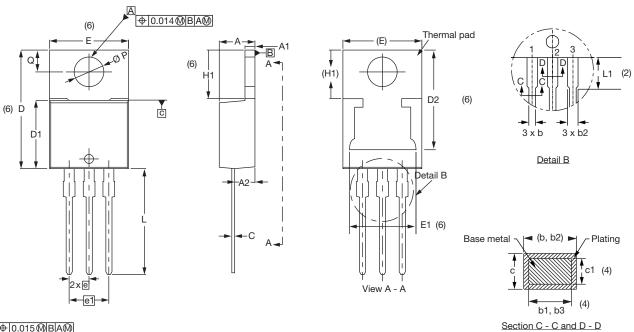
ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-60CTQ150-M3	50	1000	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?96154</u>							
Part marking information	www.vishay.com/doc?95028						

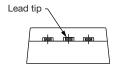


3L TO-220AB

DIMENSIONS in millimeters and inches



⊕ 0.015 **M** B A **M**



Conforms to JEDEC® outline TO-220AB

SYMBOL	MILLIM	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			Е	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355								

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



Vishay

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