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Vishay Semiconductors

# Ultralow V<sub>F</sub> Ultrafast Rectifier, 6 A FRED Pt<sup>®</sup>

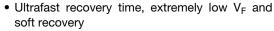




0 1 N/C	3 O And	ode

PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	6 A					
$V_{R}$	600 V					
V <sub>F</sub> at I <sub>F</sub>	0.87 V					
t <sub>rr</sub> (typ.)	59 ns					
T <sub>J</sub> max.	175 °C					
Package	DPAK (TO-252AA)					
Circuit configuration	Single					

### **FEATURES**





- 175 °C maximum operating junction temperature
- For PFC DCM operation
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

**HALOGEN** FREE

• Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	$V_{RRM}$		600	V			
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 156 °C	6				
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	80	Α			
Peak repetitive forward current	I <sub>FM</sub>	T <sub>C</sub> = 156 °C, f = 20 kHz, d = 50 %	12				
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS				
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-			
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 6 A	-	0.99	1.25	V		
		I <sub>F</sub> = 6 A, T <sub>J</sub> = 150 °C	-	0.87	1.05			
B		$V_R = V_R$ rated	-	-	5			
Reverse leakage current	I <sub>R</sub>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	-	125	μΑ		
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	3.5	-	pF		
Series inductance	Ls	Measured lead to lead 5 mm from package body	-	8	-	nH		





<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 A, dI_F/dt = 10$	$00 \text{ A/}\mu\text{s}, V_{\text{R}} = 30 \text{ V}$	ı	59	70		
Poverne recovery time	t <sub>rr</sub>	$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	75	-		
Reverse recovery time		T <sub>J</sub> = 25 °C	$I_F = 6A$ $dI_F/dt = 200 A/\mu s$ $V_R = 390 V$	-	154	-	ns	
		T <sub>J</sub> = 125 °C		-	215	-		
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	13.3	-	Α	
		T <sub>J</sub> = 125 °C		-	15.4	-		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	1055	-	0	
		T <sub>J</sub> = 125 °C		-	1600	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C	
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	-	3	°C/W	
Approximate weight			0.3		g		
Approximate weight				0.01		OZ.	
Marking device		Case style DPAK (TO-252AA)	6EWL06FN				



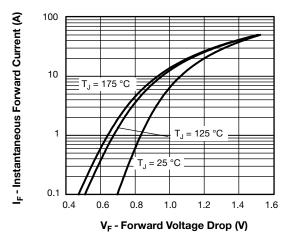


Fig. 1 - Typical Forward Voltage Drop Characteristics

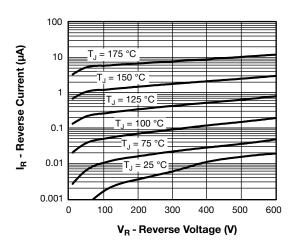


Fig. 2 - Typical Values of Reverse Current vs.
Reverse Voltage

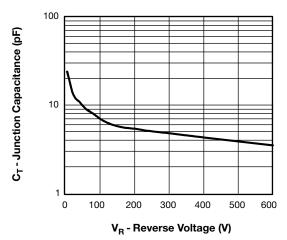


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

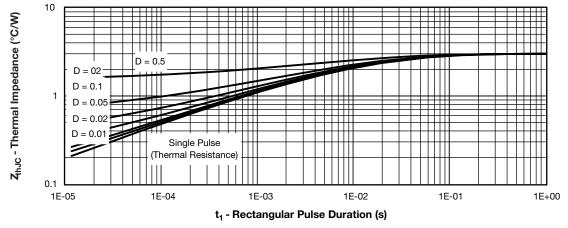


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

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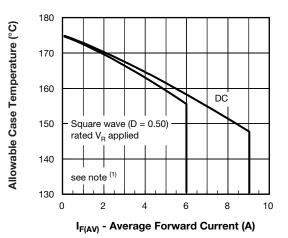


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

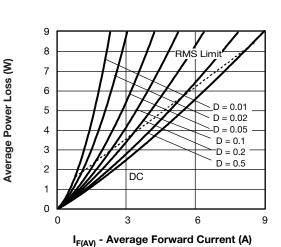


Fig. 6 - Forward Power Loss Characteristics

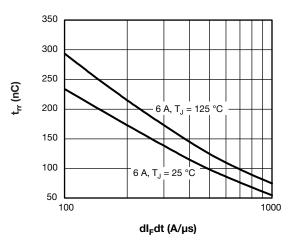


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

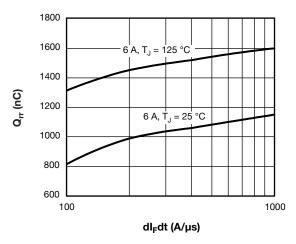


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{B1} \times I_B \text{ (1 - D)}; I_B \text{ at } V_{B1} = \text{rated } V_B \\ \end{array}$ 

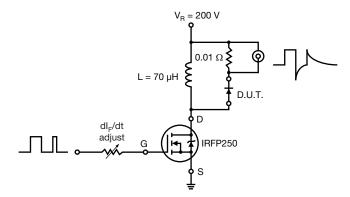
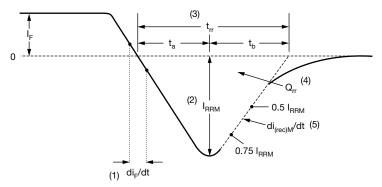


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $t_{\rm rr}$  reverse recovery time measured from zero crossing point of negative going  $I_{\rm F}$  to point where a line passing through 0.75  $I_{\rm RRM}$  and 0.50  $I_{\rm RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

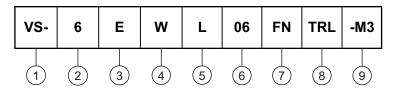
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_{\rm b}$  portion of  $t_{\rm rr}$ 

Fig. 10 - Reverse Recovery Waveform and Definitions

### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Vishay Semiconductors product
- 2 Current rating (6 = 6 A)
- 3 Circuit configuration:
  - E = single diode
- Package identifier:
  - W = D-PAK
- 5 L = low V<sub>F</sub>, fast recovery
- 6 Voltage rating (06 = 600 V)
- 7 FN = TO-252AA
- 8 • None = tube
  - TR = tape and reel
  - TRL = tape and reel (left oriented)
  - TRR = tape and reel (right oriented)
- 9 Environmental digit:
  - -M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-6EWL06FN-M3	75	3000	Antistatic plastic tube				
VS-6EWL06FNTR-M3	2000	2000	13" diameter reel				
VS-6EWL06FNTRL-M3	3000	3000	13" diameter reel				
VS-6EWL06FNTRR-M3	3000	3000	13" diameter reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95627				
Part marking information	www.vishay.com/doc?95176				
SPICE model	www.vishay.com/doc?95218				



# D-PAK (TO-252AA) "M"

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	-	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	5.21	-	0.205	1	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STINIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	BSC	0.090	BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74 BSC		0.108 REF.		
L2	0.51	BSC	0.020 BSC		
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
Ø	0°	10°	0°	10°	
Ø1	0°	15°	0°	15°	
Ø2	25°	35°	25°	35°	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- (5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (6) Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC® outline TO-252AA



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