Vishay Semiconductors

Hyperfast Rectifier, 60 A FRED Pt[®]



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PRIMARY CHARACTERISTICS								
I _{F(AV)}	60 A							
V _R	650 V							
V _F at I _F	1.6 V							
t _{rr} typ.	40 ns							
T _J max.	175 °C							
Package	TO-247AD 2L							
Circuit configuration	Single							

FEATURES

- Low forward voltage drop
- Hyperfast soft recovery time
- 175 °C operating junction temperature
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>



RoHS COMPLIANT HALOGEN FREE

DESCRIPTION / APPLICATIONS

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS						
Repetitive peak reverse voltage	V _{RRM}		650	V						
Average rectified forward current	I _{F(AV)}	T _C = 90 °C (d = 0.50)	60	^						
Non-repetitive peak surge current	I _{FSM}	T_C = 25 °C, t_p = 8.3 ms; half sine wave	500	A						
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C						

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	650	-	-				
Forward voltage	V _F	I _F = 60 A	-	2.1	2.5	V			
		I _F = 60 A, T _J = 150 °C	-	1.6	1.8				
Povoroa loakaga ourrant	I _R	$V_R = V_R$ rated	-	0.02	30				
Reverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	300	μΑ			
Junction capacitance	CT	V _R = 650 V	-	37	-	pF			
Series inductance	Ls	Measured lead to lead 5 mm from package body	-	8.0	-	nH			

 Revision: 15-May-2018
 1
 Document Number: 96452

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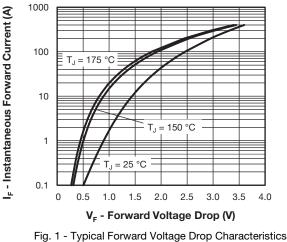
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS				
	t _{rr}	$I_F = 1 \text{ A}, \ dI_F/dt = 100$) A/µs, V _R = 30 V	-	42	-				
Reverse recovery time		T _J = 25 °C		-	40	-	ns - A			
		T _J = 125 °C	I _F = 60 A dI _F /dt = 1000 A/μs V _B = 400 V	-	90	-				
	I _{RRM}	T _J = 25 °C		-	19	-				
Peak recovery current		T _J = 125 °C		-	36	-				
Reverse recovery charge	Q _{rr}	T _J = 25 °C	• <u>n</u> = 100 1	-	540	-	nC			
		T _J = 125 °C		-	1850	-				

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C			
Thermal resistance, junction to case	R _{thJC}		-	-	0.65	°C/W			
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-				
Weight			-	5.5	-	g			
weight			-	0.2	-	oz.			
Mounting torque			1.2 (10)	-	2.4 (20)	kgf · cm (lbf · in)			
Marking device		Case style TO-247 2L		EPX6	5007L				

VS-EPX6007L-N3

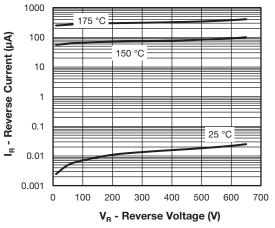
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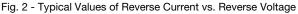


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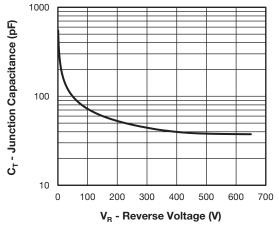


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

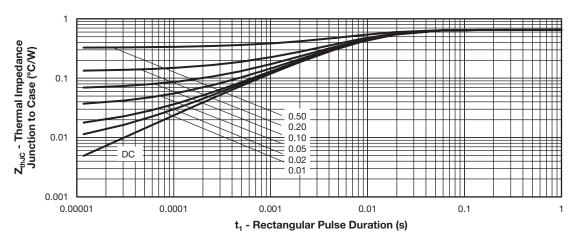
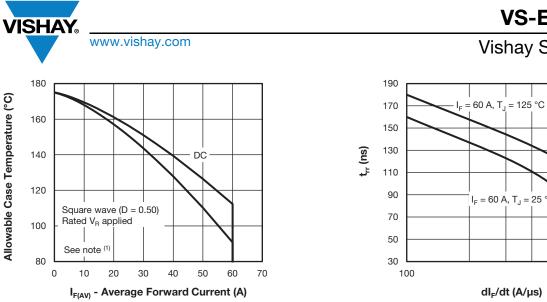
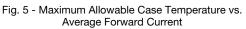


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics





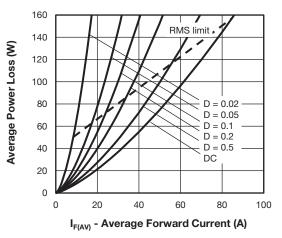
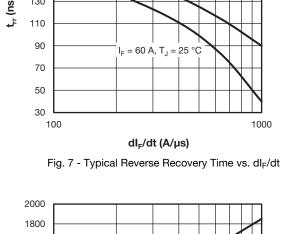


Fig. 6 - Forward Power Loss Characteristics



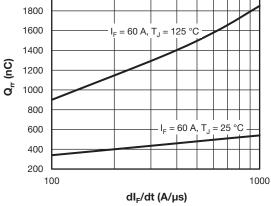


Fig. 8 - Typical Stored Charge vs. dl_F/dt

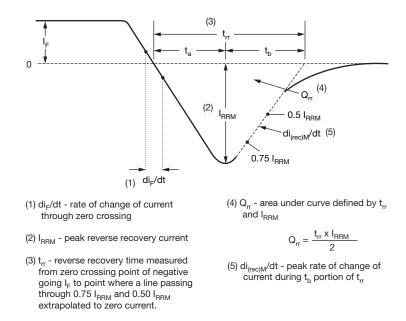


Fig. 9 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code	VS-	Е	Р	x	60	07	L	-N3
		(2)	(3)	4	(5)	6	$\overline{(7)}$	(8)
		Visl	nay Sen	niconduc	\bigcirc	oduct	\bigcirc	\bigcirc
	2 - E = single diode							
	3 -	P =	TO-247	,				
	4 -	X =	hyperfa	ist recov	ery time	e		
	5 -	Cur	rent coc	le (60 =	60 A)			
	6 -	Vol	age coo	de (07 =	650 V)			
	7 -	L =	long lea	ad				
	8 -			ntal digit				
		-N3	= halog	jen-free,	RoHS-	complia	nt and f	totally le

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-EPX6007L-N3	25	500	Antistatic plastic tube					

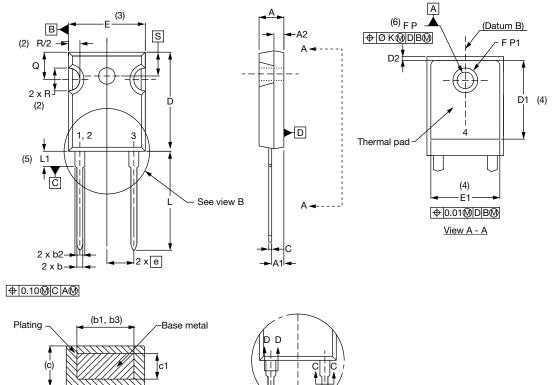
LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95536						
Part marking information	www.vishay.com/doc?95648					



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TO-247AD 2L

DIMENSIONS in millimeters and inches



Section C - C, D - D

(b. b2)

(4)

/	\square
	C C
Vie	<u>w B</u>

SYMBOL	MILLIM	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIDUL	MIN.	MAX.	MIN.	MAX.	NUTES
А	4.65	5.31	0.183	0.209			Е	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102			E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098			е	5.46	BSC	0.215	BSC	
b	0.99	1.40	0.039	0.055			ØК	0.2	254	0.0)10	
b1	0.99	1.35	0.039	0.053			L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	
D2	0.51	1.35	0.020	0.053								

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

⁽²⁾ Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

(7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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