

APPROVAL SHEET

MR10, MR12, MR08, MR06, MR04

±1%, ±5%, Jumper

Thick film General Purpose Chip Resistors Size 1206, 1210, 0805, 0603, 0402 Automotive & Military Compliant Anti-sulfur ASTM B-809 60°C 500hs RoHS 2 Compliant with exemption 7C-I Halogen free

*Contents in this sheet are subject to change without prior notice.

FEATURE

- 1. High reliability and stability ±1%
- 2. Sulfuration resistant
- 3. Automotive AEC Q-200 & Military MIL-STD Compliant
- 4. 100% CCD inspection
- 5. RoHS 2 compliant with exemption 7C-I and Halogen free products
- 6. Anti-sulfuration ASTM B-809 60°C 500hrs

APPLICATION

- Automotive application
- Consumer electrical equipment
- EDP, Computer application
- Telecom application

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

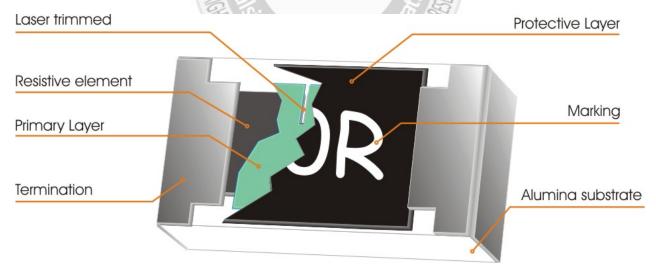


Fig 1. Construction of a Chip-R

QUICK REFERENCE DATA

Item		G	eneral Specification	on			
Series No.	MR10	MR12	MR08	MR06	MR04		
Size code	1210(3225)	1210(3225) 1206(3216) 0805(2012) 0603(1608) 0402(1					
Resistance Range		1Ω~10M	1Ω (±5% tolerance)	, Jumper			
		E96/E24	I1Ω~10MΩ (±1% to	lerance)			
Resistance Tolerance			±1%, E96/E24				
			±5%, E24				
TCR (ppm/°C)							
R > 1MΩ	≤ ± 200	≤ ± 200	≤ ± 200	≤ ± 200	≤ ± 200		
$10\Omega < R \le 1M\Omega$	≤ ± 100	≤ ± 100	≤ ± 100	≤ ± 100	≤ ± 100		
$R \le 10\Omega$	-200~+400	-200~+400	-200~+400	-200~+400	-200~+400		
Max. dissipation @ T _{amb} =70°C	1/2 W	1/4 W	1/4 W	1/8 W	1/10 W		
Max. Operation Voltage (DC or RMS)	200V	200V	150V	75V	50V		
Max. Overload Voltage (DC or RMS)	400V 400V 300V 150V 100V						
Climatic category (IEC 60068)	55/155/56						

Note:

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

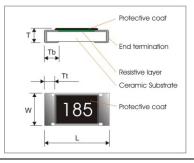
 $RCWV = \sqrt{Rated Power \times Resistance Value or Max. RCWV listed above, whichever is lower.}$

For Jumper (0Ω) :

	Item	General Specification						
Series No.		MR10	MR12	MR08	MR06	MR04		
Size code		1210(3225)	1206(3216)	0805(2012)	0603(1608)	0402(1005)		
Sistance Range ≤50mΩ for P tolerance ≤30mΩ for F tolerance								
TCR (ppm/	/°C)		TCR is not	applicable for Jump	er product			
Max. dissip @ T _{amb} =70		1/2 W	1/4 W	1/4 W	1/8 W	1/10 W		
Р	Rated Current	3A	2A	1.5A	1A	1A		
tolerance	Peak Current	7.5A	5A	3.5A	3A	2A		
F	Rated Current	4A	2.9A	2A	1.8A	1A		
tolerance Peak Current		10A	5.8A	4A	3.6A	2A		
Operation	temperature			-55 ~ +155°C		•		

DIMENSIONS (unit: mm)

	MR10	MR12	MR08	MR06	MR04
L	3.10 ± 0.10	3.10 ± 0.10	2.00 ± 0.10	1.60 ± 0.10	1.00 ± 0.05
W	2.60 ± 0.10	1.60 ± 0.10	1.25 ± 0.10	0.80 ± 0.10	0.50 ± 0.05
Т	0.55 ± 0.10	0.60 ± 0.15	0.50 ± 0.15	0.45 ± 0.15	0.35 ± 0.05
Tb	0.50 ± 0.20	0.45 ± 0.20	0.40 ± 0.20	0.30 ± 0.15	0.25 ± 0.10
Tt	0.50 ± 0.20	0.50 ± 0.20	0.40 ± 0.20	0.30 ± 0.10	0.20 ± 0.10



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CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

MR12	X	472_	J	Т	L
Size code	Type code	Resistance code	Tolerance code	Packaging code	Termination code
MR10: 1210 MR12: 1206 MR08: 0805 MR06: 0603 MR04: 0402	X : Jumper $\pm 5\%$ tol. $1\Omega \sim 10 M\Omega$ $\pm 1\%$ tol. $10\Omega \sim 1 M\Omega$ W : $\pm 1\%$ tol. $1\Omega < R < 10\Omega$ $\pm 1\%$ tol. $1M\Omega < R < 10M\Omega$	$\pm 5\%$,E24: 2 significant digits followed by no. of zeros and a blank 4.7Ω=4R7_ $\pm 10\Omega$ =100_ $\pm 20\Omega$ =221_ $\pm 1\%$,E24+E96: 3 significant digits followed by no. of zeros $\pm 102\Omega$ =1020 $\pm 102\Omega$ =37.4KΩ=3742 $\pm 102\Omega$ =82R0 For Jumper (0Ω) P tol. ± 50 mΩ=000_ F tol. ± 30 mΩ=0000 Remark: ± 2 means a blank	$F: \pm 1\%$ $J: \pm 5\%$ For Jumper (0Ω) $P: \le 50m\Omega$ $F: \le 30m\Omega$	 T :7" Reeled taping Q :10" Reeled taping G :13" Reeled taping H :13" reel 50Kpcs only for 0402 B :Bulk D :7" reel 20Kpcs only for 0402 A :7" reel 15Kpcs only for 0402 	L:Sn base (lead-free)

Remark:

1210, 1206, 0805, 0603:

- 1. 8mm width paper taping 5000pcs per 7" reel, 10kpcs per 10" reel, 20kpcs per 13" reel.
- 2. Bulk packaging: 5000pcs per poly-bag

0402:

- 1. 8mm width paper taping 10,000pcs per 7" reel, 70kpcs per 13" reel.
- 2. Bulk packaging: 5000pcs per poly-bag

MARKING

Size \ Nr. Of digit of code\tolerance	±5% & Jumper(0Ω)	±1%
1210/1206/0805	3 digits marking	4 digits marking
0603 (1608)	3 digits marking	3 digits marking
0402(1005)	NO MA	RKING

3 digits marking [$\pm 5\%$: 1210, 1206, 0805, 0603 & Jumper (0 Ω)]

Each resistor is marked with a three digits code on the protective coating to designate the nominal resistance value.

3 digits marking $(\pm 1\% : 0603)$

Nominal	l resistan	ce			Description										
1.E-24 s	series			As <i>0603</i>	s 0603 WR06X±5%.										
2.E-96 s	series			The 1st value :	two digit o	codes ar	e referrin	g to the	CODE or	the tab	le, the 3r	d code is	s the inde	x of resi	stance
				Y=10 ⁻² ,	X=10 ⁻¹ ,	A=10 ⁰ ,	B=10 ¹ ,	C=10 ² ,	$D=10^3$,	E=10 ⁴ ,	F=10 ⁵				
						EX:	17.8 Ω=	25X,17	'8Ω=25A	,1K78	=25B				
						18	17K8=2	25C · 17	78K=25D	, 1M78	=25E				
3. Rema	ark			There is	no marki	ng for th	ie items a	re not u	nder E-24	and E-	96 series				
CODE	R_value	CODE	R_value	CODE	R_Value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

4 digits marking (\pm 1%: 1210,1206,0805)

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value.

Example

RESISTANCE	Jumper (0 Ω)	10Ω	12Ω	100Ω	6800Ω	47000Ω
3 digits marking (1210, 1206, 0805, 0603 ±5% & Jumper)	000	100	120	101	682	473
4 digits marking	-	10R0	12R0	1000	6801	4702

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FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E24 series for resistors with a tolerance of $\pm 5\%$, and E24+E96 series for resistors with a tolerance of $\pm 1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063".

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

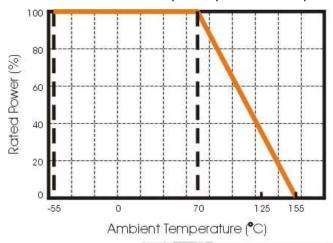


Figure 2 Maximum dissipation in percentage of rated power as a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

Storage and Handling Conditions:

- 1. Products are recommended to be used up within two years since operation date as ensured shelf life. Check solderability in case shelf life extension is needed.
- 2. To store products with following condition:

Temperature :5 to 40°C

Humidity :20 to 70% relative humidity

- 3. Caution:
 - a.Don't store products in a corrosive environment such as sulfide, chloride gas, or acid.

It may cause oxdization of electrode, which easily be resulted in poor soldering

- b.To store products on the shelf and avoid exposure to moisture.
- c.Don't expose products to excessive shock, vibration, direct sunlight and so on

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SOLDERING CONDITION follows J-STD-020D

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds.

Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

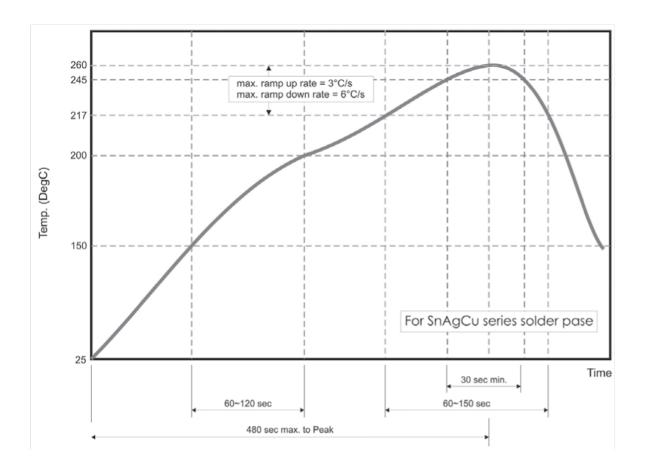


Fig 3. Recommended IR reflow soldering profile for SMT process with SnAgCu series solder paste

TEST AND REQUIREMENTS

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, sub-clause 5.3. Unless otherwise specified, the following value supplied:

Temperature: 15°C to 35°C. Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar). All soldering tests are performed with midly activated flux.

TEST	PROCEDURE / TEST METHOD	REQUIREMEN'	TS
IESI	PROCEDURE / TEST WETHOD	Resistance	Ω0
Electrical Characteristics JISC5201-1: 1998 Clause 4.8	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade. $\frac{R_2-R_1}{R_1(t_2-t_1)}\times 10^6 \text{ (ppm/°C)} t_1:20^\circ\text{C+5°C-1°C}$ $R_1: \text{Resistance at reference temperature}$ $R_2: \text{Resistance at test temperature}$	Within the specified tole Refer to "QUICK REFEI DATA"	
Resistance to soldering heat (R.S.H) MIL-STD-202 method 210	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 270°C±5°C	ΔR/R max. ±(0.5%+0.05Ω) No visible damage	<50mΩ
Solderability J-STD-002	 a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C / 5sec. b) Steam the sample dwell time 1 hour/ solder dipping 260°C / 7sec. 	95% coverage min., go and no visible damage	od tinning
Temperature cycling JESD22 method JA-104	1000 cycles, -55°C ~ +155°C, dwell time 5~10min	Δ R/R max. $\pm (0.5\% + 0.05\Omega)$ No visible damage	<50mΩ
Moisture Resistance MIL-STD-202 method 106	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	Δ R/R max. $\pm (0.5\% + 0.05\Omega)$ No visible damage	<50mΩ
Bias Humidity MIL-STD-202 method 103	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	ΔR/R max. ±(1%+0.05Ω) No visible damage	<50mΩ
Operational Life MIL-STD-202 method 108	1000+48/-0 hours; 35% of operation power, 125±2°C	Δ R/R max. $\pm (1\% + 0.05\Omega)$ No visible damage	<50mΩ

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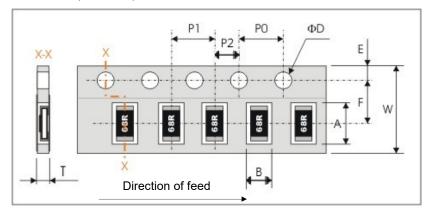


TECT	DROCEDURE / TEST METHOD	REQUIREMENTS	
TEST	PROCEDURE / TEST METHOD	Resistance	0Ω
High Temperature Exposure MIL-STD-202 method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	Δ R/R max. \pm (1%+0.05 Ω) No visible damage	<50mΩ
Mechanical Shock MIL-STD-202 method 213	1/2 Sine Pulse / 1500g Peak / Velocity 15.4ft/sec	Within the specified tolerance No visible damage	<50mΩ
Board Flex AEC-Q200-005	Resistors mounted on a 90mm glass epoxy resin PCB(FR4), bending once 2mm for 10sec	Δ R/R max. \pm (1.0%+0.05 Ω). No visible damage	<50mΩ
Terminal strength AEC-Q200-006	Pressurizing force: 1Kg, Test time: 60±1sec.	No remarkable damage or rem the terminations	oval of
Vibration MIL-STD-202 method 204	Test 5g's for 20min., 12 cycles each of 3 orientations	Δ R/R max. \pm (1.0%+0.05 Ω) No visible damage	<50mΩ
Thermal shock MIL-STD-202 method 107	Test –55 to 155℃/ dwell time 15min/ Max transfer time 20sec 300cycles	Δ R/R max. \pm (0.5%+0.05 Ω) No visible damage	<50mΩ
Short Time Overload (STOL) JISC5201-1: 1998 Clause 4.13	2.5 times RCWV or max. overload voltage, for 5seconds	$\pm 5\%$: ΔR/R max. $\pm (2\%+0.05\Omega)$ $\pm 1\%$: ΔR/R max. $\pm (1\%+0.05\Omega)$ No visible damage	<50mΩ
ESD AEC-Q200-002	Test contact 1.0KV (0.5KV for 0402 only)	Δ R/R max. \pm (1%+0.05 Ω) No visible damage	<50mΩ
Sulfuration test ASTM B-809-95	ASTM B-809 60'C 500hrs	\pm 5%: ΔR/R max. \pm (2%+0.10Ω) \pm 1%: ΔR/R max. \pm (2%+0.05Ω) No visible damage	<50mΩ

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PACKAGING

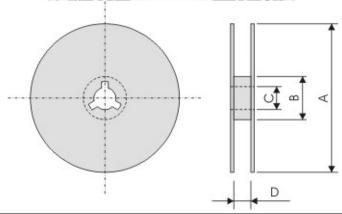
Paper Tape specifications (unit :mm)



Series No.	А	В	W	F	Е
MR10	3.60±0.20	3.00±0.20			
MR12	3.60±0.20	2.00±0.20			
MR08	2.40±0.20	1.65±0.20	8.00±0.30	3.50 ± 0.20	1.75±0.10
MR06	1.90±0.20	1.10±0.20	18 SX		
MR04	1.20±0.10	0.70±0.10			

Series No.	P1	P0	P2	ΦD	Т
MR10/12/08	4.00±0.10	77	THE STATE OF THE S		Max. 1.0
MR06	4.00±0.10	4.00±0.10	2.00±0.10	Φ 1.50 $^{+0.1}_{-0.0}$	0.65±0.05
MR04	2.00±0.10	PASSIVE SYSTEM	ALL TANCE		0.40±0.05

Reel dimensions



Symbol	А	В	С	D
7" reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5
10" reel	Ф254.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5
13" reel	Ф330.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5

单击下面可查看定价,库存,交付和生命周期等信息

>>Walsin Technology(华新科技)