

# APPROVAL SHEET

# **RF Switch Series – RoSH Compliance**

SPDT GPIO Switch

**Halogens Free Product** 

Any 2G/3G/4G Band for TRx System

P/N: RFASWA141ATF09

\*Contents in this sheet are subject to change without prior notice.



## **FEATURES**

Low Insertion Loss and Low Distortion

■ Broadband frequency range: 0.4 to 2.7 GHz

■ Low ON-state resistance and OFF-state capacitance

■ High power and peak voltage handling

■ Low control voltage: 1.2V to 2.8V

■ High ESD tolerance of 2kV HBM at all pins

■ Miniature footprint : 1.67 x 1.47 x 0.55 mm<sup>3</sup>

■ Moisture Sensitive Level 3 (MSL3)

#### **Description**

■ The RFASWA141ATF09 is a Single-Pole, Double-Throw (SPDT) switch designed for antenna tuning applications that require very low R<sub>ON</sub> and C<sub>OFF</sub>. The RFASWA141ATF09 provides rugged power handling and simple 1-bit GPIO control. The EN pin configures an "all-off state" as well as low power mode. The RFASWA141ATF09 is manufactured in LGA-9 (1.67 x 1.47 x 0.55 mm<sup>3</sup>) package

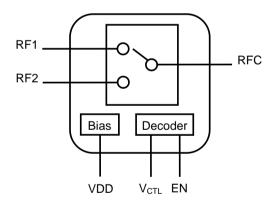
■ The RFASWA141ATF09 features very low DC power consumption.

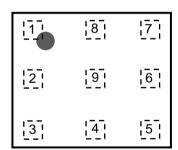
■ The RFASWA141ATF09 has ESD protection devices to achieve excellent ESD performances. No DC Blocking capacitors are required for all RF ports unless DC is biased externally.

#### **Application**

- Antenna Tuning
- Band Switching
- Impedance Tuning

#### **Block Diagram and Pin Out (Top View)**



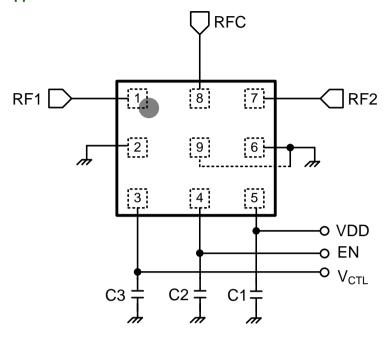


#### **Pin Names and Descriptions**

| Pin | Name             | Description        | Pin | Name | Description    |
|-----|------------------|--------------------|-----|------|----------------|
| 1   | RF1              | RF path 1          | 6   | GND  | Ground         |
| 2   | GND              | Ground             | 7   | RF2  | RF path 2      |
| 3   | V <sub>CTL</sub> | DC control voltage | 8   | RFC  | RF common port |
| 4   | EN               | Enable             | 9   | GND  | Ground         |
| 5   | VDD              | DC power supply    |     |      |                |



# **Application Circuit**



#### **Parts List**

| Parts No. | Value                         |
|-----------|-------------------------------|
| C1        | 100 F                         |
| C2-C3     | No placement, do not populate |

# **Absolute Maximum Ratings**

| Parameter  | Symbol                        | Minimum | Maximum | Units |
|--|-------------------------------|---------|---------|-------|
| RFx Input Power, $50\Omega$                                      | Pin                           |         | +41     | dBm   |
| DC Supply Voltage  | VDD                           |         | +5.2    | V     |
| DC Control Voltage   | V <sub>CTL</sub>              |         | +3.0    | V     |
| Enable Voltage   | EN                            |         | +5.2    | V     |
| Max differential RF voltage between the RF ports V <sub>RF</sub> | V <sub>P</sub>                | 40      |         | V     |
| Storage temperature  | T <sub>STG</sub>              | -55     | +150    | °C    |
| Operating temperature  | T <sub>OP</sub>               | -30     | +85     | °C    |
| HBM ESD Voltage, All Pins  | V <sub>ESD</sub> <sup>1</sup> | -       | +2000   | V     |

Note 1: Human Body Model ESD Voltage

Exceeding absolute maximum ratings may cause permanent damage. Operation between operating range maximum and absolute maximum for extended periods may reduce reliability.



**Electrical Specifications** 

(Top= 25°C, VDD=2.85V, EN & V<sub>CTL</sub>=0/1.8V, Characteristic Impedance ZO= 50 Ω, Unless Otherwise Noted)

| (10p= 23 0, VDD=2.03 V, EN & 1                            | 7 CIL 07 1.0 17     |   | T ==, C.             | 1                            |                              | 110100.,             |
|---|---------------------|---|----------------------|------------------------------|------------------------------|----------------------|
| Parameter Sys   |                     | Test Condition  | Min.                 | Тур.                         | Max.                         | Units                |
| RF Specifications   |                     |   |                      |                              |                              |                      |
| Operating Frequency                                       | f                   |   | 400                  |                              | 3000                         | MHz                  |
| Insertion Loss<br>(RFC to RF1/2 port)                     | IL                  | 700 MHz<br>915 MHz<br>1910 MHz<br>2700 MHz  |                      | 0.20<br>0.20<br>0.30<br>0.35 | 0.35<br>0.40<br>0.60<br>0.70 | dB<br>dB<br>dB<br>dB |
| Isolation<br>(RFC to RF1/2 port)<br>Logic State 1&2       | Iso                 | 700 MHz<br>915 MHz<br>1910 MHz<br>2700 MHz  | 21<br>20<br>14<br>11 | 25<br>23<br>17<br>14         |                              | dB<br>dB<br>dB<br>dB |
| Isolation<br>(RFC to RF1/2 port)<br>Logic State 3         | Iso                 | 700 MHz<br>915 MHz<br>1910 MHz<br>2700 MHz  | 16<br>14<br>9<br>7   | 20<br>18<br>12<br>10         |                              | dB<br>dB<br>dB<br>dB |
| Isolation<br>(RF1 to RF2 port)<br>Logic State 3           | Iso                 | 700 MHz<br>915 MHz<br>1910 MHz<br>2700 MHz  | 40<br>35<br>25<br>20 | 45<br>40<br>29<br>24         |                              | dB<br>dB<br>dB<br>dB |
| On state match  | VSWR                | 915 MHz<br>1910 MHz   | 1.43<br>1.43         | 1.22<br>1.22                 |                              | -                    |
| On resistance<br>(RFC to RF1/2 port)<br>Logic State 1&2   | R <sub>ON</sub>     |   |                      | 1.1                          | 1.4                          | Ω                    |
| Off capacitance<br>(RFC to RF1/2 port)<br>Logic State 1&2 | C <sub>OFF</sub>    |   |                      | 0.22                         | 0.27                         | pF                   |
| RFx Harmonics   | 2f0                 | PIN = +23dBm, f = 700MHz<br>PIN = +35dBm, f = 915MHz<br>PIN = +23dBm, f = 2570MHz |                      | -97<br>-73<br>-90            |                              | dBm<br>dBm<br>dBm    |
| TA X Hamilton   | 3f0                 | PIN = +23dBm, f = 700MHz<br>PIN = +35dBm, f = 915MHz<br>PIN = +23dBm, f = 2570MHz |                      | -98<br>-73<br>-93            |                              | dBm<br>dBm<br>dBm    |
| 2nd Order Input Intercept Point                           | IIP2                | See IIP2 test conditions Table  | 110                  | 120                          |                              | dBm                  |
| 3rd Order Input Intercept Point                           | IIP3                | See IIP3 test conditions Table  | 72                   | 75                           |                              | dBm                  |
| DC Specification (Decoder)                                |                     |   |                      |                              |                              |                      |
| Supply Voltage  | VDD                 |   | 2.4                  | 2.85                         | 5.0                          | V                    |
| Cupply Current  |                     | Active Mode, VDD= 2.85V   |                      | 55                           | 75                           | μA                   |
| Supply Current  | I <sub>DD</sub>     | Low Power Mode, VDD= 2.85V  |                      | 2.5                          | 5                            | μA                   |
| Enable Control Voltage                                    | V <sub>EN</sub>     |   | 1.2                  | 1.8                          | 5.0                          | V                    |
| Enable Control Current                                    | I <sub>EN</sub>     | EN= 1.8V  |                      |                              | 5                            | μΑ                   |
| Control Voltage(High)                                     | V <sub>CTL(H)</sub> |   | 1.2                  | 1.8                          | 2.8                          | V                    |
| Control Voltage(Low)                                      | V <sub>CTL(L)</sub> |   | 0                    |                              | 0.45                         | V                    |
| Control Current   | I <sub>CTL</sub>    | V <sub>CTL</sub> = 1.8V   |                      |                              | 5                            | μΑ                   |
| Switching Specification                                   |                     |   |                      |                              |                              |                      |
| On switching speed  | T <sub>SW</sub>     | 50% V <sub>CTL</sub> to 90% RF On   |                      | 8                            | 10                           | μs                   |
| Off switching speed                                       | T <sub>SW</sub>     | 50% V <sub>CTL</sub> to 90% RF Off  |                      | 8                            | 10                           | μs                   |
|   |                     |   |                      |                              |                              |                      |

Note : All measurements made in a  $50\Omega$  system with 0/+1.8V control voltages, unless otherwise specified.



# **IIP2 Test Conditions**

| Band    | In-Band Freq<br>(MHz) | CW tone 1<br>(MHz) | CW tone 1<br>(dBm) | CW tone 2<br>(MHz) | CW tone 2<br>(dBm) |
|---------|-----------------------|--------------------|--------------------|--------------------|--------------------|
| 1       | 2140.0                | 1950.0             | +20                | 190.0              | <b>–15</b>         |
| '       | 2140.0                | 1950.0             | +26                | 4090.0             | -20                |
| 2       | 2 1960.0              | 1960.0 1880.0      | +20                | 80.0               | <b>–15</b>         |
| 2       |                       |                    | +26                | 3840.0             | -20                |
| 5       | 004 5                 | 926 5              | +20                | 45.0               | <b>–</b> 15        |
| 5       | 5 881.5               | 836.5              | +26                | 1718.0             | -20                |
| 0       | 0 040.5               | 0.40.5             | +20                | 45.0               | <b>–15</b>         |
| 8 942.5 | 942.5 897.5           |                    | +26                | 1840.0             | -20                |

## **IIP3 Test Conditions**

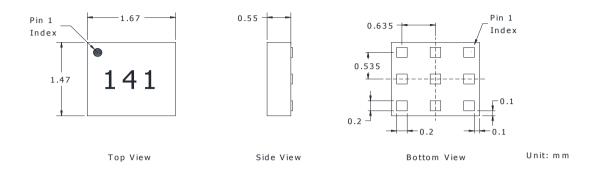
| Band | In-Band Freq<br>(MHz) | CW tone 1<br>(MHz) | CW tone 1<br>(dBm) | CW tone 2<br>(MHz) | CW tone 2<br>(dBm) |
|------|-----------------------|--------------------|--------------------|--------------------|--------------------|
| 1    | 2140.0                | 1950.0             | +20                | 1760.0             | <b>–15</b>         |
| 2    | 1960.0                | 1880.0             | +20                | 1800.0             | <b>–15</b>         |
| 5    | 881.5                 | 836.5              | +20                | 791.5              | <b>–15</b>         |
| 8    | 942.5                 | 897.5              | +20                | 852.5              | <b>–15</b>         |

# Logic Table for Switch On-Path (High=1.8V ,Low= 0V)

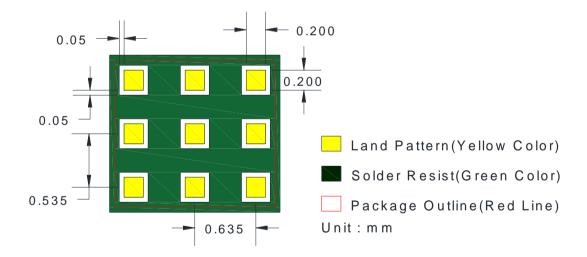
| Logic State | EN | V <sub>CTL</sub> | RF1            | RF2 |  |
|-------------|----|------------------|----------------|-----|--|
| 1           | 1  | 0                | on             | off |  |
| 2           | 1  | 1                | off            | on  |  |
| 3           | 0  | 1                | off            | off |  |
| 4           | 0  | 0                | Low Power Mode |     |  |



# Package Dimensions



#### **Solder Land Pattern**





# Reliability test

| TEST                       | PROCEDURE / TEST METHOD                      | REQUIREMENT                                      |
|----------------------------|--|--|
| Solderability              | *Solder bath temperature: 255 ± 5°C          | At least 95% of a surface of each terminal       |
| JIS C 0050-4.6             | *Immersion time: 5 ± 0.5 sec                 | electrode must be covered by fresh solder.       |
| JESD22-B102D               | Solder : Sn3Ag0.5Cu for lead-free            |  |
| High temperature           | *Temperature : 90°C±2°C                      | No mechanical damage.                            |
| JIS C 0021                 | *Test duration: 1000+24/-0 hours             | Electrical specification shall satisfy the       |
|                            | Measurement to be made after keeping at room | descriptions in electrical characteristics under |
|                            | temperature for 24±2 hrs                     | the operational temperature range within -30 ~   |
|                            |  | 90°C.  |
| Low temperature            | *Temperature: -30°C±2°C                      | No mechanical damage.                            |
| JIS C 0020                 | *Test duration: 1000+24/-0 hours             | Electrical specification shall satisfy the       |
|                            | Measurement to be made after keeping at room | descriptions in electrical characteristics under |
|                            | temperature for 24±2 hrs                     | the operational temperature range within -30 ~   |
|                            |  | 90°C.  |
| Temperature cycle          | 1. 30±3 minutes at -30±3°C,                  | No mechanical damage.                            |
| JIS C 0025                 | 2. 10~15 minutes at room temperature,        | Electrical specification shall satisfy the       |
|                            | 3. 30±3 minutes at +90±3°C,                  | descriptions in electrical characteristics under |
|                            | 4. 10~15 minutes at room temperature,        | the operational temperature range within -30 ~   |
|                            | Total 100 continuous cycles                  | 90°C.  |
|                            | Measurement to be made after keeping at room |  |
|                            | temperature for 24±2 hrs                     |  |
| High temperature operation | *Temperature : 90°C                          | No mechanical damage.                            |
| life (HTOL)                | *VDD = 4.8V                                  | Electrical specification shall satisfy the       |
|                            | *Time: 1000+24/-0 hrs.                       | descriptions in electrical characteristics under |
|                            | Measurement to be made after keeping at room | the operational temperature range within -30 ~   |
|                            | temperature for 24±2 hrs                     | 90°C.  |

# **Soldering condition**

Typical examples of soldering processes that provide reliable joints without any damage are given in Figure 11.

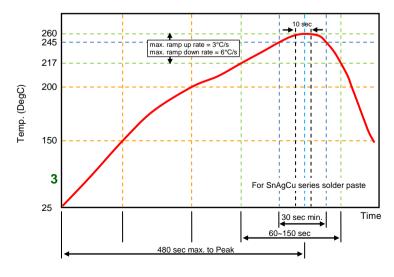


Figure 11. Infrared soldering profile

# **Preliminary Product Information**

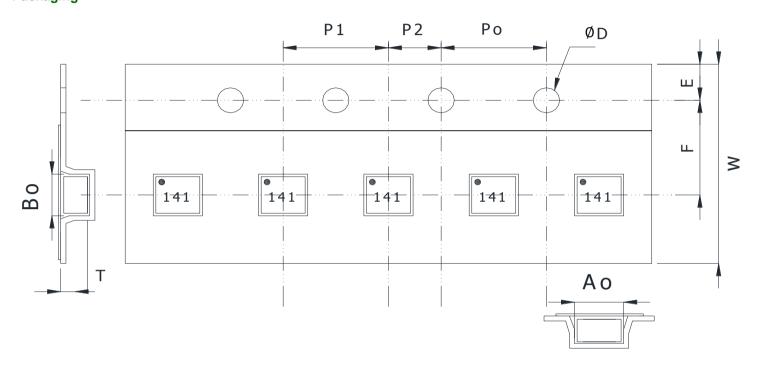


Ordering code

| RF               | ASW                 | Α           | 141A        | Т         |
|------------------|---------------------|-------------|-------------|-----------|
| RF module        | Module type         | Application | Design Code | Packing   |
| RM:              | ASW: Antenna Switch | K : SPDT    |             | T: Taping |
| Walsin RF Switch |                     |             |             |           |
| Device           |                     |             |             |           |

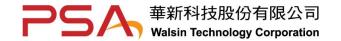
Minimum Ordering Quantity: 3000 pcs per reel.

# **Packaging**

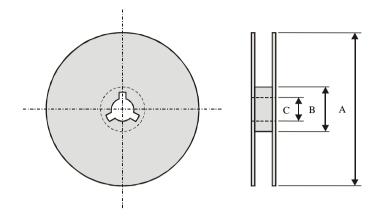


# Plastic Tape specifications (unit :mm)

| Index          | Ao          | Во              | ΦD              | T               | W               |
|----------------|-------------|-----------------|-----------------|-----------------|-----------------|
| Dimension (mm) | 1.87 ± 0.05 | 1.67 ± 0.05     | $0.80 \pm 0.01$ | $0.75 \pm 0.05$ | 8.00 + 0.3/-0.1 |
| Index          | E           | F               | Po              | P1              | P2              |
| Dimension (mm) | 1.75 ± 0.05 | $3.50 \pm 0.05$ | 4.00 ± 0.10     | 4.00 ± 0.10     | 2.00 ± 0.05     |



#### Reel dimensions



| Index          | А      | В     | С     |
|----------------|--------|-------|-------|
| Dimension (mm) | Ф178.0 | Ф54.0 | Ф13.2 |

Taping Quantity: 3000 pieces per 7" reel

#### Caution of handling

#### **Limitation of Applications**

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects, which might directly cause damage to the third party's life, body or property.

- (1) Aircraft equipment
- (2) Aerospace equipment
- (3) Undersea equipment
- (4) Medical equipment
- (5) Disaster prevention / crime prevention equipment
- (6) Traffic signal equipment
- (7) Transportation equipment (vehicles, trains, ships, etc.)
- (8) Applications of similar complexity and /or reliability requirements to the applications listed in the above.

#### Storage condition

- (1) Products should be used in 6 months from the day of WALSIN outgoing inspection, which can be confirmed.
- (2) Storage environment condition.
  - Products should be storage in the warehouse on the following conditions.

Temperature : -10 to +40°C

Humidity: 30 to 70% relative humidity

- Don't keep products in corrosive gases such as sulfur. Chlorine gas or acid or it may cause oxidization of electrode, resulting in poor solderability.
- Products should be storage on the palette for the prevention of the influence from humidity, dust and son on.
- Products should be storage in the warehouse without heat shock, vibration, direct sunlight and so on.
- Products should be storage under the airtight packaged condition.

单击下面可查看定价,库存,交付和生命周期等信息

>>Walsin Technology(华新科技(华科))