

APPROVAL SHEET

WK12L, WK20L, WK25L

±1%, ±5%

Thick Film Power Chip Resistor
Wide Termination
RoHS compliant and Halogen free
Size 0612, 1020, 1225
Automotive AEC Q200 Compliant
ASTM B-809-95 Compliant

*Contents in this sheet are subject to change without prior notice.



FEATURE

- 1. High power rating and compact size
- 2. High reliability and stability
- 3. Reduced size of final equipment
- 4. RoHS compliant and Halogen free products
- 5. AEC Q200 Compliant
- 6. ASTM B-809-95 60'C 1000hrs compliant

APPLICATION

- Power supply
- PDA
- Digital meter
- Computer
- Automotives
- Battery charger
- DC-DC power converter

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added at longer sides. For ease of soldering the outer layer of these end terminations is Tin (lead free) alloy.

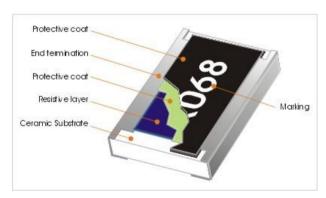


Fig 2. Construction of Chip-R



QUICK REFERENCE DATA

Item	General Specification			
Series No.	WK12L	WK12L WK20L WK25L		
Size code	0612(1632)	1020 (2550),	1225(3264)	
Resistance Tolerance	±:	±5% (E24); ±1% (E24+E96)		
Resistance Range		1Ω ~ 1ΜΩ,		
TCR (ppm/°C):	± 200 ppm/°C	± 200 ppm/°C ± 200 ppm/°C ± 200 ppm/°C		
Max. dissipation at T _{amb} =70°C	3/4W	1 W	2W	
Max. Operation Voltage (DC or RMS)	200V 200V 200V			
Max. Overload Voltage (DC or RMS)	400V 400V 400V			
Climatic category (IEC 60068)	55/155/56			

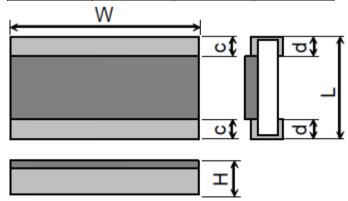
Note:

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage: So called RCWV (Rated Continuous Working Voltage) is determined by

RCWV = $\sqrt{\text{RatedPower} \times \text{Resistance Value}}$ or Max. RCWV listed above, whichever is lower.

MECHANICAL DATA (unit: mm)

TYPE	WK12L	WK20L	WK25L
W	3.20±0.20	5.00±0.20	6.30±0.20
L	1.60±0.20	2.50±0.15	3.20±0.20
Н	0.55±0.10	0.55±0.10	0.55±0.10
С	0.50±0.25	0.60±0.20	0.60±0.20
d	0.50±0.25	0.60±0.20	0.60±0.20





MARKING

Each resistor is marked with a four-digit code on the protective coating to designate the nominal resistance value.

Example:

 $1R00 = 1\Omega$ $3901 = 3900\Omega$ $1101 = 1100\Omega$



FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E96 & E24 series for resistors with a tolerance of $\pm 5\%$ & $\pm 1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063".

Derating curve

The power that the resistor can dissipate depends on the operating temperature; see Fig.3

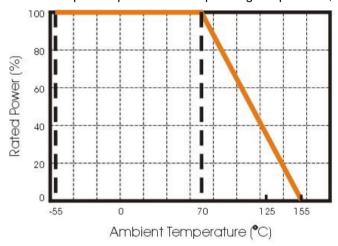


Figure 3. Maximum dissipation in percentage of rated power as a function of the ambient temperature.

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.



SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 4.

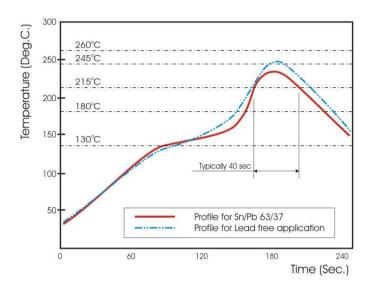


Fig 4. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

WK12	L	472_	J	Т	L	V
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination	Special code
WK25 : 1225	L : wide	5% E24 : 2 significant	J : ±5%	T:7" Reel	code	V = AEC Q200
WK20 : 1020	termination	digits followed by no. of zeros and a blank	F : ±1%	taping	L = Sn base (lead free)	Compliant + ASTM B-809-
WK12: 0612		4.7Ω =4R7_	P : Jumper		, ,	95 Compliant
		10Ω =100_				
		1% E24+E96: 3 significant digits followed by no. of zeros				
		102Ω =1020				
		37.4ΚΩ =3742				



TEST AND REQUIREMENTS (AEC Q200)

No.	Test items	Condition of test	Performance requirements
1	High temperature exposure	MIL-STD-202 Method 108	Resistor: ΔR/R: Within ± (3%+0.1Ω)
	AEC Q200 - No.3	Ambient temperature:155±2°C,	Chip jumper: 50mΩ max.
		Condition: Without load,	No visible damage
		Duration: 1000 +48 h	710 10m20 0m210g0
		Interval measurements: 250 h and 500 h	
2	Temperature cycling	JESD22 Method JA-104	Resistor: ΔR/R: Within ± (1%+0.05Ω)
	AEC Q200 - No.4	Temperature: -55±3°C / 125±2°C,	Chip jumper: 50mΩ max.
		Dwell time: 30min maximum at each temp.	No visible damage
		Transition time: 1 min. max.	The visible damage
		Number of cycles: 1000 cycles.	
		Interval measurements: 250 cy and 500 cy	
3	Bias humidity	MIL-STD-202 Method 103	Resistor: $\Delta R/R$: Within \pm (3%+0.1 Ω)
	AEC Q200 - No.7	Condition: 85°C & 85% R.H.	Chip jumper: 50mΩ max.
		Test power: 10% of rated power shall be	No visible damage
		applied for continuously.	
		Duration: 1,000 ⁺⁴⁸ ₀ h	
		Interval measurements: 250 h and 500 h	
4	Operational life	MIL-STD-202 Method 108	Resistor: ΔR/R: Within ± (3%+0.1Ω)
-	AEC Q200 – No.8	Ambient temperature: 125±2°C	Chip jumper: $50\text{m}\Omega$ max.
	ALC Q200 = No.8	The applied voltage shall be the voltage to be	No visible damage
		calculated at 35% of rated dissipation or the	No visible damage
		limiting element voltage whichever is the	
		smaller.	
		Condition: The voltage shall be applied for	
		continuously.	
		Duration: 1000 +48 h	
		Interval measurements: 250 h and 500 h	
5	Dimensions	JESD22 Method JB-100	As in Table–3
"	AEC Q200 – No.10	OLODZZ MCGIOGOD-100	As iii labic=5
6	Resistance to Solvents	MIL-STD-202 Method 215	Resistor: ΔR/R: Within ± (1%+0.05Ω)
"	AEC Q200 – No.12	Solvent: 2-propanol at 25°C	Chip jumper: 50mΩ max.
	7120 0(200 110.12	Immersion time: 3 min	No visible damage
		Brush: 10 times brushing	No visible damage
		Immersion and brush cycle: 3cycle	
7	Mechanical Shock	MIL-STD-202 Method 213	Resistor: ΔR/R: Within ± (1%+0.05Ω)
l '	AEC Q200 – No.13	Waveform: half sine,	Chip jumper: $50m\Omega$ max.
		Peak value100G,	No visible damage
		Normal duration 6ms	No visible damage
		Condition: XX'YY'ZZ', 10times each	
8	Vibration	MIL-STD-202 Method 204	Resistor: ΔR/R: Within ± (1%+0.05Ω)
-	AEC Q200 – No.14	Peak acceleration and Sweep time: 5 g's for 20	Chip jumper: $50 \text{m}\Omega$ max.
	7.23 3.233 713.77	min , Frequency 10Hz to 2000Hz,	No visible damage
		Condition: 12 cycles each of 3 orientations	TVO VISIDIE Garriage
9	Resistance to soldering heat	MIL-STD-202 Method 210	Resistor: ΔR/R: Within ± (1%+0.05Ω)
"	AEC Q200 - No.15	Solder bath temp: 260±5°C	
	7.20 Q200 - 140.10	Immersed time: 10±1s	Chip jumper: 50mΩ max. No visible damage
		miniciped unic. 10±13	INO VISIDIE GAITIAGE

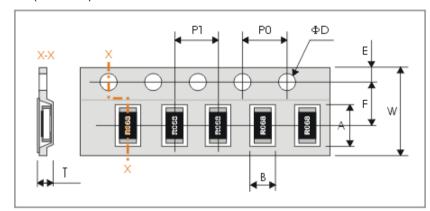


No	Test items	Condition of test	Performance requirements
10	ESD test AEC Q200 – No.17	AEC-Q200-002 Human body model, 2 Kohm, 150 pF, Test voltage: 2000V	Resistor: Δ R/R: Within \pm (5%+0.1 Ω) Chip jumper: 50m Ω max. No visible damage
11	Solderability AEC Q200 – No.18	J-STD-002 a) Bake the sample for 155 °C dwell time 4h / solder dipping 235°C/5s. Solder: Sn96.5-Ag3-Cu0.5 b) Category 3, Solder dipping 215°C/5s. Solder: Sn63Pb37 c) Category 3, Solder dipping 260°C/7s.	The surface of terminal immersed shall be min. of 95% covered with a new coating of solder.
12	Electrical Characterization AEC Q200 - No.19	1. D.C. Resistance 2. Temperature Coefficient of Resistance –55 °C / +20 °C +20 °C / +125 °C	The resistance value shall correspond with the rated resistance taking into account the specified tolerance. As in Table–1
13	Bending strength AEC Q200 – No.21	AEC-Q200-005 Bending value2mm Holding time: 60sec.	Resistor: Δ R/R: Within \pm (1%+0.05 Ω) Chip jumper: 50 m Ω max. No visible damage
14	Adhesion AEC Q200 – No.22	AEC-Q200-006 Pressurizing force: 17.7N Test time: 60±1s.	Resistor: $\Delta R/R$: Within \pm (1%+0.05 Ω) Chip jumper: $50m\Omega$ max. No visible damage
15	Humid Sulfur vapor test (FOS)	ASTM B809 Reagent: Sulfur (Saturated vapor) Test temp.: 60°C Relative humidity: 95%RH Test period: 1000h	Resistor: $\Delta R/R$: Within $\pm (1.0\% + 0.05\Omega)$ Chip jumper: $50 \text{m}\Omega$ max. No visible damage



PACKAGING

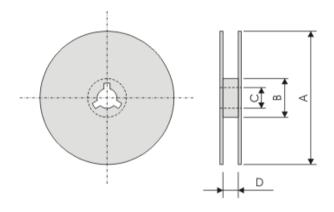
Tape specifications (unit :mm)



Type	Α	В	W	F	E
WK12L	3.60±0.20	2.00±0.15	8.00±0.30	3.50±0.10	
WK20L	5.50±0.20	3.10±0.20	12.00±0.30	5.50±0.10	1.75±0.10
WK25L	6.90±0.20	3.60±0.20	12.00±0.30	5.50±0.10	

Туре	P1	P0	ΦD	Т
WK12L			Ф1.50 ^{+0.1}	Max 1.0
WK20L	4.00±0.10	4.00±0.10		1.10±0.15
WK25L				1.10±0.15

Reel dimensions



(unit: mm)

Symbol	Α	В	С	D
7" Reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	12.4.0±1.00
10" Reel	Ф254.0±2.0	Φ100.0±1.0	13.0±0.2	14.0±0.20
13" Reel	Ф330.0±2.0	Φ100.0±1.0	13.0±0.2	14.0±0.20

Taping quantity

WK20L, WK25L by plastic tape taping 4,000 pcs per 7" reel!

WK12L by paper tape taping 5,000 pcs per 7" reel!

单击下面可查看定价,库存,交付和生命周期等信息

>>Walsin Technology(华新科技)