

APPROVAL SHEET

SR12, SR10, SR08, SR06, SR04

±1%, ±5%

Thick Film Anti-Sulfur Chip Resistors
Size 1206, 1210, 0805, 0603, 0402
(Automotive & Anti-Sulfuration)
RoHS 2 Compliant with exemption 7C-I
Halogen free

*Contents in this sheet are subject to change without prior notice.

FEATURE

- 1. High reliability and stability ±1%
- 2. Sulfuration resistant against ASTM B-809 105'C 1000hrs
- 3. Automotive grade AEC Q-200 Compliant
- 4. 100% CCD inspection
- 5. RoHS 2 compliant with exemption 7C-I and Halogen free products

APPLICATION

- Automotive application
- Consumer electrical equipment
- EDP, Computer application
- Telecom application

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The extra protective metal film is added onto top side electrodes to protect termination from sulfuration. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

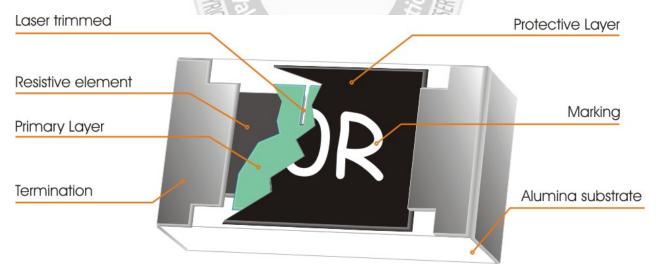


Fig 1. Construction of a Chip-R

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QUICK REFERENCE DATA

Item	General Specification							
Series No.	SR10_W	SR12_W	SR08_W	SR06_W	SR04_W			
Size code	1210(3225)	1206(3216)	0805(2012)	0603(1608)	0402(1005)			
Resistance Range	1Ω~1ΜΩ							
Resistance			± 1%, E96+E24					
Tolerance			± 5%, E24					
TCR (ppm/°C)								
$10\Omega < R \le 1M\Omega$	≤ ± 100	≤ ± 100	≤ ± 100	≤ ± 100	≤ ± 100			
R <= 10Ω	-200~+400	-200~+400	-200~+400	-200~+400	-200~+400			
Max. dissipation @ T _{amb} =70°C	1/3 W	1/4 W	1/8 W	1/10 W	1/10 W			
Max. Operation Voltage (DC or RMS)	200V	200V	150V	75V	50V			
Max. Overload Voltage (DC or RMS)	400V	400V	300V	150V	100V			
Climatic category (IEC 60068)	matic tegory (IEC 55/155/56							

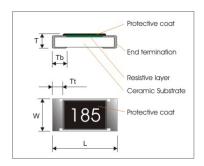
Note:

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage: So called RCWV (Rated Continuous Working Voltage) is determined by

 $RCWV = \sqrt{Rated Power \times Resistance Value}$ or Max. RCWV listed above, whichever is lower.

DIMENSIONS (unit: mm)

	SR10	SR12	SR08	SR06	SR04
L	3.10 ± 0.10	3.10 ± 0.10	2.00 ± 0.10	1.60 ± 0.10	1.00 ± 0.05
W	2.60 ± 0.10	1.60 ± 0.10	1.25 ± 0.10	0.80 ± 0.10	0.50 ± 0.05
Т	0.55 ± 0.10	0.60 ± 0.15	0.50 ± 0.15	0.45 ± 0.15	0.35 ± 0.05
Tb	0.50 ± 0.20	0.45 ± 0.20	0.40 ± 0.20	0.30 ± 0.15	0.25 ± 0.10
Tt	0.50 ± 0.20	0.50 ± 0.20	0.40 ± 0.20	0.30 ± 0.10	0.20 ± 0.10



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MARKING

Size \ Nr. Of digit of code\tolerance	± 1%, ±.5%
1210/1206/0805	± 1%: 4-digits marking; ± 5%: 3-digits marking
0603 (1608)	3-digits marking
0402(1005)	N0 MARKING

3-digits marking (0603)

Nomina	l resistan	се			Description										
E-96 se	ries			The 1st value :	The 1st two digit codes are referring to the CODE on the table, the 3rd code is the index value:							x of resi	stance		
				Y=10 ⁻² ,	X=10 ⁻¹ ,	A=10 ⁰ ,	B=10 ¹ ,	C=10 ² ,	$D=10^3$,	E=10 ⁴ ,	F=10 ⁵				
						EX:	17.8 Ω=	25X,17	′8Ω=25A	,1K78	=25B				
							17K8=2	25C · 17	78K=25D	, 1M78	=25E				
3. Rema	ark			There is	no marki	ng for th	e items a	re not u	nder E-24	and E-	96 series				
CODE	R_value	CODE	R_value	CODE	R_Value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

4-digits marking (1210/1206/0805)

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value.

Example

RESISTANCE	10Ω	12Ω	100Ω	6800Ω	47000Ω
4-digits marking	10R0	12R0	1000	6801	4702

FUNCTIONAL DESCRIPTION

Product Dimension of a and b.

Location		Measuring point	Control Conditions
Extended length of end electrode	Measure the length of a dimension		Size above 0603: a>35um 0402: a >25um 0201: a> 15um
The thickness of the resistive protective layer by the electrode interface.	Measure the thinkness of b dimension	THE RESERVE OF THE PERSON OF T	Size above 0603: b>7um 0402/0201: b>6um

Product characterization

Standard values of nominal resistance are taken from the E24& E96 series for resistors with a tolerance of $\pm 0.5\%$, $\pm 0.1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063"

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

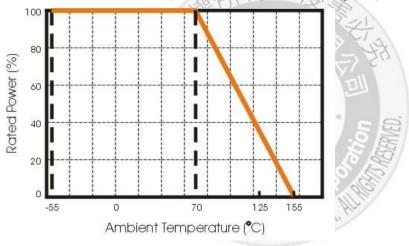


Figure 2 Maximum dissipation in percentage of rated power as a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

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SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

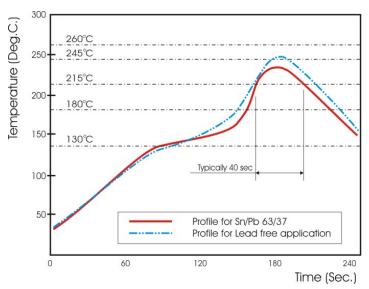


Fig 3. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

SR12	X	1020	F	Т	L	w
Automotive	Type code	Resistance code	Tolerance	Packaging code	Termination	Special code
Code SR10: 1210 SR12: 1206 SR08: 0805 SR06: 0603 SR04: 0402	X: ±1%, 10-1M ±5%, 1-10M W: ±1%, <10, >1M	E24+E96: 3 significant digits followed by no. of zeros E24: 102Ω = 1020 E96: 37.4KΩ = 3742	F : ±1% J : ±5% P : Jumper	T: 7" Reeled taping Q: 10" Reeled taping G: 13" Reeled taping 70K/RL H: 13" Reeled taping 50K/RL B: Bulk	code L = Sn base (lead free)	W = Anti-sulfur ASTM B-809 105'C 1000hrs

SR10, SR12, SR08, SR06:

1. Reeled tape packaging: 8mm width paper taping 5000pcs per 7" reel, 10kpcs per 10" reel, 20kpcs per 13" reel.

2. Bulk packaging : 5000pcs per poly-bag

SR04:

1. Reeled tape packaging : 8mm width paper taping 10,000pcs per reel,

2. Bulk packaging : 10,000pcs per poly-bag

TEST AND REQUIREMENTS

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, sub-clause 5.3. Unless otherwise specified, the following value supplied:

Temperature: 15°C to 35°C. Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar). All soldering tests are performed with midly activated flux.

TEOT	PROCEDURE / TEST METUOR	REQUIREMENT			
TEST	PROCEDURE / TEST METHOD	Resistor	0Ω		
Electrical Characteristics	- DC resistance values measurement	Within the specified tolerance	е		
	- Temperature Coefficient of Resistance (T.C.R)	Refer to "QUICK REFERENCE	CE		
JISC5201-1: 1998	Natural resistance change per change in degree centigrade.	DATA"			
Clause 4.8	$\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)} t_1 : 20^{\circ}\text{C} + 5^{\circ}\text{C} - 1^{\circ}\text{C}$				
	R ₁ : Resistance at reference temperature				
	R ₂ : Resistance at test temperature				
Resistance to soldering	Un-mounted chips completely immersed for 10±1second in a SAC	Δ R/R max. ±(0.5%+0.05Ω)			
heat (R.S.H)	solder bath at 270°C±5°C	No visible damage	45000		
MIL-STD-202	· · · · · · · · · · · · · · · · · · ·		<50mΩ		
method 210	0,02				
Solderability	a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C/ 5sec.	95% coverage min., good tir	nning		
J-STD-002	b) Steam the sample dwell time 1 hour/ solder dipping 215°C/ 5sec.	No visible damage			
	c) Steam the sample dwell time 1 hour/ solder dipping 260°C/7sec.				
Temperature cycling	1000 cycles, -55 $^{\circ}$ C ~ +155 $^{\circ}$ C, dwell time 5~10min	Δ R/R max. \pm (0.5%+0.05 Ω)			
JESD22		No visible damage	<50mΩ		
method JA-104					
Moisture Resistance	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	Δ R/R max. \pm (0.5%+0.05 Ω)			
MIL-STD-202		No visible damage	<50mΩ		
method 106					
Bias Humidity	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	Δ R/R max. \pm (1.0%+0.05 Ω)			
MIL-STD-202		No visible damage	<50mΩ		
method 103					
Operational Life	1000+48/-0 hours; 35% of operation power, 125±2°C	Δ R/R max. \pm (1.0%+0.05 Ω)			
MIL-STD-202 method		No visible damage	$<$ 50m Ω		
108					

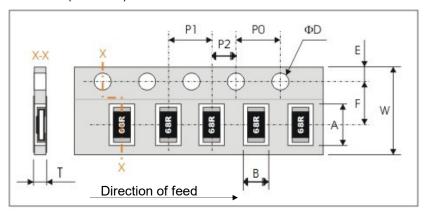
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TEAT	DDOGEDLIDE / TEGT METUOD	REQUIREMENT	REQUIREMENT			
TEST	PROCEDURE / TEST METHOD	Resistor	0Ω			
High Temperature	1000+48/-0 hours; without load in a temperature chamber	Δ R/R max. ±(1.0%+0.05Ω)				
Exposure	controlled 155±3°C	No visible damage	50 0			
MIL-STD-202			<50mΩ			
method 108						
Mechanical Shock	1/2 Sine Pulse / 1500g Peak / Velocity 15.4ft/sec	Within the specified				
MIL-STD-202		tolerance	<50mΩ			
method 213		No visible damage				
Board Flex	Resistors mounted on a 90mm glass epoxy resin PCB(FR4),	ΔR/R max.				
AEC-Q200-005	bending once 2mm for 10sec	\pm (1.0%+0.05Ω).	<50mΩ			
		No visible damage				
Terminal strength	Pressurizing force: 1Kg, Test time: 60±1sec.	No remarkable damage or remova				
AEC-Q200-006		of the terminations				
Vibration	Test 5g's for 20min., 12 cycles each of 3 orientations	Δ R/R max. \pm (1.0%+0.05 Ω)				
MIL-STD-202	派所有 信~	No visible damage	<50mΩ			
method 204	THE PLANT OF THE PARTY OF THE P					
Thermal shock	Test –55 to 155 $^{\circ}\text{C}$ / dwell time 15min/ Max transfer time 20sec	Δ R/R max. \pm (0.5%+0.05 Ω)				
MIL-STD-202	300cycles	No visible damage	<50mΩ			
method 107	## V Y					
ESD	Test contact 1.0KV (0.5KV for 0402 only)	Δ R/R max. \pm (1.0%+0.05 Ω)	<50mΩ			
AEC-Q200-002	PASSIVE SYSTEM ALLIANCE	No visible damage	\ 0011122			
Sulfuration test	105'C 1000hrs against EIA-977-2016 condition B	Δ R/R max. \pm (2.0%+0.05 Ω)	<50mΩ			
ASTM B-809		No visible damage	\J011182			

PACKAGING

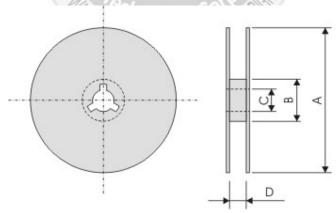
Paper Tape specifications (unit :mm)



Series No.	А	В	W	F	Е
SR10	3.60±0.20	3.00±0.20			
SR12	3.60±0.20	2.00±0.20	13		
SR08	2.40±0.20	1.65±0.20	8.00±0.30	3.50 ± 0.20	1.75±0.10
SR06	1.90±0.20	1.10±0.20	12 21		
SR04	1.20±0.10	0.70±0.10	19 M 76		'

Series No.	P1	P0	P2	ΦD	Т
SR10/12/08	4.00±0.10	1.677	Ñ		0.80±0.1
SR06	4.00±0.10	4.00±0.10	P2=1/2P0	Φ 1.50 $^{+0.1}_{-0.0}$	0.70±0.05
SR04	2.00±0.10	5		と同	0.50±0.05

Reel dimensions (unit :mm)



Symbol	А	В	С	D
7" reel	Ф178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5
10" reel	Ф254.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5
13" reel	Ф330.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5

单击下面可查看定价,库存,交付和生命周期等信息

>>Walsin Technology(华新科技)