

APPROVAL SHEET

WK25S WK20S WK10S WK12S WK08S WK06S

±5%, ±10%, ±20%

Thick Film Power Surge Chip Resistors AEC Q-200 Qualified Size 2512, 2010, 1210, 1206, 0805, 0603

*Contents in this sheet are subject to change without prior notice.



FEATURE

- 1. Power rating and compact size
- 2. High reliability and stability
- 3. Reduced size of final equipment
- 4. High anti-surge protection
- 5. Halogen free
- 6. Automotive grade AEC Q-200 qualified

APPLICATION

- Power supply
- Measurement instrument
- Automotive industry
- Medical or Military equipment

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to request resistance to nominal value within tolerance which controlled by printing process in this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (Pb free) alloy.

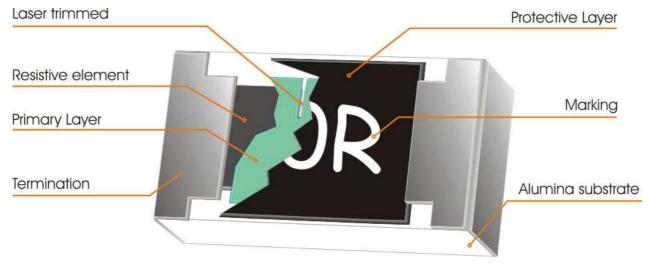


Fig 1. Construction of Chip-R WKxxS



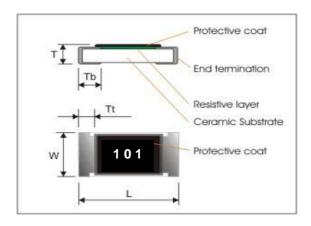
QUICK REFERENCE DATA

Item	General Specification					
Series No.	WK25S	WK20S	WK10S	WK12S	WK08S	WK06S
Size code	2512(6432)	2010 (5025)	1210 (3225)	1206 (3216)	0805 (2012)	0603 (1608)
Resistance Tolerance			±5% :	±10% ±20% (E2	4)	
Resistance Range			0.27Ω ~ 22MΩ	Ω		1Ω ~ 1ΜΩ
TCR (ppm/°C)						
1.1ΜΩ ~ 22ΜΩ		±200 ppm /°C				
1Ω ~ 1ΜΩ	± 100 ppm /°C					1 - 9.1 Ω : ± 200 ppm
$0.27\Omega \sim 0.91\Omega$		± 200 ppm /°C				
Max. dissipation at T _{amb} =70°C	1 W	3/4W	1/2W	1/3W	1/4W	1/4W
Max. Operation Voltage	200V	150V				
Max. Overload Voltage	400V	400V	400V	400V	300V	300V
Operation temperature	- 55 ~ +155'C					

Note:

- This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by $RCWV = \sqrt{Rated Power \times Resistance Value} \quad \text{or Max. RCWV listed above, whichever is lower.}$

MECHANICAL DATA(unit: mm)



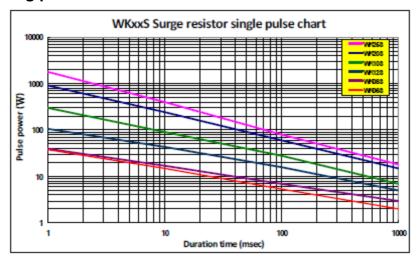
Symbol	WK25S	WK20S	WK10S	WK12S	WK08S	WK06S
L	6.30±0.15	5.00±0.15	3.10±0.15	3.10±0.15	2.00 ± 0.10	1.60 ± 0.10
W	3.20±0.15	2.50±0.15	2.50±0.15	1.60±0.15	1.25 ± 0.10	0.80 +0.15/-0.05
Т	0.55±0.15	0.55±0.15	0.55±0.15	0.55±0.10	0.55 ± 0.10	0.45 ± 0.10
Tt	0.30±0.15	0.30±0.15	0.30±0.20	0.30±0.20	0.30 ± 0.20	0.30 ± 0.20
Tb	0.60±0.20	0.60±0.20	0.50±0.25	0.50±0.25	0.40 ± 0.20	0.30 ± 0.10



MARKING

1. Each resistor is marked with a three-digit code on the protective coating to designate the nominal resistance value.

Single pulse limiting power chart



De-rating curve

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

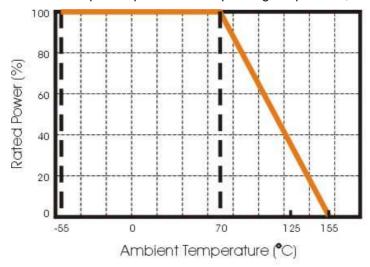


Fig.2 Maximum dissipation in percentage of rated power As a function of the ambient temperature



MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

SOLDERING CONDITION

robust construction resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C 30 Typical for seconds. examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

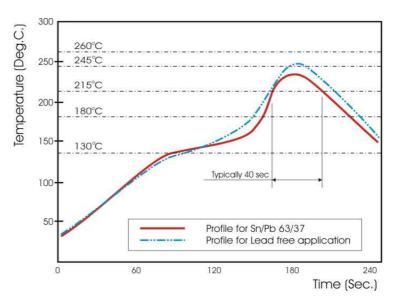


Fig 3. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

WK12	s	472_	J	Т	L	J
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination code	Special code
WK25 : 2512	S : surge	J	J: ±5%	T:7"Reeled	L = Sn base (lead	J = Automotive
WK20 : 2010		digits followed by no. of zeros and a	K: ±10%	taping	free)	grade AEC Q200 Compliant
WK10 : 1210		blank	M: ±20%			
WK12 : 1206		472 = 4K7				
WK08:0805						
WK06:0603						

Taping quantity

- 1210, 1206, 0805, 0603 Chip resistors 5,000 pcs paper tape per reel.
- 2512, 2010 Chip resistors 4,000 pcs PC tape per reel.



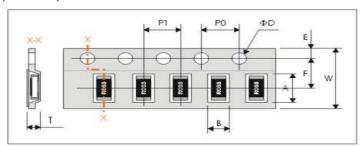
TEST AND REQUIREMENTS(JIS C 5201-1: 1998)

TEST	PROCEDURE	REQUIREMENT
Temperature Coefficient of Resistance(T.C.R) Clause 4.8	Natural resistance change per change in degree centigrade. $\frac{R_2-R_1}{R_1(t_2-t_1)}\times 10^6 \ \ (\text{ppm/°C}) \ \ t_1:20\text{C}+5\text{C-1C}$	Refer to "QUICK REFERENCE DATA"
	R ₁ : Resistance at reference temperature	
	R ₂ : Resistance at test temperature	
Short time overload (S.T.O.L) Clause 4.13	Permanent resistance change after a 2 second application of a voltage 2.5 times RCWV or the maximum overload voltage specified in the above list, whichever is less.	Δ R/R max. \pm (1%+0.05 Ω) no visible damage
	Un-mounted chips completely immersed for 10±1second in a	no visible damage
Resistance to soldering heat(R.S.H) AEC Q200-15	solder bath at 260°C±5°C	Δ R/R max. ±(1%+0.05 Ω)
Solderability	Baked 155'C x 4hrs/ Un-mounted chips completely	good tinning (>95% covered)
AEC Q200-18	immersed for 2±0.5second in a solder bath at 235°C±5°C	no visible damage
Temperature cycling	30 minutes at -55°C±3°C, 2~3 minutes at 20℃+5℃-1℃, 30	no visible damage
AEC Q200-4	minutes at +155°C±3°C, 2~3 minutes at 20℃+5℃-1℃, total 1000 cycles	Δ R/R max. ±(1%+0.05 Ω)
Operational Life	1000+48/-0 hours; 35% of operation power, 125±2°C	Δ R/R max. \pm (3%+0.1 Ω)
MIL-STD-202		no visible damage
method 108		
Bias Humidity AEC Q200-7	1000 hours, at 10% rated continuous power in humidity chamber controller at 85°C±2°C and 85% relative humidity,	Δ R/R max. \pm (3%+0.1 Ω)
AEC Q200-7	onamber controller at co one of and copy rotation numbers,	no visible damage
High temperature	155 °C, no load, 1000hours	Δ R/R max. \pm (3%+0.1 Ω)
exposure AEC Q200-3		no visible damage
Bending strength CAEC Q200-21	Resistors mounted on a 90mm glass epoxy resin PCB(FR4); bending : 2 mm, once for 10 seconds	Δ R/R max. ±(1%+0.05 Ω)
Adhesion	Pressurizing force: 20N, Test time: 60±1sec.	No remarkable damage or
AEC Q200-22		removal of the terminations
Insulation Resistance	Apply the maximum overload voltage (DC) for 1minute	R≥1GΩ
Clause 4.6		
Dielectric Withstand	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flashover
Voltage	-	
Clause 4.7		



PACKAGING

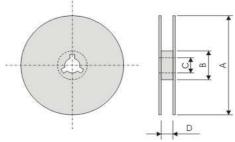
Tape specifications (unit :mm)



Series No.	А	В	W	F	E
WK25S	6.90±0.20	3.60±0.20	12.00±0.30	5.50±0.05	1.75±0.10
WK20S	5.50±0.20	3.100±0.20	12.00±0.30	3.30±0.03	1.75±0.10
WK10S	3.50±0.20	2.85±0.20	8.00±0.30	3.50±0.05	1.75±0.10
WK12S	3.60±0.20	2.00±0.15	8.00±0.20	3.50±0.05	1.75±0.10
WK08S	2.50±0.20	1.65±0.15	0.00±0.20	0.00±0.00	1.7020.10
WK06S	1.90±0.20	1.15±0.15	8.00±0.30	3.50±0.05	1.75±0.10

Series No.	P1	P0	ΦD	Т
WK25S		4.00±0.10	Ф1.50 ^{+0.1}	1.1±0.15
WK20S				1.1±0.15
WK10S	4.00 - 0.40			1.0±0.20
WK12S	4.00±0.10			Max. 1.0
WK08S				Max. 1.0
WK06S				Max. 0.8

Reel dimensions



Reel / Tape	А	В	С	D
7" reel for 8mm tape	Ф180.0+0/-1.5	Φ60.0+1/0	13.0±0.2	9+1.0/0
7" reel for 12mm tape	Ф180.0+0/-1.5	Ф60.0+1/0	13.0±0.2	13+1.0/0

Taping quantity

- 1210, 1206, 0805, 0603 Chip resistors 5,000 pcs paper tape per reel.
- 2010, 2512 Chip resistors 4,000 pcs plastic tape per reel.

单击下面可查看定价,库存,交付和生命周期等信息

- >>Walsin Technology(华新科技(华科))
- >>点击查看相关商品