

APPROVAL SHEET

WR12, WR08, WR06, WR04

±1%, ±5%

Thick film general purpose chip resistors Size 1206, 0805, 0603, 0402 (Automotive & Anti-sulfur) RoHS 2 Compliant with exemption 7C-I Halogen free

*Contents in this sheet are subject to change without prior notice.

FEATURE

- 1. Automotive grade AEC Q-200 compliant
- 2. Anti-sulfuration ASTM B-809 105℃ x 750hrs compliant
- 3. 100% CCD inspection
- 4. RoHS 2 Compliant with exemption 7C-I and Halogen free

APPLICATION

- Automotive application
- Consumer electrical equipment
- EDP, Computer application
- Telecom application

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

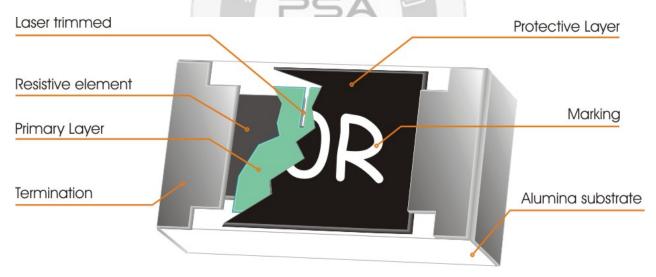


Fig 1. Construction of Chip-R



QUICK REFERENCE DATA

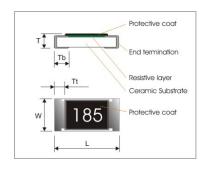
Item		General Specification							
Series No.	WR1	2	WR	WR08		6	WR0	4	
Size code	1206(32	216)	0805(2	(012)	0603(16	608)	0402(10	005)	
Resistance Range			1Ω~1	0MΩ (±5% to	lerance), Jump	er			
			1	Ω~10MΩ (±1	% tolerance)				
Resistance	±1%	±5%	±1%	±5%	±1%	±5%	±1%	±5%	
Tolerance	E96/E24	E24	E96/E24	E24	E96/E24	E24	E96/E24	E24	
TCR (ppm/°C)									
R>1MΩ	≤ ± 20	00	≤ ± 200		≤ ± 200		≤ ± 200		
$10\Omega < R \le 1M\Omega$	≤ ± 10	00	≤ ± 100		≤ ± 100		≤ ± 100		
R≦ 10Ω	-200~+	400	-200~+	- 400	-200~+400		-200~+400		
Max. dissipation @ T _{amb} =70°C	1/4 V	V	1/8 W		1/10 W		1/16 W		
Max. Operation Voltage (DC or RMS)	200\	200V		150V		75V		50V	
Max. Overload Voltage (DC or RMS)	400\	粉页多排	300V		150\	′	100\	′	
Climatic category		PASSIVE SYSTEM ALLIS5/155/56							

Note:

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115.8"
- 2. Max. Operation Voltage: So called RCWV (Rated Continuous Working Voltage) is determined by
- 3. $RCWV = \sqrt{Rated Power \times Resistance Value}$ or Max. RCWV listed above, whichever is lower.
- 4. The resistance of Jumper is defined $< 0.05\Omega$.

DIMENSIONS (unit: mm)

	WR12	WR08	WR06	WR04
L	3.10 ± 0.10	2.00 ± 0.10	1.60 ± 0.10	1.00 ± 0.05
w	1.60 ± 0.10	1.25 ± 0.10	0.80 ± 0.10	0.50 ± 0.05
Т	0.60 ± 0.15	0.50 ± 0.15	0.45 ± 0.15	0.35 ± 0.05
Tb	0.45 ± 0.20	0.40 ± 0.20	0.30 ± 0.15	0.25 ± 0.10
Tt	0.50 ± 0.20	0.40 ± 0.20	0.30 ± 0.10	0.20 ± 0.10



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MARKING

Size \ Nr. Of digit of code\tolerance	±5% & Jumper(0Ω)	±1%
1206/ 0805	3-digits marking	4-digits marking
0603 (1608)	3-digits marking	3-digits marking
0402(1005)	N0 MA	RKING

3-digits marking [$\pm 5\%$: 1206, 1210, 0805 & 0603 & Jumper (0 Ω)]

Each resistor is marked with a three digits code on the protective coating to designate the nominal resistance value.

3-digits marking ($\pm 1\%$: 0603)

Nominal	l resistan	се			Description										
1.E-24 s	series			As <i>0603</i>	0603 WR06X ±5%.										
2.E-96 s	series			The 1st t	two digit o	codes ar	re referrin	g to the	CODE or	the tab	ole, the 3rd	d code is	s the inde	x of resi	stance
				Y=10 ⁻² ,	X=10 ⁻¹ ,	A=10 ⁰ ,	B=10 ¹ ,	C=10 ² ,	$D=10^3$,	E=10 ⁴ ,	F=10 ⁵				
						EX:	口口	13	⁄8Ω=25A						
0.0				<u>. </u>	X	98 LT		Alexandra (78K=25D						
3. Rema			1		- 11V		ne items a	775							
	R_value		R_value	7.3	R_Value		R_value		R_value	1277	R_value	CODE	R_value		R_value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

4-digits marking (±1%: 1206/0805)

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value.

Example

RESISTANCE	Jumper (0Ω)	10Ω	12Ω	100Ω	6800Ω	47000Ω
3-digits marking (1206 & 0805 & 0603 ±5%, & Jumper)	000	100	120	101	682	473
4-digits marking	-	10R0	12R0	1000	6801	4702

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FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E24 series for resistors with a tolerance of $\pm 5\%$, and E96 series for resistors with a tolerance of $\pm 1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063"

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

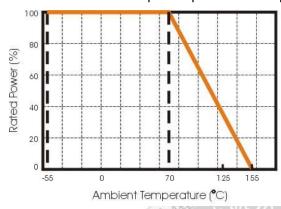


Figure 2 Maximum dissipation in percentage of rated power as a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.



SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

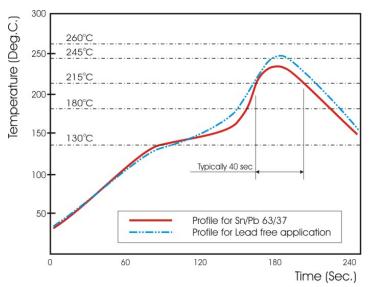


Fig 3. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

WR12	X	472_	J	Т	L	Z
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination	Special code Z
WR12: 1206 WR08: 0805 WR06: 0603 WR04: 0402	X: ±5%, 1Ω ~ 10MΩ ±1%, 10Ω ~ 1MΩ $W:$ ±1%, < 10Ω; >1MΩ	$\pm 5\%$, E24: 2 significant digits followed by no. of zeros and a blank 4.7Ω =4R7_ 10Ω =100_ 220Ω =221_ 20Ω Jumper =000_ ("_" means a blank) $\pm 1\%$: E24+E96: 3 significant digits followed by no. of zeros 102Ω =1020 $37.4K\Omega$ =3742	F:±1% J:±5% P:Jumper	T: 7" Reeled taping Q: 10" Reeled taping G: 13" Reeled taping B: Bulk H: 13" reel 50Kpcs only for 0402 D: 7" reel 20Kpcs only for 0402 A: 7" reel 15Kpcs only for 0402	code L= Sn base (lead free)	 1. 100% CCD visual check 2. Meet AEC Q-200 test 3. Anti-sulfur ASTM B-809 105°C x 750hrs

WR12,WR08,WR06:

1. Reeled tape packaging: 8mm width paper taping 5000pcs per 7" reel, 10kpcs per 10" reel, 20kpcs per 13" reel.

2. Bulk packaging : 5000pcs per poly-bag

WR04:

1. Reeled tape packaging: 8mm width paper taping 10,000pcs per reel,

2. Bulk packaging : 10,000pcs per poly-bag

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TEST AND REQUIREMENTS

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied:

Temperature: 15°C to 35°C. Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar). All soldering tests are performed with midly activated flux.

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
1531	PROCEDURE / TEST METHOD	Resistor	0Ω
Electrical	- DC resistance values measurement	Within the specified tolerance	
Characteristics	- Temperature Coefficient of Resistance (T.C.R)	Refer to "QUICK	
JISC5201-1: 1998	Natural resistance change per change in degree centigrade.	REFERENCE DATA"	
Clause 4.8	$ \frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)} t_1 : 20^{\circ}\text{C} + 5^{\circ}\text{C} - 1^{\circ}\text{C} $	3	<50mΩ
	R ₁ : Resistance at reference temperature		
	R ₂ : Resistance at test temperature		
Resistance to soldering	Un-mounted chips completely immersed for	Δ R/R max. ±(1.0%+0.05 Ω)	
heat(R.S.H)	10±1second in a SAC solder bath at 270°C ±5°C	no visible damage	<50mΩ
MIL-STD-202 method 210	Corporate Corporate		
Solderability J-STD-002	 a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C/ 5sec. b) Steam the sample dwell time 1 hour/ solder dipping 260°C/ 7sec. 	95% coverage min., good tinnii visible damage	ng and no
Temperature cycling	1000 cycles, -55° C ~ +155 $^{\circ}$ C, dwell time 5~10min	ΔR/R max. ±(1.0%+0.05Ω)	
JESD22		No visible damage	<50mΩ
Method JA-104			
Moisture Resistance MIL-STD-202	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	Δ R/R max. \pm (1.0%+0.05 Ω) No visible damage	<50mΩ
method 106			
Bias Humidity	1000+48/-0 hours; 85°C, 85% RH, 10% of operation	Δ R/R max. ±(2.0%+0.10 Ω)	
MIL-STD-202	power	No visible damage	<50mΩ
method 103			
Operational Life	1000+48/-0 hours; 35% of operation power, 125±2°C	AD/D may 1/2 00/ 10 10)	
MIL-STD-202 method		Δ R/R max. \pm (2.0%+0.1 Ω) No visible damage	<50mΩ
108		INO VISIDIE GAITIAGE	

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TEOT	PROCEDURE / TEST METUOR	REQUIREMENT	
TEST	PROCEDURE / TEST METHOD	Resistor	0Ω
High Temperature Exposure MIL-STD-202 Method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	$\Delta R/R$ max. $\pm (2.0\% + 0.10\Omega)$ No visible damage	<50mΩ
Board Flex AEC-Q200-005	Resistors mounted on a 90mm glass epoxy resin PCB(FR4), bending once 2mm for 10sec.	Δ R/R max. \pm (1.0%+0.05 Ω) No visible damage	<50mΩ
Terminal strength AEC-Q200-006	Pressurizing force: 1Kg, Test time: 60±1sec.	No remarkable damage or rem	oval of
Thermal shock MIL-STD-202 method 107	Test –55 to 155°C / dwell time 15min/ Max transfer time 20sec 300cycles	Δ R/R max. \pm (1.0%+0.05 Ω) No visible damage	<50mΩ
ESD AEC-Q200-002	Test contact 1.0KV (0.5KV for 0402 only)	Δ R/R max. \pm (1.0%+0.05 Ω) No visible damage	<50mΩ
Short Time Overload JISC5201-1: 1998 Clause 4.13	2.5 times RCWV or max. overload voltage, for 5seconds	Δ R/R max. \pm (2.0%+0.10 Ω) No visible damage	<50mΩ
Load life in Humidity JISC5201-1: 1998 Clause 4.24	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	Δ R/R max. \pm (2%+0.10 Ω) No visible damage	<50mΩ
Load life (endurance) JISC5201-1: 1998 Clause 4.25	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller 70±2°C, 1.5 hours on and 0.5 hours off	Δ R/R max. \pm (2%+0.10 Ω) No visible damage	<50mΩ
Sulfuration test	ASTM B-809 105'C, 750hrs	Δ R/R max. \pm (2.0%+0.05) No visible damage	<50mΩ

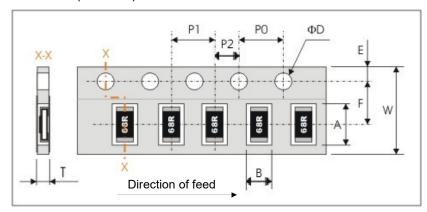
TEST CONDITION FOR JUMPER (0 Ω)

Item	WR12	WR08	WR06	WR04			
Power Rating At 70°C	1/4W	1/8W	1/10W	1/16W			
Resistance	Max. 50mohm						
Rated Current	2A	1.5A	1A	1A			
Peak Current	5A	3.5A	3A	1.5A			
Operating Temperature	-55~155°C						

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PACKAGING

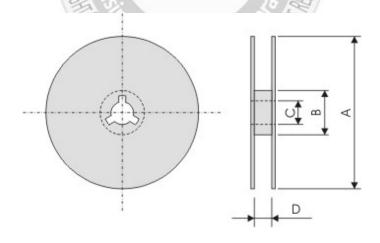
Paper Tape specifications (unit :mm)



Series No.	Α	В	W	F	E
WR12	3.60±0.20	2.00±0.20			
WR08	2.40±0.20	1.65±0.20	9.00+0.20	3.50+0.20	1.75+0.10
WR06	1.90±0.20	1.10±0.20	8.00±0.30	3.50±0.20	1.75±0.10
WR04	1.20±0.10	0.70±0.10	2 16		

Series No.	P1	P0	P2	ΦD	T
WR12 / WR08	4.00+0.40		$\nabla \setminus S$		Max. 1.0
WR06	4.00±0.10	4.00±0.10	2.00±0.10	Φ 1.50 $^{+0.1}_{-0.0}$	0.65±0.05
WR04	2.00±0.10				0.40±0.05

Reel dimensions



Symbol	А	В	С	D
7" reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5
10" reel	Ф254.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5
13" reel	Ф330.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5

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单击下面可查看定价,库存,交付和生命周期等信息

>>Walsin Technology(华新科技)