

APPROVAL SHEET

SR20X, SR25X

±1%, ±5%

Power chip resistors

Size 2010, 2512

Automotive & Anti-Sulfuration



FEATURE

- 1. High reliability and stability ±1%
- 2. Sulfuration resistant ASTM B-809-95 compliant
- 3. Automotive grade AEC Q-200 Compliant
- 4. Higher component and equipment reliability
- 5. RoHS compliant Lead free products

APPLICATION

- Power supply
- Industry
- Motor control
- M/B Computer
- Automotives
- Server

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. **The extra protective metal film is added onto top side electrodes to protect termination from sulfuration**. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

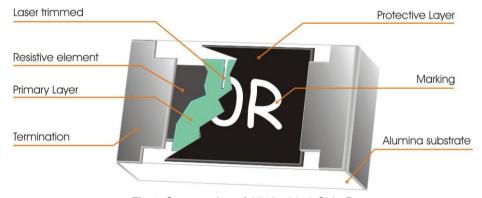


Fig 1. Construction of 2512, 2010 Chip-R



QUICK REFERENCE DATA

Item	General Specification			
Series No.	SR20	SR25		
Size code	2010 (5025),	2512(6432)		
Resistance Tolerance	±5% (E24); ±	1% (E24+E96)		
Resistance Range	$1\Omega \sim 10 M\Omega$, Jumper (0Ω)			
TCR (ppm/ $^{\circ}$ C) < 10 Ω	± 200 ppm/°C			
10Ω ~ 1ΜΩ	± 100 ppm/°C			
> 1MΩ	± 200 ppm/°C			
Max. dissipation at T _{amb} =70°C	0.75 W 1W			
Max. Operation Voltage (DC or RMS)	200V 250V			
Max. Overload Voltage (DC or RMS)	400V 500V		ltage (DC or RMS) 400V 500V	
Operation temperature	-55 ~ +155'C			

Test conditions for jumper (0 ohm)

Туре	SR20X	SR25X
Power Rating At 70C	0.75 W	1 W
Resistance	Max. 50mR	Max. 50mR
Rated Current	3.2 A	4.5 A
Peak Current	8 A	11 A
Operating Temperature	-55C ~ 155C	-55C ~ 155C

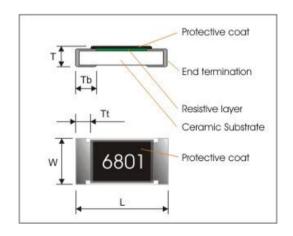
Note:

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage: So called RCWV (Rated Continuous Working Voltage) is determined by

 $RCWV = \sqrt{RatedPower \times Resistance \ Value}$ or Max. RCWV listed above, whichever is lower.

MECHANICAL DATA (unit : mm)

TYPE	SR20	SR25
L	5.00±0.20	6.40±0.20
W	2.50±0.20	3.20±0.20
Т	0.55±0.10	0.60±0.10
Tt	0.65±0.25	0.65±0.25
Tb	0.60±0.25	0.90±0.25





MARKING

Each resistor is marked with a four-digit code on the protective coating to designate the nominal resistance value ±5%, ±1% tolerance!

Size	±5%	±1%	
2512, 2010	4-digits marking	4-digits marking	

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E96 & E24 series for resistors with a tolerance of $\pm 5\%$ & $\pm 1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063".

Derating curve

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

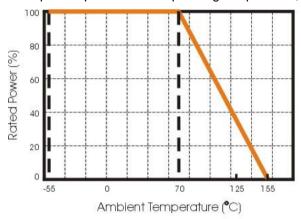


Fig.2 Maximum dissipation in percentage of rated power As a function of the ambient temperature

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

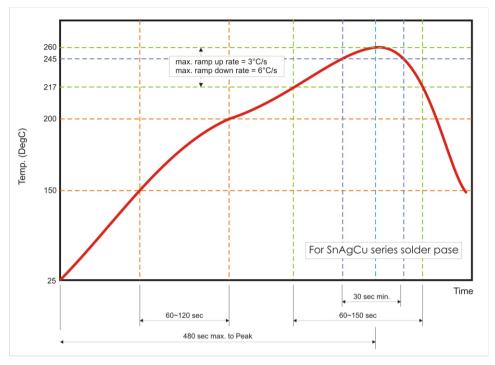


Fig 3. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

SR25	x	472_	J	Т	L
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination
SR25 : 2512 SR20 : 2010	X: $\pm 5\%$: 1Ω - 10MΩ $\pm 1\%$: 10Ω - 1MΩ W: $\pm 1\%$: <10Ω; > 1MΩ	E24: 2 significant digits followed by no. of zeros $100\Omega = 101_$ $10K\Omega = 103$ E96: 3 significant digits followed by no. of zeros	F:±1% J:±5% P:Jumper	T: 7" Reeled taping Q: 10" Reeled taping G: 13" Reeled taping B: Bulk	code L = Sn base (lead free)
		$102\Omega = 1020$ $37.4K\Omega = 3742$			

^{*} Anti-Sulfuration test conditions: ASTM B-809-95 105'C, 750hrs! Acceptance criteria +/-2%!

^{* 100%} CCD visual inspection to guarantee visual quality.



TEST AND REQUIREMENTS (AEC Q-200)

TEOT	DDGGEDUDE (TEGT METUOD	REQUIREMENT		
TEST	PROCEDURE / TEST METHOD	Resistor	0Ω	
Electrical	- DC resistance values measurement	Within the specified tolerance	N/A	
Characteristics	- Temperature Coefficient of Resistance (T.C.R)	Refer to "QUICK REFERENCE		
	Natural resistance change per change in degree	DATA"		
JISC5201-1: 1998	centigrade.			
Clause 4.8	$\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)} t_1 : 20\text{°C+5°C-1°C}$			
	R ₁ : Resistance at reference temperature			
	R ₂ : Resistance at test temperature			
Resistance to	Un-mounted chips completely immersed for			
soldering heat(R.S.H)	10±1second in a SAC solder bath at 270°C±5°C	Δ R/R max. \pm (0.5%+0.05 Ω)	<50mΩ	
MIL-STD-202 method 210		no visible damage		
Solderability J-STD-002	 a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C/ 5sec. b) Steam the sample dwell time 8 hour/ solder dipping 215°C/ 5sec. c) Steam the sample dwell time 8 hour/ solder 	95% coverage min., good tinning and no visible damage		
	dipping 260°C/ 7sec.		1	
Temperature cycling	1000 cycles, -55°C ~ +155°C, dwell time 30min	Δ R/R max. ±(0.5%+0.05Ω)	$<$ 50m Ω	
JESD22	maximum.	no visible damage		
Method JA-104				
Moisture Resistance MIL-STD-202	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	Δ R/R max. \pm (0.5%+0.05 Ω) No visible damage	<50mΩ	
method 106				
Bias Humidity	1000+48/-0 hours; 85°C, 85% RH, 10% of operation	Δ R/R max. ±(1.0%+0.05 Ω)	$<$ 50m Ω	
MIL-STD-202	power	No visible damage		
method 103				
Operational Life	1000+48/-0 hours; 35% of operation power,	Δ R/R max. \pm (1.0%+0.05 Ω)		
MIL-STD-202	125±2°C	No visible damage	$<$ 50m Ω	
method 108		The vicinit damage		
High Temperature	1000+48/-0 hours; without load in a temperature	Δ R/R max. \pm (1%+0.05 Ω)	$<$ 50m Ω	
Exposure	chamber controlled 155±3°C	No visible damage		
MIL-STD-202				
Method 108				
Board Flex	Resistors mounted on a 90mm glass epoxy resin	Δ R/R max. ±(1.0%+0.05Ω)	$<$ 50m Ω	
AEC-Q200-005	PCB(FR4),bending once 2mm for 60sec.	No visible damage		
Terminal strength AEC-Q200-006	Pressurizing force: 1.8Kg, Test time: 60±1sec.	No remarkable damage or remove terminations	al of the	
Thermal shock	Test –55 to 155°C / dwell time 15min/ Max transfer	Δ R/R max. ±(0.5%+0.05Ω)	<50mΩ	
MIL-STD-202	time 20sec 300cycles	No visible damage		
method 107	,			
ESD	Test contact 3KV.	Δ R/R max. ±(1.0%+0.05Ω)	<50mΩ	
AEC-Q200-002		No visible damage		

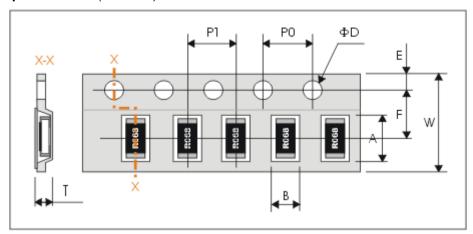


Mechanical Shock MIL-STD-202 method 213	Test ½ Sine Pulse, Peak value: 100g, normal duration: 6ms, Velocity change:12.3ft/sec. Three shocks in each direction, total 18 shocks.	Within product specification tolerance and no visible damage.	<50mΩ
Vibration MIL-STD-202 method 204	Test 5g's for 20 min., 12 cycles each of 3 orientations.	\triangle R/R max. \pm (1%+0.05 Ω) and no visible damage.	<50mΩ
Resistance to Solvents : MIL-STD- 202 Method 215	Solvent is Isopropyl alcohol, immersion 3mins at 25 °C and brush 10 strokes with a toothbrush with a handle made of a non-reactive material (wet bristle), immersion and brush 3 times and then air blow dry.	No superficial defect on marking, encapsulation, coating, appearance. Electrical characteristics within products specification and tolerance. Inspect at 3X max. for marking, inspect at 10X for part damage.	<50mΩ
External Visual MIL-STD-883 method 2009	Electrical test not required. Inspect device construction, marking and workmanship	No visual damage and refer WTC code.	marking
Physical Dimension JESD22 method JB-100	Verify physical dimensions(L, W, T, Tb, Tt)	Within the specified tolerance for	WTC.



PACKAGING

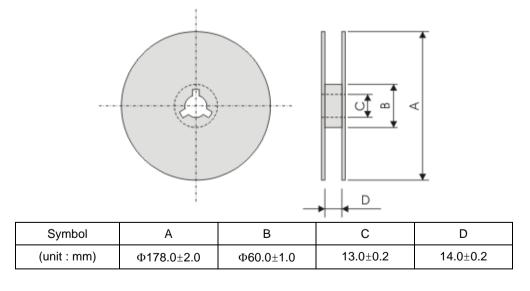
Plastic Tape specifications (unit :mm)



Type	Α	В	W	F	E
SR25	6.90±0.20	3.60±0.20	12.00±0.30	5.50±0.10	1.75±0.10
SR20	5.50±0.20	2.80±0.20	12.00±0.30	5.50±0.10	1.75±0.10

Туре	P1	P0	ΦD	Т
SR25	4.00±0.10	4.00±0.10	Ф1.50 ^{+0.1}	MAX1.2
SR20	4.00±0.10	4.00±0.10	Ψ1.50 _{-0.0}	IVIAA 1.2

Reel dimensions



Taping quantity

SR20, SR25 by plastic tape taping 4,000 pcs per reel.

单击下面可查看定价,库存,交付和生命周期等信息

>>Walsin Technology(华新科技)