

# APPROVAL SHEET

# **WW25R**

±1%, ±5%, 2W

Metal Plate Low Ohm Power Chip Resistors

Size 2512 (6432)

RoHS Exemption free and Lead free products

Halogen free

**Current Sensing Type** 

\*Contents in this sheet are subject to change without prior notice.

#### **FEATURE**

- 1. Ultra low and stable TCR performance
- 2. High power rating and compact size
- 3. High reliability and stability
- 4. Reduced size of final equipment
- 5. RoHS exemption free and Lead free product
- 6. Low inductance below 3nH

#### **APPLICATION**

- Power supply
- PDA
- Digital meter
- Computer
- Automotives
- Battery charger
- DC-DC power converter

#### **DESCRIPTION**

The resistors are constructed in a **high grade low resistive metal body**. The resistive layer is covered with a protective coat and printed a resistance marking code over it. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Lead-free terminations.

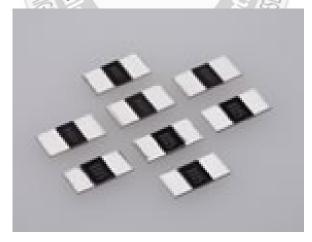


Fig 1. Construction of Chip-R

## **QUICK REFERENCE DATA**

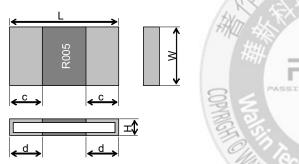
Item	General Specification
Series No.	WW25R
Size code	2512 ( 6432 )
Resistance Tolerance	±5%, ±1%
Resistance Value	$0.001\Omega,0.002\Omega,0.003\Omega,0.004\Omega,0.005\Omega,0.006\Omega,0.007\Omega,0.008\Omega,0.009\Omega,0.010\Omega$
TCR (ppm/°C)	≤ ±75 ppm/°C
Max. dissipation at T <sub>amb</sub> =70°C	2 W
Max. Operation Current (DC or RMS)	44.7A, 31.6A, 25.8A, 22.3A, 20A, 18.2A, 16.9A, 15.8A, 14.9A, 14.1A
Operation temperature	-55 ~ +155°C

#### Note:

1. Max. Operation Current : So called RCWC (Rated Continuous Working Current) is determined by

 $RCWC = \sqrt{Rated Power / Resistance Value}$ 

## **MECHANICAL DATA**



Туре	Size(inch)	Resistance	L(mm)	W(mm)	H(mm)	c(mm)	d(mm)
		1mΩ	CHNOLOGY	3.2±0.25	0.38±0.15		
		2mΩ	3mΩ 4mΩ 5mΩ 6.3±0.25 7mΩ 8mΩ	3.1±0.25	0.58±0.15	2.2±0.25	0.25
		3mΩ			0.45±0.15		0.25
		4mΩ			0.34±0.15		
\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	WW25R 2512	5mΩ			0.51±0.15	1.1±	0.25
VVVVZJI		6mΩ			0.5±0.15	1.1±	0.25
		7mΩ			0.5±0.15	0.6±	0.25
		8mΩ			0.35±0.15	1.1±	0.25
		9mΩ			0.35±0.15	0.8±	0.25
		10mΩ			0.35±0.15	0.5±	0.25

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#### **MARKING**

Each resistor is marked with a four-digit code on the protective coating to designate the nominal resistance value.

 $R005 = 5 \text{ m}\Omega$   $R001 = 1 \text{ m}\Omega$ 

# **FUNCTIONAL DESCRIPTION**

#### **Derating curve**

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

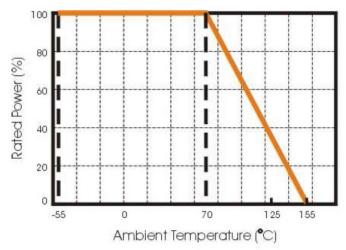


Fig.2 Maximum dissipation in percentage of rated power As a function of the ambient temperature

#### **MOUNTING**

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.



#### **SOLDERING CONDITION**

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 3 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

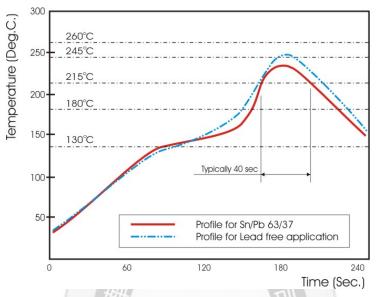


Fig 3. Infrared soldering profile for Chip Resistors WW25

#### **CATALOGUE NUMBERS**

The resistors have a catalogue number starting with .

WW25	R	R001	F	Т	L
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination code
WW25 : 2512	R: 2W Sensing type	R is first digit followed by 3 significant digits. $0.001\Omega = R001$ $0.005\Omega = R005$ $0.010\Omega = R010$	J:±5% F:±1%	T:7" reel in tape	L = Sn base (lead free)

Reeled tape packaging : 12mm width plastic emboss taping 4,000pcs per reel.

## **TEST AND REQUIREMENTS(JIS C 5201-1: 1998)**

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied:

Temperature: 15°C to 35°C. Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar). All soldering tests are performed with midly activated flux.

TEST	PROCEDURE				REQUIREMENT	
	Resistance value shall be measured by mounting the substrate of the following condition.  Current terminal Current terminal Copper clad Voltage terminal Solder resist					
Dimension		20	32. 87	Un	it:mm	
Resistance	Style	Resistance $value(m\Omega)$	а	b	С	The resistance should be within specified tolerance per resistance value!
Clause 4.4.2	WW25R	2 to 4 5 to 10	3.5			
	Thickness of copper clad: 0.035mm 4-Terminal method Measurement current: 1(A) Note: The measuring apparatus corresponding to DC Low-ohm Mater (1A) of AX-1152D for ADEX CORPORATION.					
Temperature Coefficient of Resistance(T.C.R)  Clause 4.8	Natural resistance change per change in degree centigrade. $\frac{R_2-R_1}{R_1\big(t_2-t_1\big)}\times 10^6 \ \ (\text{ppm/°C})  t_1: 20^{\circ}\text{C+5°C-1°C}$ $\text{R}_1: \text{Resistance at reference temperature}$ $\text{R}_2: \text{Resistance at test temperature}$				Refer to "QUICK REFERENCE DATA"	
Short time overload (S.T.O.L) Clause 4.13	Permanent resistance change after a 2 second application of a voltage 2.5 times RCWC or the maximum operation current specified in the above list, whichever is less.				no visible damage $\Delta$ R/R max. $\pm$ (1%+0.0001 $\Omega$ )	
Resistance to soldering heat(R.S.H)	Un-mounted chips completely immersed for 10±0.5 second in a SAC solder bath at $260^{\circ}\!C\pm\!5^{\circ}\text{C}$				no visible damage $\Delta R/R$ max. $\pm (1\%+0.0001\Omega)$	
IEC 60068-2-58: 2004						,

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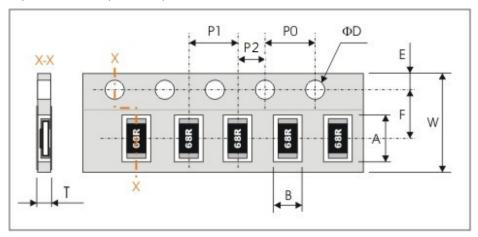


Solderability IEC 60068-2-58: 2004	Un-mounted chips completely immersed for 2±0.5 second in a SAC solder bath at 235 $^\circ\!$	good tinning (>95% covered) no visible damage
Temperature cycling Clause 4.19	30 minutes at -55°C±3°C, 2~3 minutes at 20°C+5°C-1°C, 30 minutes at +155°C±3°C, 2~3 minutes at 20°C+5°C-1°C, total 5 continuous cycles	no visible damage $\Delta$ R/R max. $\pm$ (1%+0.0001 $\Omega$ )
Load life (endurance) Clause 4.25	1000 +48/-0 hours, loaded with RCWC or max. operation current in chamber controller $70\pm2^{\circ}$ C, 1.5 hours on and 0.5 hours off	no visible damage $\Delta R/R \text{ max. } \pm (5\% + 0.0001\Omega)$
Endurance at upper temperature	1000 +48/-0 hours, no load at 155±2°C,	no visible damage $\Delta R/R \text{ max. } \pm (5\% + 0.0001\Omega)$
Load life in Humidity  Clause 4.24	1000 +48/-0 hours, no loaded at 40°C±2°C and 90~95% relative humidity.	no visible damage $\Delta$ R/R max. $\pm$ (5%+0.0001 $\Omega$ )
Adhesion Clause 4.32	Pressurizing force: 5N, Test time: 10±1sec.	No remarkable damage or removal of the terminations



#### **PACKAGING**

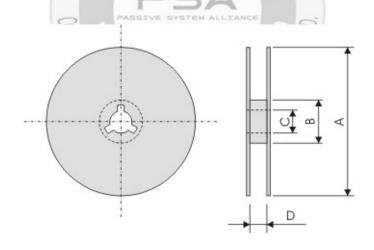
#### Plastic Tape specifications (unit :mm)



Symbol	А	В	W	F	E
Dimensions	6.75±0.20	3.50±0.20	12.00±0.30	5.50±0.10	1.75±0.10

Symbol	P1	P0 又 /	) A P2	ΦD	T
Dimensions	4.00±0.10	4.00±0.10	2.00±0.10	Ф1.50 <sup>+0.1</sup> <sub>-0.0</sub>	0.90±0.20

#### **Reel dimensions**



Symbol	А	В	С	D
(unit : mm)	Ф180.0 -1.5	Φ60.0±1.0	13.0±0.2	13.0±1.0

#### **Taping quantity**

- Chip resistors 4,000 pcs per reel.

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# 单击下面可查看定价,库存,交付和生命周期等信息

>>Walsin Technology(华新科技)