

Y5V Hi-K CERAMIC CAPACITOR

POE-D08-00-E-12

Ver: 12

Page: 1 / 17

PRODUCT SPECIFICATION

PRODUCT: CERAMIC DISC CAPACITOR

TYPE: Y5V Hi-K CERAMIC CAPACITOR

CUSTOMER:

DOC. NO.: POE-D08-00-E-12

Ver.: 12

APPROVED BY CUSTOMER

PASSIVE SYSTEM ALLIANCE

VENDOR:

☐ WALSIN TECHNOLOGY CORPORATION

566-1, KAO SHI ROAD, YANG-MEI TAO-YUAN, TAIWAN

1. PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.

NO.277,HONG MING ROAD,EASTERN SECTION, HUANGPU DISTRICT ,GUANG ZHOU,CHINA

MAKER: PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.

 ${\tt NO.277, HONG\ MING\ ROAD, EASTERN\ SECTION, } \\ {\tt HUANGPU\ DISTRICT\ , GUANG\ ZHOU, CHINA} \\$



Y5V Hi-K CERAMIC CAPACITOR POE-D08-00-E-12 Ver: 12 Page: 2 / 17

Record of change

Date	Version	Description				
2008.6.3	1	1. D21-00-E-03 (before) → POE-D08-00-E-01(1st edition)				
2008.8.22	2	2. Revised diameter as below	5			
		Before After				
		YV501102X050* not available				
		YV102102X060* not available				
		YV102222X060* not available				
		2. Complete lead code	14-17			
		3. Add last SAP code "H" for halogen and Pb free, epoxy resin	6			
2008.12.12	3	1.Complete the 13 th to 17 th codes of SAP P/N.	4-7			
		2.Page layout adjustment.				
		3. Added marking when the coating resin is Halogen and Pb free Epoxy.				
2009.8.5	4	1. Change PSA & POE logo to Walsin & POE logo.				
2013/5/6 5		1. Review the Lead diameter φ from 0.60 +/-0.06mm to 0.55+/-0.05mm				
		2. Review the "DΦ≤6.0mm shall be omitted." To " DΦ≤060 shall be omitted.				
		3. Review the Solderability temperature from $255(+5/-0)^{\circ}$ C to $245\pm5^{\circ}$ C "Solderability time from 2 ± 0.5 s to 5 ± 0.5 s,				
2013/10/18	6	Review the packing specification	9			
2016/3/3	7	 Review the Available lead code of Lead Configuration. Delete the definition about "Old Part No." Modify the contents of the use of epoxy resin for 1KV products Review the Specification and test method Review 9. Drawing of internal structure and material list 	5 5-6 7 10-12 17			
2019/7/27	8	1. Review the Hole-down tape width (W0) from 11.0mm min. to 8.0mm min.				
2021/9/9	9	1. Delete Walsin & POE logo.	1			
2022/1/8	10	1. Add "Soldering Recommendation"	16			
2022/4/21	11	1. Add 8.5.3 List of substances that affect the insulation strength of coating	15			
2023/6/15	12	1. The last code "B" is changed from "Epoxy Resin, Pb free" to "Halogen free and Pb free, epoxy resin".	4,7			



Y5V Hi-K CERAMIC CAPACITOR POE-D08-00-E-12

Ver: 12

Page: 3 / 17

Table of Contents

No.	Item	Page
1	Part number for SAP system	4
2	Mechanical	5~6
3	Capacitance value vs. rated voltage, product diameter	7
4	Marking	7
5	Taping Format	8
6	Packing specification	9
7	Specification and test method	10~12
8	Cautions & Notices	13~15
9	Soldering Recommendation	16
10	Drawing of internal structure and material list	17
	PASSIVE SYSTEM ALLIANCE	



Y5V Hi-K CERAMIC CAPACITOR	POE-D08-00-E-12	Ver: 12	Page: 4 / 17	
----------------------------	-----------------	---------	--------------	--

1. Part number for SAP system(total eighteen code):

 $\frac{YV}{0}$ $\frac{500}{2}$ $\frac{102}{6}$ $\frac{Z}{0}$ $\frac{040}{6}$ $\frac{B}{0}$ $\frac{20}{6}$ $\frac{C}{0}$ $\frac{5}{0}$ $\frac{P}{0}$

• Temperature characteristic:

Code	YV(Y5V)
Operating temperature	-25°C to +85°C
Cap. change	-82%~+22%

2 Rated voltage (Vdc):

l	Voltage	50V	100V	500V	1000V	2000V
	Code	500	101	501	102	202

❸Capacitance(pF):

Capacitors (pF)	1000	2200	4700
Code	102	222	472

4 Capacitance tolerance : $M=\pm 20\% \cdot Z=+80\%-20\%$

6 Nominal body diameter dimension (Ref. to page.7 D ϕ Code spec.).

6 Code of lead type: Please refer to Item "2.Mechanical".

• Packing mode and lead's length (identified by 2-figure code)

Taping Code	Description
AN	Ammo / Pitch of component:12.7 mm
	44,

Bulk Code	Description
3E	Lead's length L: 3.5mm
04	Lead's length L: 4mm
4E	Lead's length L: 4.5mm
20	Lead's length L: 20mm

SYSTEM ALLIANCE

8 Length tolerance

Code	Description		
A	±0.5 mm(Only for short kink lead code "D/X/H")	Short lead	
В	±1.0 mm	Short lead	
С	Min.	Long lead	
D	Taping special purpose	Taping	

Pitch

Code	Description	Code	Description
5	5.0±0.8mm (For Bulk)	7	7.5 ±1mm
5	5.0+0.8mm-0.2mm (For Taping)	0	10.0 ±1mm
2	2.5 ±0.8 mm		

Coating code

Code	Description			
P	Helegan free and Dh free mhanelic recin			
A	Halogen free and Pb free, phenolic resin			
В	Jalacan free and Dh free anavy regin			
Н	Halogen free and Pb free , epoxy resin			



Y5V Hi-K CERAMIC CAPACITOR	POE-D08-00-E-12	Ver: 12	Page: 5 / 17	
----------------------------	-----------------	---------	--------------	--

2. Mechanical:
Available lead code: (unit: m

Available lead code: (unit: mm)						
Lead type	SAP P/N (13-17) digits	Pitch (F)	Lead length (L)	Available rated voltage	Packing	Lead configuration
	B20C2	2.5 ± 0.8	20 MIN.	50V		D max. T max.
	B20C5	5.0 ± 0.8	20 MIN.			
	B20C6	6.4 ± 1.0	20 MIN.	501/5001/	Bulk	
Lead style: B	B20C7	7.5 ± 1.0	20 MIN.	50V,500V, 1KV,2KV		()
Straight long	B20C0	10 ± 1.0	20 MIN.	1K V,2K V		
lead	BAND5	5.0 +0.8 -0.2				
Total	BAND2	2.5 ± 0.8	Taping Spec. (Ref.to page.8)	50V	Tap. Ammo	Ø d→
	L05B2	2.5 ± 0.8	5.0 ± 1.0	-		D max. T max.
	L4EB5	5.0 ± 0.8	4.5 ± 1.0			
	L05B5	5.0 ± 0.8	5.0 ± 1.0			
Lead style: L	L05B6	6.4 ± 1.0	5.0 ± 1.0			()
Straight short	L4EB7	7.5 ± 1.0	4.5 ± 1.0	50V,500V, 1KV,	Bulk	
lead	L05B7	7.5 ± 1.0	5.0 ± 1.0	2KV		
	L4EB0 L04B0	10 ± 1.0 10 ± 1.0	4.5 ± 1.0 4.0 ± 1.0			
				/>		
	L05B0	10 ± 1.0	5.0 ± 1.0	13 ~		
	H3EA5	5.0 ± 0.8	3.5 ± 0.5			
	H04A5	5.0 ± 0.8	4.0 ± 0.5	份谷、冬		
	H4EB5 H05B5	5.0 ± 0.8	4.5 ± 1.0 5.0 ± 1.0		(D)	
	H20C5	5.0 ± 0.8 5.0 ± 0.8	20 MIN.	· (f -,)	オカー	D max. T max.
	H3EA7	7.5 ± 1.0	3.5 ± 0.5	Y (,	D 11	
Lead style: H	H04A7	7.5 ± 1.0	4.0 ± 0.5	50V,500V, 1KV,		
Lead Style - 11	H4EB7	7.5 ± 1.0	4.5 ± 1.0	2KV	Bulk	()
Inside kink	H05B7	7.5 ± 1.0	25.0 ±1.0 svs	TEM ALLIANCE	9	× 1
lead	H3EA0	10 ± 1.0	3.5 ± 0.5	, ĉ		So max
	H04A0	10 ± 1.0	4.0 ± 0.5	45	35	₩ F → III
	H4EB0 H05B0	10 ± 1.0	4.5 ± 1.0	30	2	[] ød→[]← <u>†-</u> [] []
	H20C0	10 ± 1.0 10 ± 1.0	5.0 ±1.0 20 MIN.	000		
	HAND5	5.0 +0.8 -0.2	Taping SPEC. (Ref.to page.8)	50V,500V, 1KV, 2KV	Tap. Ammo	
	X3EA5	5.0±0.8	TOT TOUR OF I	OKHOKHIJO.		
	X3EA7	7.5±1.0	3.5 ± 0.5			D max. T max.
	X3EA0	10±1.0				
Lead style: X	370445	5.0±0.8		1		
Outside kink	X04A7	7.5±1.0	4.0 ± 0.5	50V,500V, 1KV,	Bulk	7 /
lead	X04A0	10±1.0		2KV	Buik	5.0 max.
1000	X05B5	5.0±0.8				
	X05B7	7.5±1.0	5.0 ± 1.0			ød- - ød -
			3.0 ± 1.0			0 0
	X05B0	10±1.0				
	D04A5	5.0±1.0				D max.
Lead style: D	D04A7	7.5±1.0	4.0 ± 0.5			
	D04A0	10±1.0]	D.,11.	(
	D3EA5	5.0±0.8		50V,500V, 1KV,	Bulk	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Vertical kink	D3EA7	7.5±1.0	3.5 ± 0.5	2KV		- 1- 1- 1- 1- 1- 1- 1- 1- 1- 1- 1- 1- 1-
short lead	D3EA0	10±1.0	3.5 ± 0.5			▎
			Taping SPEC.	-		ø d→
	DAND5	$5.0^{+0.8}$ -0.2	(Ref.to page.8)		Tap. Ammo	u <u>r</u> u



Y5V Hi-K CERAMIC CAPACITOR	POE-D08-00-E-12	Ver: 12	Page: 6 / 17	
----------------------------	-----------------	---------	--------------	--

Lead type	SAP P/N (13-17) digits	Pitch (F)	Lead length (L)	Available rated voltage	Packing	Lead configuration
	M05B5	5.0 ± 0.8	5.0 ± 1.0			D max. T max.
	M05B7	7.5 ± 1.0				
	M05B0	10 ± 1.0				
Lead style: M	d style: M M04B5 5.0 ± 0.8		()			
Double outside kink lead	M04B7	7.5 ± 1.0	4.0 ± 1.0	50V,500V, 1KV, 2KV	Bulk	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
	M04B0	10 ± 1.0				Fød-

- % Lead diameter ϕ = 0.55+/-0.05 mm
- \divideontimes Phenolic resin coating for 50V/500V/1KV product; Epoxy resin coating for 2KV product.
- **※ e** (Coating **extension** on leads):

For straight lead style: 1.5mmMax when the rated voltage is 50Vdc & 100Vdc;

2.0mmMax when the rated voltage is 500Vdc and 1KVdc;

3.0mmMax when the rated voltage is 2KVdc.

For kink lead style: not exceed the kink.

%When Dφ≥11mm, only for bulk, but Dφ≤10mm can do Bulk or Taping.





Y5V Hi-K CERAMIC CAPACITOR	POE-D08-00-E-12	Ver: 12	Page: 7 / 17
----------------------------	-----------------	---------	--------------

3. Capacitance value vs. rated voltage, product diameter:

T.C.	Y5V (CLASS II, Temperature: -25°C ~+85°C, T.C.C.: +22% ~-82%)												
Rate voltage		50)V			100V			500V		1KV	2	KV
Dφ	040	050	060	080	040	050	060	050	070	080	100	070	120
D max. (mm)	4.5	5.5	6.5	8.5	4.5	5.5	6.5	5.5	7.5	8.5	11.0	8.5	13.5
T max. (mm)	3.5	3.5	3.5	3.5	3.5	3.5	3.5	4.0	4.0	4.0	4.5	5.0	5.0
1000	102				102							102	
2200		222				222		222				222	
4700		472				472			472				
10000			103				103			103	103		103
22000				223									
47000													
Packing	TAPING or BULK						TAPING or E	BULK	BULK				
Coating		Phenolic Resin					Phenolic or Epoxy Resin	Ерох	y Resin				

4. Marking:

Marking	(2) 102Z (4) (6) UK (5)			
(1). Temp. char.	Y5V: Shall be omitted			
(2). Rated capacitance	Identified by 3-Figure Code. Ex. 1000pF→"102", 4700pF→"472"			
	50V&100V Marked with code "_"under the rated capacitance.			
(3). Rated voltage	No any marking under the rated capacitance.			
1	1000V&2000V Marked with code: 1000V→"1KV", 2000V→"2KV"			
(4). Capacitance tolerance	$M=\pm 20\% \cdot Z=+80\%-20\% (for Y5V)$			
(5). Manufacturer's identification	Shall be marked as "", but DΦ≤060 shall be omitted.			
(6). Halogen and Pb free	There is a "_'marking under the code "V" when the coating resin is Halogen free and Pb free Epoxy. (For the last code "H" and "B" of the SAP P/N)			

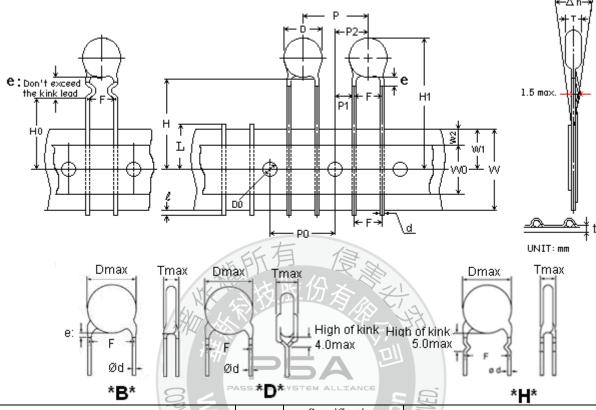


Y5V Hi-K CERAMIC CAPACITOR POE-D08-00-E-12 Ver: 12 Page: 8 / 17

5. Taping specifications:

* Lead spacing: $F=5.0^{+0.8}$ -0.2 (mm)

• 12.7mm pitch/lead spacing 5.0mm taping Lead code: *BAND5 & *DAND5 & *HAND5



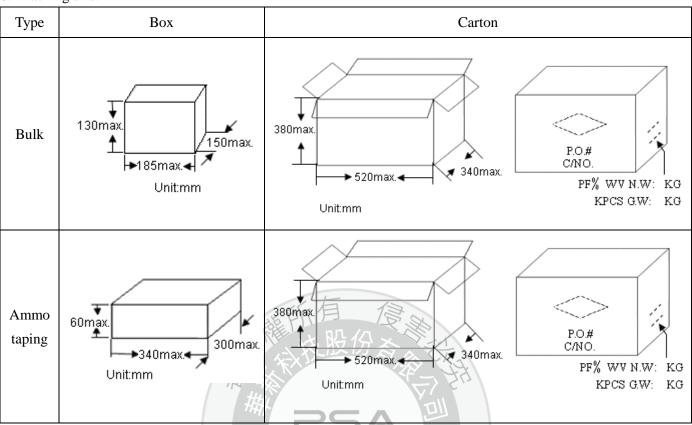
Item	景台	Symbol	Specification		Remarks	
Titlii G		Symbol	Value	Tolerance	Kemarks	
Body diameter	0,1	D	*	max.	See Section"3. Capacitance value vs. rated	
Body thickness	1/1	Q.T.	*	max.	voltage, product diameter".	
Lead-wire diameter	7/5/1/	d/l	0.55	±0.05		
Pitch of component	-11/	$\langle \gamma \rangle P_{\gamma \gamma}$	12.7	±1.0		
Feed hole pitch		C//P0//	(\) [2.7]	±0.3	Cumulative pitch erroe:1.0mm/20 pitch	
Feed hole center to lead		P1	3.85	±0.7	To be measured at bottom of clinch	
Hole center to component center		P2	6.35	±1.3		
Lead-to-lead distance		F	5.0	+0.8,-0.2		
Component alignment, F-R		Δh	0	±2.0		
Tape width		W	18.0	+1.0,-0.5		
Hole-down tape width		W0	8.0	min.		
Hole position		W1	9.0	+0.75,-0.5		
Hole-down tape position		W2	3.0	max.		
Height of component form tape	For straight lead type	Н	20.0	+1.0 -0.5		
center	For kinked lead type	H0	16.0	±0.5		
Component height		H1	32.25	max.		
Lead-wire protrusion			2.0	max.	Or the end of lead wire may be inside the tape.	
Food hole diameter			4.0	±0.2		
Total tape thickness			0.7	±0.2	Ground paper:0.5±0.1mm	
Length of sniped lead			11.0	max.		
Coating rundown on leads			Please refer to page 6 "e(Coating extension on leads)".			



POE-D08-00-E-12	Ver: 12	Page: 9 / 17
	POE-D08-00-E-12	POE-D08-00-E-12 Ver: 12

6. Packing Baggage:

6.1 Packing size:



6.2 Packing quantity:

Packing type	Th	e code of 14th to15th in SAP P/N	MPQ (Kpo	Remark	
Tonina		AN	2		Phenolic resin
Taping		AN	1.5		Epoxy resin
Packing type	Lead length	Size code of 10th to 12th in SAP P/N	MPQ (Kpcs/Bag)	Kpcs/Box	Remark
		040~070	1	3	
	Long lead (L≥16mm)	080~100	1	2	
	(L≦ Ioiiiii)	120	0.5	1.5	
Bulk		040~060	1	6	
	Short lead (L<16mm)	070~080	1	4	
		090~100	1	3	
		120	1	2	

PASSIVE SYSTEM ALLIANCE



Y5V Hi-K CERAMIC CAPACITOR	POE-D08-00-E-12	Ver: 12	Page: 10 / 17
----------------------------	-----------------	---------	---------------

7. Specification and test method:

7.1 SCOPE: THIS SPECIFICATION APPLIES TO Y5V HI-K CERAMIC TYPE CAPACITOR.

7.2 TEST CONDITIONS:

OTHERWISE SPECIFIED, ALL TESTS SHALL BE OPERATED AT THE STANDARD TEST CONDITIONS OF TEMPERATURE 5° C TO 35° C AND RELATIVE HUMIDITY 45% TO 85%. WHEN FAILS A TEST, RETEST BE OPERATED AT THE CONDITIONS OF TEMPERATURE 25° C \pm 2° C, RELATIVE HUMIDITY OF 60% TO 70% AND BAROMETRIC PRESSURE 860 TO 1060 MBAR.

7.3 HANDLE PROCEDURE: TO AVOID UNEXPECTED TESTING RESULTS FROM OCCURRING, THE TESTED CAPACITOR MUST BE KEPT AT ROOM TEMPERATURE FOR AT LEAST 30 MINUTES AND COMPLETELY DISCHARGED.

7.4 TEST ITEMS:

ITEM	POST-TEST REQUIREMENTS	TESTING PROCEDURE		
APPEARANCE STRUCTURE SIZE	NO ABNORMALITIES	AS SECTION 2 & 3.		
MARKING	临所走	AS STATED IN SECTION 4		
WITHSTAND VOLTAGE	NO ABNORMALITIES	A. BELOW 1KV: 250% RATED VOLTAGE WITH 50mA MAX. CHARGING CURRENT FOR 1~5 SEC. B. 1KV & ABOVE: 200% RATED VOLTAGE WITH 50MA MAX. CHARGING CURRENT FOR 1~5 SEC.		
INSULATION RESISTANCE	$10,000~\mathrm{M}\Omega$ MIN	INSULATION RESISTANCE SHALL BE MEASURED AT 60 ± 5 SECONDS AFTER RATED VOLTAGE APPLIED. RATED VOLTAGE : $50V = 50V$ $100V = 100V$ $500V & ABOVE = 500V$		
CAPACITANCE	TOLERANCE: Z:-20~+80%;	TESTING FREQUENCY : 1 KHZ ± 20 % TESTING VOLTAGE : 1.0 VRMS		
OPERATING TEMPERATURE RANGE	-25°C ~ +85°C	Y CORPORATION.		
DISSIPATION FACTOR (D.F.)	BELOW 5.0%	AS ABOVE STIPULATION OF CAPACITANCE		
TEMPERATURE CHARACTERISTIC	CAP. CHANGE: WITHIN +22%, -82%	CAPACITANCE SHALL BE MEASURED AT 25°C. AND CLASSIFIED AS CAP. CHANGE: -25°C ~ +85°C Pre-treatment: Capacitor shall be stored at 125±3°C for 1 hour.then placed at ** 1room condition for 24±2 hours		
TERMINAL	TENSILE STRENGTH : NO BREAKDOWN	WIRE DIA.0.5 M/M, LOADING WEIGHT 0.5KGS, FOR 10±1 SECONDS. WIRE DIA.0.6 M/M, LOADING WEIGHT 1.0KGS, FOR 10±1 SECONDS.		
STRENGTH	BENDING STRENGTH : NO BREAKDOWN	WIRE DIA.0.5 M/M, LOADING WEIGHT 0.25 KGS. WIRE DIA.0.6 M/M, LOADING WEIGHT 0.5 KGS. (BENDING BACK AND FORTH 90 DEGREE TWICE)		

^{*1&}quot;room condition" Temperature:15~35, Relative humidity: 45~75%, Atmospheric pressure:86~106kPa



Y5V Hi-K CERAMIC CAPACITOR	POE-D08-00-E-12	Ver: 12	Page: 11 / 17	
----------------------------	-----------------	---------	---------------	--

ITEM	POST-TEST REQUIREMENTS	TESTING PROCEDURE
SOLDERBILITY	LEAD WIRE SHALL BE SOLDERED OVER 3/4 OF THE CIRCUMFERENTIAL DIRECTION.	TO COMPLY WITH JIS-C-5102 8.4 SOLDER TEMPERATURE245±5°C AND DIPPING TIME 5±0.5 SECONDS. FLUX: WEIGHT RATIO OF RESIN 25%.
SOLDERING HEAT RESISTANCE	APPEARANCE: NO ABNORMALITIES CAP. CHANGE: ± 20% MAX WITHSTAND VOLTAGE: (BETWEEN TERMINALS) NO ABNORMALITIES	LEAD WIRE OR TERMINALS SHALL BE IMMERSED UP TO 2.0 M/M FORM BODY. (A) BODY DIA. ≤ 5.0mm: INTO THE MOLTEN SOLDER OF WHICH TEMPERATURE: 260(+5/-0)°C FOR 3.0±0.5 SECONDS. (B) BODY DIA. > 5.0mm: INTO THE MOLTEN SOLDER OF WHICH TEMPERATURE 260(+5/-0)°C FOR 5~10 SECONDS. THEN LEAVE AT STANDARD TEST CONDITIONS FOR 1~2 HOURS, THEN MEASURED. ※WHEN SOLDERING CAPACITOR WITH A SOLDERING IRON, IT SHOULD BE PERFORMED IN FOLLOWING CONDITIONS. TEMPERATURE OF IRON-TIP: 350~400 °C SOLDERING IRON WATTAGE: 50W MAX. SOLDERING TIME: 3.5 SEC. MAX.
HUMIDITY CHARACTERISTIC (STABLE SITUATION)	APPEARANCE : NO ABNORMALITIES CAP. CHANGE: ± 30% MAX D.F: 7.5% MAX INSULATION RESISTANCE : 1000ΜΩ MIN.	CAPACITORS SHALL BE SUBJECTED TO A RELATIVE HUMIDITY OF 90 \sim 95% AT 40±2°C FOR 500(+24/-0) HOURS. THEN DRIED FOR 24±2 HOURS AND MEASURED.
HUMIDITY LOADING	APPEARANCE: NO ABNORMALITIES. CAP. CHANGE: ± 30% MAX. D.F.: 7.5% MAX INSULATION RESISTANCE: 500 MΩ MIN.	CAPACITORS SHALL BE SUBJECTED TO A RELATIVE HUMIDITY OF 90 ~ 95% AT 40 ± 2°C FOR 500(+24/-0) HOURS WITH RATED VOLTAGE APPLIED WITH 50MA MAX., THEN DRIED FOR 24±2 HOURS AND MEASURED. Pre-treatment: Capacitor shall be stored at125±3°C for 1hour.then placed at **1room condition for 24±2hours

^{* 1&}quot;room condition" Temperature: 15~35, Relative humidity: 45~75%, Atmospheric pressure: 86~106kPa



Y5V Hi-K CERAMIC CAPACITOR POE-D08-00-E-12 Ver: 12 Page: 12 / 17

ITEM	POST-TEST REQUIREMENTS	TESTING PROCEDURE
	APPEARANCE : NO ABNORMALITIES.	CAPACITORS SHALL BE SUBJECTED TO A TEST OF
	CAP. CHANGE : ± 30% MAX	(A) BELOW 1KV: 200% RATED VOLTAGE WITH 50mA MAX.
HIGH TEMPERATURE	D.F: 7.5% MAX	(B) 1KV & ABOVE: 150% RATED VOLTAGE WITH 50mA MAX.
LOADING	INSULATION RESISTANCE : $2000M\Omega$ MIN.	FOR 1000(+48/-0) HOURS AT 85 ± 2°C (FOR Y5V) AND THEN DRIED FOR 24±2 HOURS AND MEASURED Pre-treatment:
		Capacitor shall be stored at 125±3°C for 1 hour. then placed at 3.4 toom condition for 24±2 hours
	APPEARANCE:	CAPACITORS SHALL BE SUBJECTED TO:
	NO ABNORMALITIES	$-25\pm3^{\circ}\mathbb{C}(30\pm3\min) \rightarrow 25^{\circ}\mathbb{C}(3\min) \rightarrow 85\pm3^{\circ}\mathbb{C}(30\pm3\min) \rightarrow$ $25^{\circ}\mathbb{C}(3\min) \text{ FOR 5 CYCLE.}$
	CAP. CHANGE :	Pre-treatment:
TEMPERATURE	Y5V: ±30% MAX	Capacitor shall be stored at125±3°C for 1hour.then placed at ※
CYCLING	D.F.	1room condition for 24±2hours
	Y5V : 7.5% MAX	SA
	INSULATION RESISTANCE: 1000 MΩ MIN.	SYSTEM ALLIANCE

^{* 1&}quot;room condition" Temperature:15~35, Relative humidity: 45~75%, Atmospheric pressure:86~106kPa



Y5V Hi-K CERAMIC CAPACITOR	POE-D08-00-E-12	Ver: 12	Page: 13 / 17
----------------------------	-----------------	---------	---------------

8. Cautions & notices:

※Application: DC or Low frequency High Voltage circuits.

As coupling and decoupling capacitors for such application where higher losses and a reduced capacitance stability are required.

8.1. Caution (Rating)

I. Operating Voltage

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range.

When the voltage is applied to the circuit, starting or stopping may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage
Positional measurement	Vo-p	V0-p	Vp-p

II. Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency current, pulse current or similar current, it may self-generate heat due to dielectric loss. The frequency of the applied sine wave voltage should be less than 100kHz. The applied voltage load (*) should be such that the capacitor's self-generated heat is within 20°C at an atmosphere temperature of 25°C. When measuring, use a thermocouple of small thermal capacity-K of Ø0.1mm in conditions where the capacitor is not affected by radiant heat from other components or surrounding ambient fluctuations. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. (Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

III. Fail-Safe

When capacitor is broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

8.2. Caution (Storage and operating condition)

I. Operating and storage environment

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 degrees centigrade and 15 to 85 % for 6 months maximum and use within the period after receiving the capacitors.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.



Y5V Hi-K CERAMIC CAPACITOR POE-D08	-00-E-12 Ver: 12	Page: 14 / 17
------------------------------------	------------------	---------------

8.3. Caution (Soldering and Mounting)

I. Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

II. Soldering

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element. When soldering capacitor with a soldering iron, it should be performed in following conditions

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage: 50W max.

Soldering time: 3.5 sec. max.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

8.4. Caution (Handling)

Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PROUCT IS USED.

8.5. Notice

PASSIVE SYSTEM ALLIANCE

8.5.1. Notice (Soldering and Mounting)

Cleaning (ultrasonic cleaning)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5 min. maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

8.5.2. Notice (Rating)

Capacitance change of capacitor

Class 2 series (Temp. Char. Y5V)

Capacitors have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor is left on for a long time. Moreover, capacitance might change greatly depending on the surrounding temperature or an applied voltage. So, it is not likely to be suitable for use in a time constant circuit.

Please contact us if you need detailed information.



Y5V Hi-K CERAMIC CAPACITOR	POE-D08-00-E-12	Ver: 12	Page: 15 / 17
----------------------------	-----------------	---------	---------------

8.5.3 List of substances that affect the insulation strength of coating:

Resin solvent

Category	Model			
Ketone	Acetone	Butanone	Cyclohexanone	
Esters	Ethyl acetate	Dibutyl phthalate		
Chlorinated hydrocarbons	Dichloromethane			

Resin thinner

Category		Model		
		HK-66 (Alkyl glycidyl ether)		
		501 (Butyl glycidyl ether)		
	Simula function aroun	690 (Phenyl Glycidyl E	ther)	
	Simple function group	AGE (C12-14Aliphatic	Polyalcohol Glycidyl	
		Ether)		
D	, +	692 (Benzyl Glycidyl E	ther)	
Reactive diluentactivated thinner	湖斯用	D-678 (Neopentyl glycol diglycidyl ether)		
/	长形的股份	622 (1,4-Butanediol diglycidyl ether)		
(±3)		669 (Ethylene glycol diglycidyl ether)		
11/1/2	Two functional groups	X-632 (Polypropylene glycol diglycidyl ether)		
	PS PASSIVE SYSTEM A	X-652 (1,6-Hexadiol diglycidyl ether)		
		D-691Epoxypropane o-methylphenyl ether		
PYR	inner lechnology	Anhydrous ethanol	Toluene	
(S)		Ethyl acetate	Dimethylbenzene	
Non-activated th		Dimethyl formamide	Butyl acetate	
		Acetone	Styrene	
	COMPOSITION CORP	Polyol	Benzyl alcohol	

Note: The above substances should not contact the coating of the product body, otherwise it will affect the insulation strength of the product



Y5V Hi-K CERAMIC CAPACITOR	POE-D08-00-E-12	Ver: 12	Page: 16 / 17
----------------------------	-----------------	---------	---------------

9. Soldering Recommendation:

9.1 Wave Soldering Profile:

- Temperature conditions of the flow is recommended as shown in the chart
- Must implement the pre-heat
- Maximum peak flow temperature is recommended 265°C
- \bullet Time "T" implement in the chart recommended within 20 sec. it temperature exceed 200°C
- Take care with the flow solder not to touch the capacitor body directly at mounting

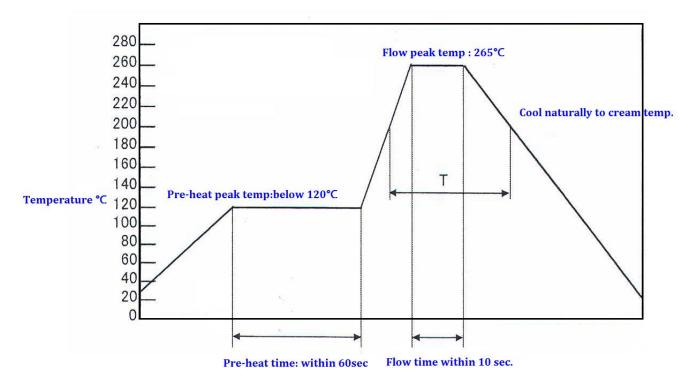


Chart to show flow recommended temp



9.2 Recommended Reworking Conditions with Soldering Iron:

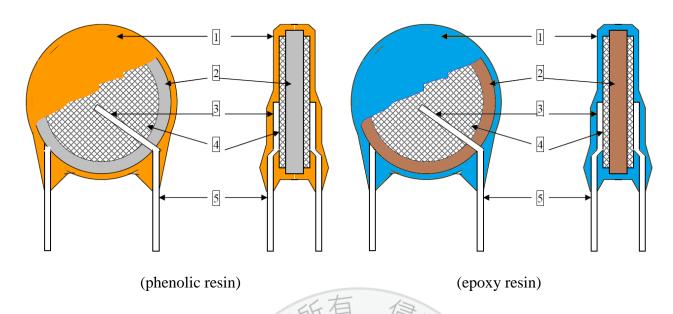
- Temperature of iron-tip: 400 degrees C. max.
- Soldering iron wattage: 50W max.
- Soldering time: 3.5 sec. max.
- Distance from coating body: 2 mm (min.)

9.3 Reflow-Soldering: Lead Ceramic Cap. should not be soldered by reflow-soldering.



Y5V Hi-K CERAMIC CAPACITOR	POE-D08-00-E-12	Ver: 12	Page: 17 / 17
----------------------------	-----------------	---------	---------------

10. Drawing of internal structure and material list:



NO.	部位	材質	構成部份	供應商
NO.	Part name	Material	Component	Vendor
1	Insulation Coating	Phenolic resin	Phenolic resin, Filler, Pigment	Kai Hua
	institution couring	Epoxy resin	Epoxy resin, SiO2, TiO2	1101
		S S PASSIV	S S S S S S S S S S S S S S S S S S S	Hua Xing
2	Dielectric Element	Ceramic	BaTiO3, CaZrO3	Wang Feng
		0,5		Fenghua
3	Solder	Tin-silver	Sn97.5-Ag2.5	Huajun
3	Solder	TIII-SIIVEI	LOCY CODDORAL MARGE. S	Haili
4	Electrodes	Λα	Silver, Glass frit	Daejoo
4 Electrodes	Ag	Silver, Glass IIII	Xinguang	
5	Leads wire	Tinned copper	Substrate metal:Fe&Cu	Hengtai
3	Leaus wire	clad steel wire	Surface plating:Sn 100%	Wuhu Taililai

单击下面可查看定价,库存,交付和生命周期等信息

>>Walsin Technology(华新科技)