

1000V 4A 2.2Ω N-ch Power MOSFET

Description

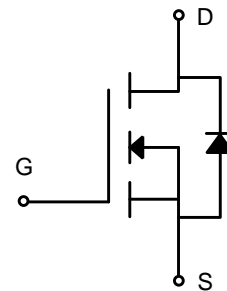
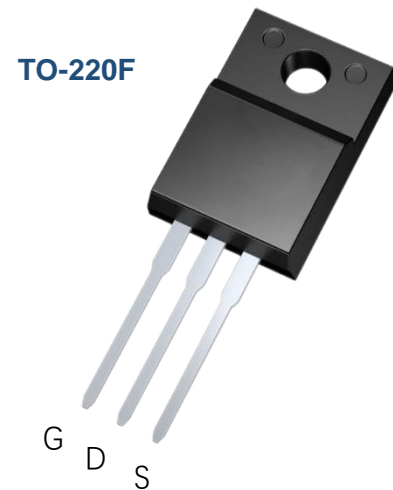
WMOS™ D1 is Wayon's 1st generation VDMOS family that is dramatic reduction in on-resistance and ultra-low gate charge for applications requiring high power density and high efficiency. And it is very robust and RoHS compliant.

Features

- $V_{DS}=1050V@T_{jmax}$
- $Typ.R_{DS(on)}=2.2\Omega@V_{GS}=10V$
- 100% avalanche tested
- Pb-free, Halogen free

Applications

- SMPS
- Charger
- DC-DC



Absolute Maximum Ratings ($T_C=25^\circ C$)

Parameter	Symbol	Test Condition	WML4N100D1	Unit
Drain-source voltage	V_{DS}		1000	V
Gate-source voltage	V_{GS}		± 30	V
Continuous drain current	I_D		4	A
Pulsed drain current	I_{DM}	t_{Pulse} limited by T_{jmax}	16	A
Avalanche energy, single pulse	E_{AS}		450	mJ
Power dissipation	P_D		33	W
Derate above 25°C			0.26	W/°C
Operating junction temperature	T_j		-55~150	°C
Storage temperature	T_{stg}		-55~150	°C
Continuous diode forward current	I_S		4	A
Diode pulse current	I_{Spulse}	t_{Pulse} limited by T_{jmax}	16	A

Thermal Characteristic

Thermal resistance,junction-to-case	$R_{\theta JC}$		3.78	°C/W
Thermal resistance,junction-to-ambient	$R_{\theta JA}$		80	°C/W

Electrical Characteristics of MOSFET

				Min.	Typ.	Max.	
Drain-source break down voltage	BV_{DSS}	$I_D=250\mu A$, $V_{GS}=0V$	$T_C=25^\circ C$	1000	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$I_D=250\mu A$, $V_{DS}=V_{GS}$	$T_J=25^\circ C$	3.0	-	5.0	V
Drain-source leakage current	I_{DSS}	$V_{DS}=1000V$, $V_{GS}=0V$	$T_J=25^\circ C$	-	-	1	μA
		$V_{DS}=800V$, $V_{GS}=0V$	$T_J=125^\circ C$	-	-	100	μA
Gate-source leakage current,forward	I_{GSSF}	$V_{DS}=0V$, $V_{GS}=30V$	$T_J=25^\circ C$	-	-	100	nA
Gate-source leakage current,reverse	I_{GSSR}	$V_{DS}=0V$, $V_{GS}=-30V$	$T_J=25^\circ C$	-	-	-100	nA
Drain-source on-state resistance	$R_{DS(ON)}$	$V_{GS}=10V$, $I_D=2A$	$T_J=25^\circ C$	-	2.2	2.5	Ω
Transconductance	G_{fs}	$V_{DS}=20V$, $I_D=2A$	$T_J=25^\circ C$	-	6.7	-	S

Dynamic Characteristics of MOSFET ($T_C=25^\circ C$)

				Min.	Typ.	Max.	
Input capacitance	C_{iss}	$f=1MHz$, $V_{DS}=25V$, $V_{GS}=0V$		-	1470	-	pF
Output capacitance	C_{oss}			-	115	-	pF
Reverse transfer capacitance	C_{rss}			-	21	-	pF
Gate to source charge	Q_{gs}	$V_{DD}=500V$		-	11	-	nC
Gate to drain charge	Q_{gd}	$I_D=4A$		-	15	-	nC
Total gate charge	Q_g	$V_{GS}=0$ to 10V		-	35	-	nC

Switching Characteristics of MOSFET ($T_C=25^\circ C$)

				Min.	Typ.	Max.	
Turn-on delay time	$t_{d on}$	$V_{DS}=500V$, $I_D=4A$, $R_G=4.7\Omega$, $V_{GS}=0$ to 10V		-	13.4	-	ns
Rise time	t_r			-	8.8	-	ns
Turn-off delay time	$t_{d off}$			-	35	-	ns
Fall time	t_f			-	9.6	-	ns

Characteristics of Body Diode ($T_C=25^\circ C$)

				Min.	Typ.	Max.	
Forward voltage	V_{SD}	$I_{SD}=4A$, $V_{GS}=0V$		-	-	1.5	V
Reverse recovery time	t_{rr}	$V_{DS}=500V$, $I_S=4A$, $V_{GS}=0V$ $-di/dt=100A/\mu s$		-	400	-	ns
Reverse recovery current	I_{rr}			-	10	-	A
Recovery charge	Q_{rr}			-	2	-	μC

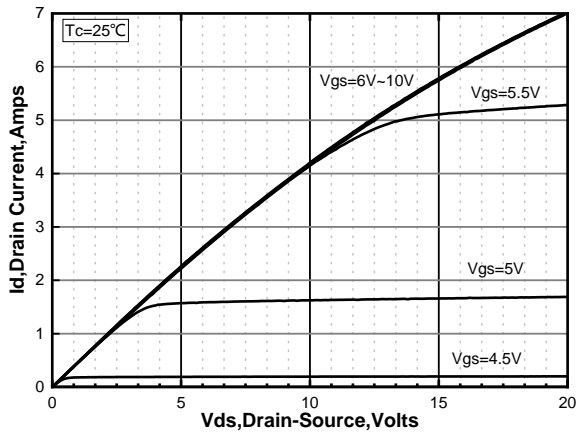


Figure 1. On-Region Characteristics

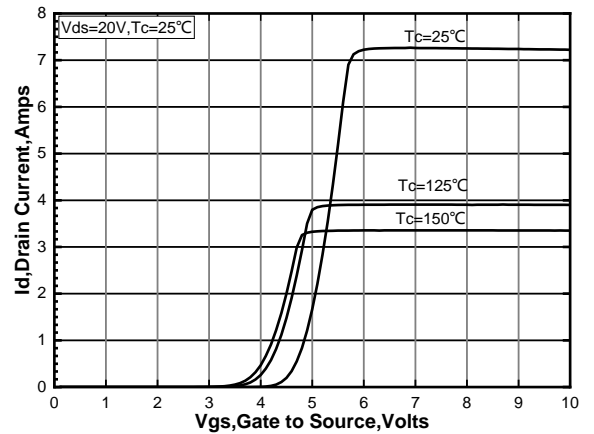


Figure 2. Transfer Characteristics

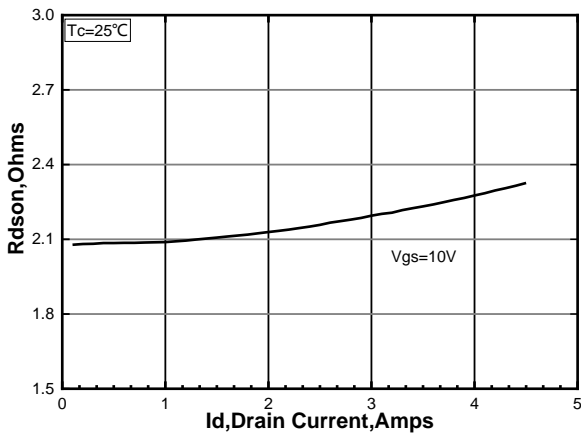


Figure 3. Static Drain-Source On Resistance

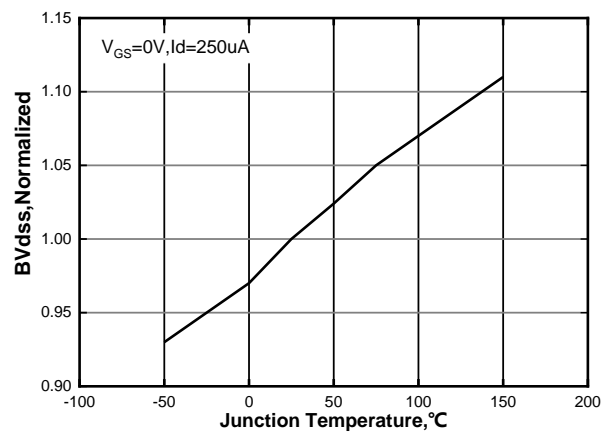


Figure 4. Normalized BV_{dss} vs. Temperature

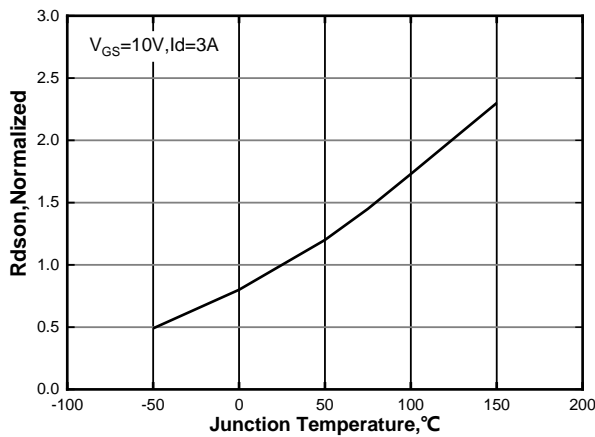


Figure 5. Normalized $R_{DS(on)}$ vs. Temperature

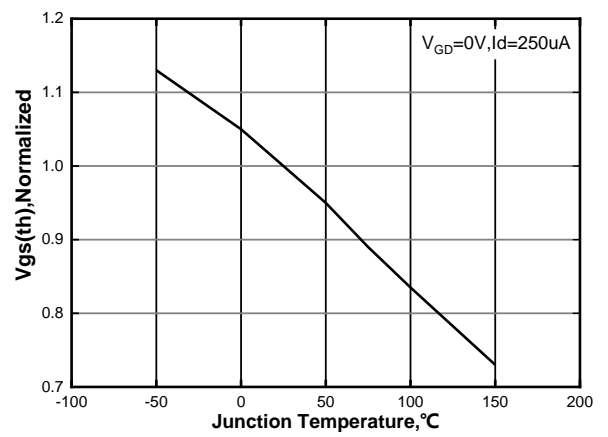


Figure 6. Normalized $V_{GS(th)}$ vs. Temperature

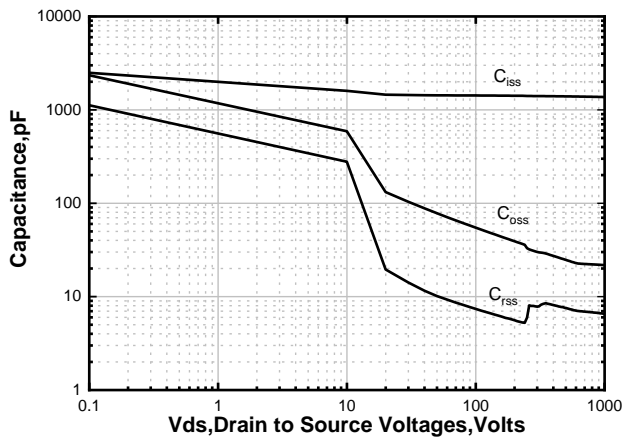


Figure 7. Capacitance Characteristics

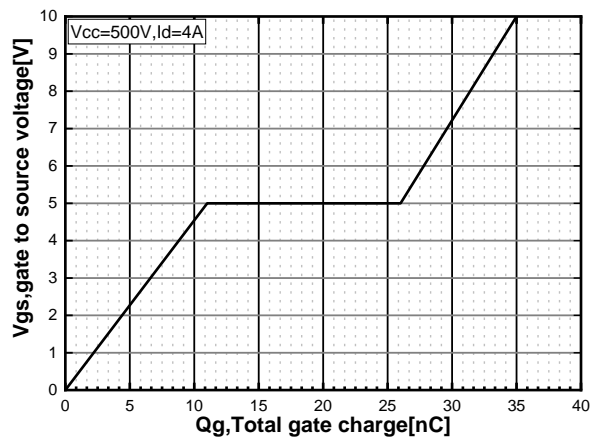


Figure 8. Gate Charge Characteristics

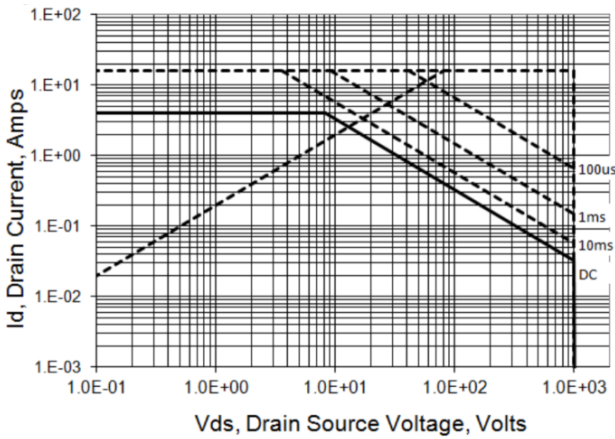


Figure 9. Maximum Safe Operating Area

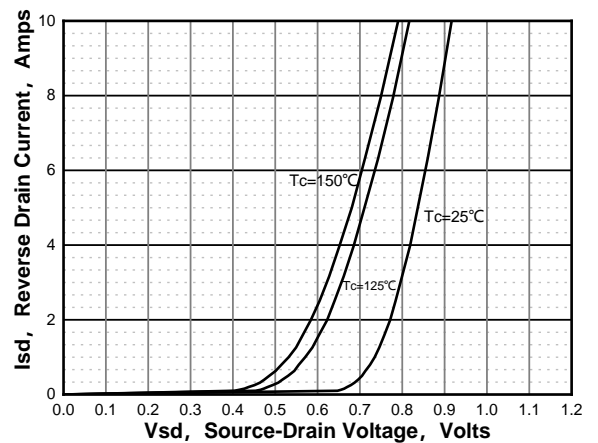


Figure 10. Typical Body Diode Transfer Characteristics

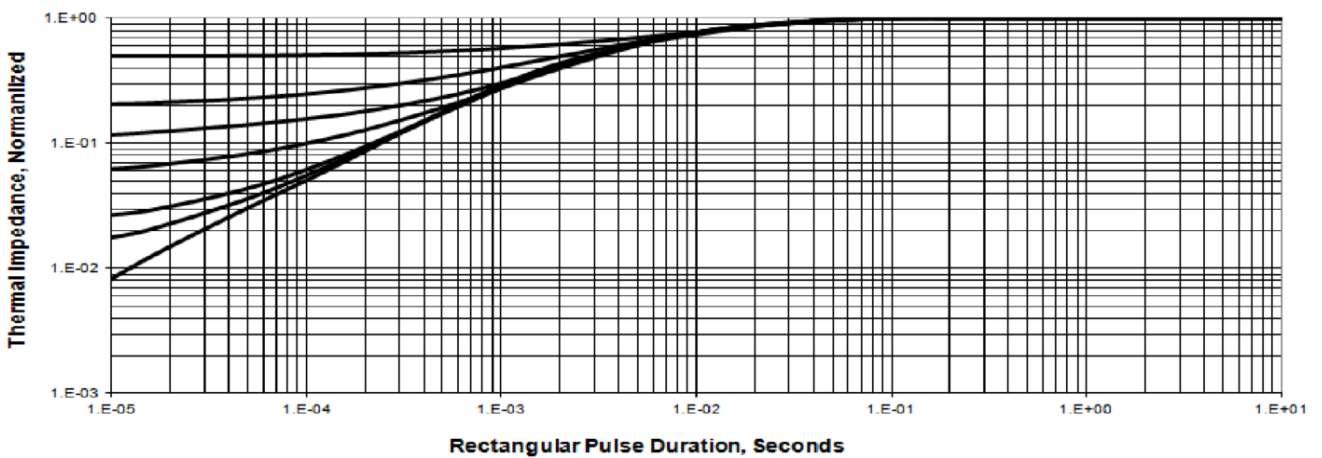
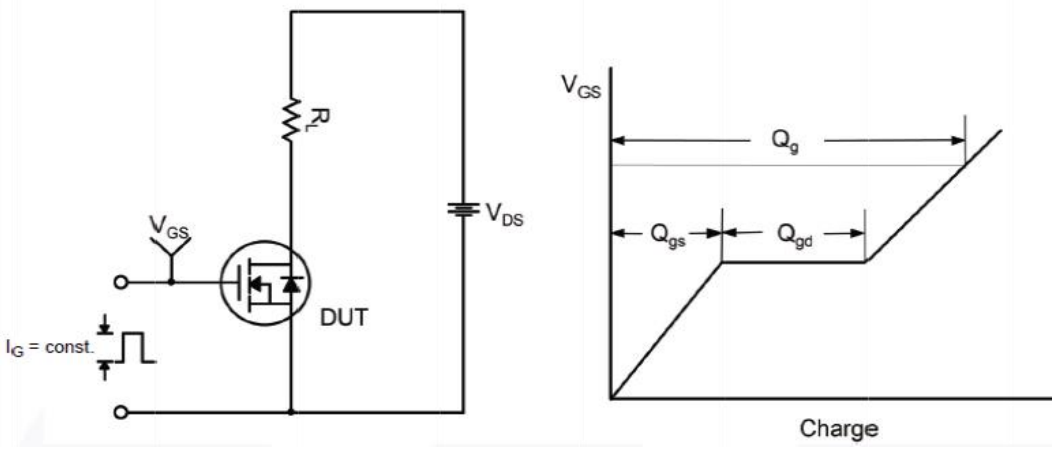
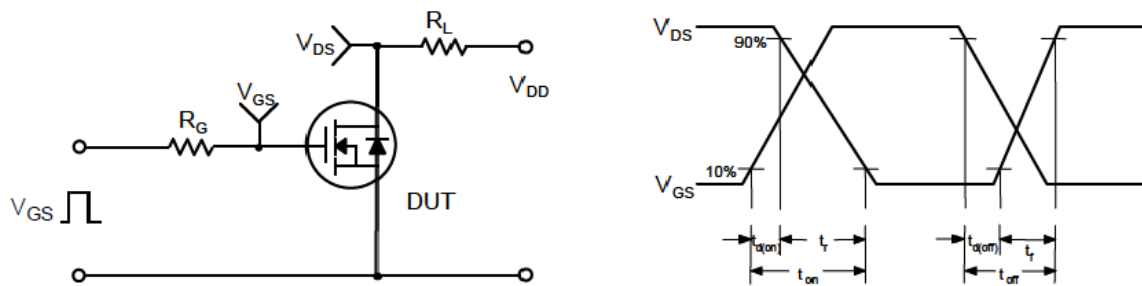


Figure 11. Transient Thermal Response Curve

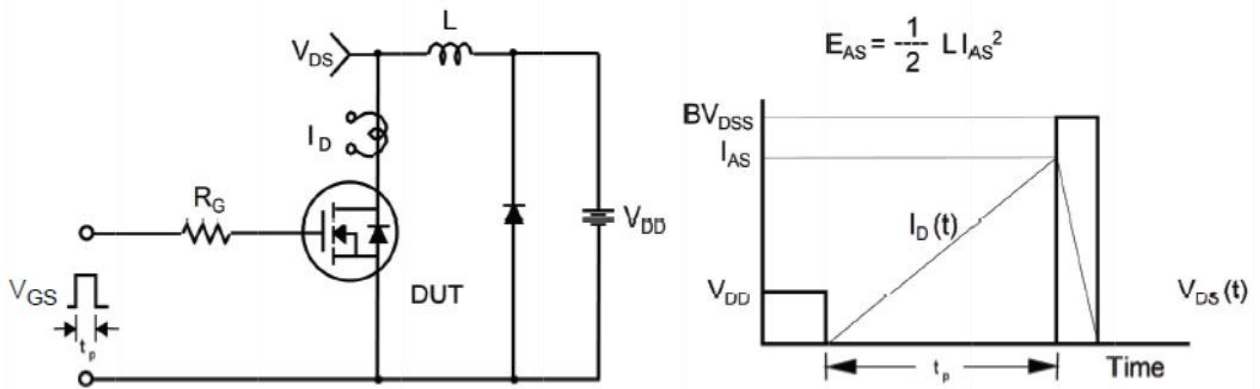
Gate Charge Test Circuit & Waveform



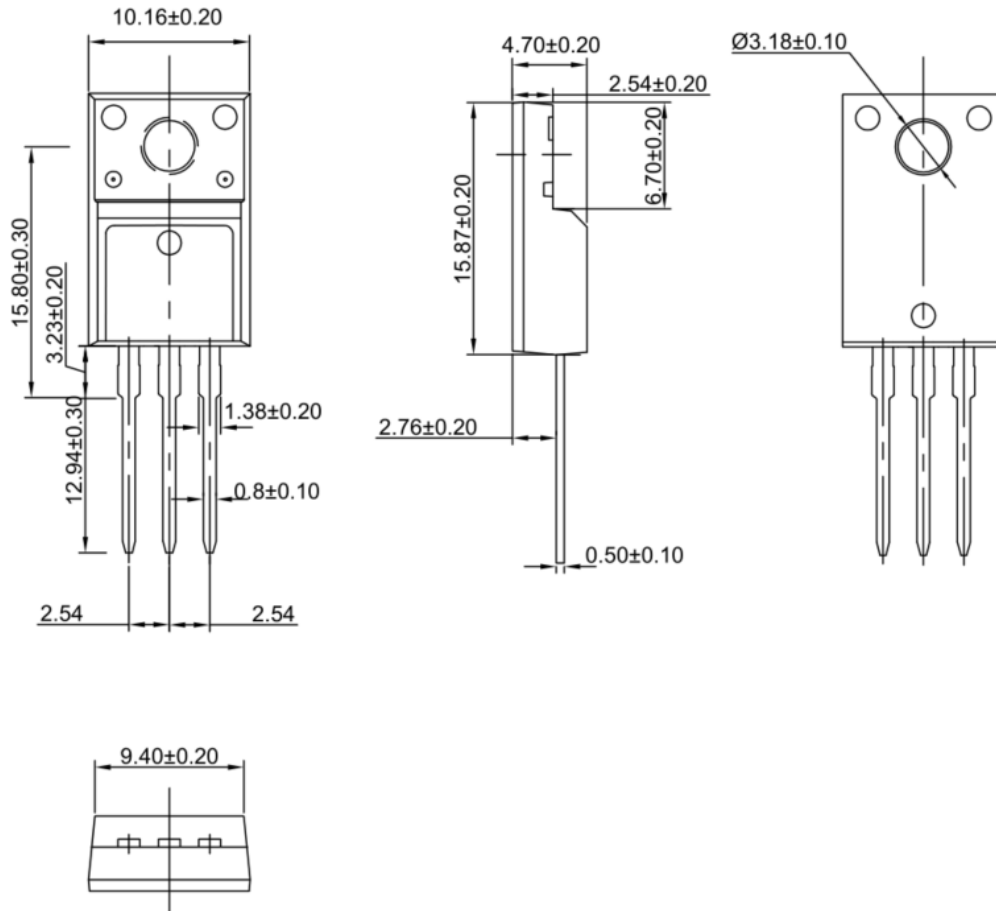
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



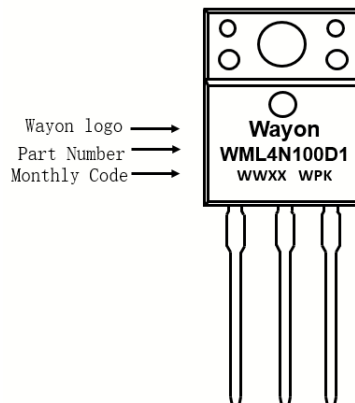
Mechanical Dimensions for TO-220F



Ordering Information

Part	Package	Marking	Packing method
WML4N100D1	TO-220F	WML4N100D1	Tube

Marking Information




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