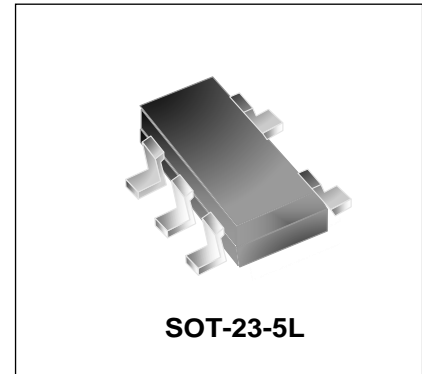


### Features

- Low operating voltage: 5V
- Ultra low capacitance
- Solid-state silicon-avalanche and active circuit triggering technology
- Up to four I/O Lines of Protection
- Low Leakage

### IEC COMPATIBILITY (EN61000-4)

- IEC 61000-4-2 (ESD)  $\pm 30\text{kV}$  (air),  $\pm 30\text{kV}$  (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 10A (8/20 $\mu\text{s}$ )



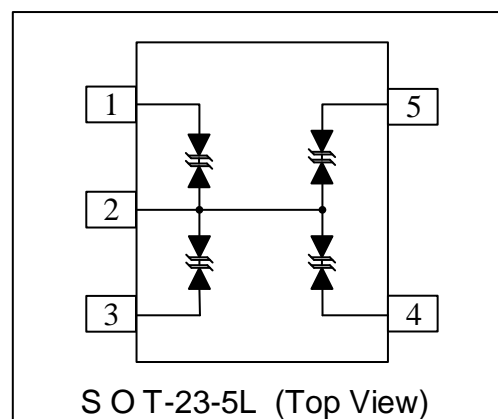
### Mechanical Characteristics

- SOT-23-5L package
- Marking : Making Code
- Packaging : Tape and Reel per EIA 481
- RoHS Compliant & HF

### Applications

- Video/Graphics Card
- Digital Visual Interface (DVI)
- USB2.0 Power and Data lines protection
- Notebook and PC Computers
- Monitors and Flat Panel Displays

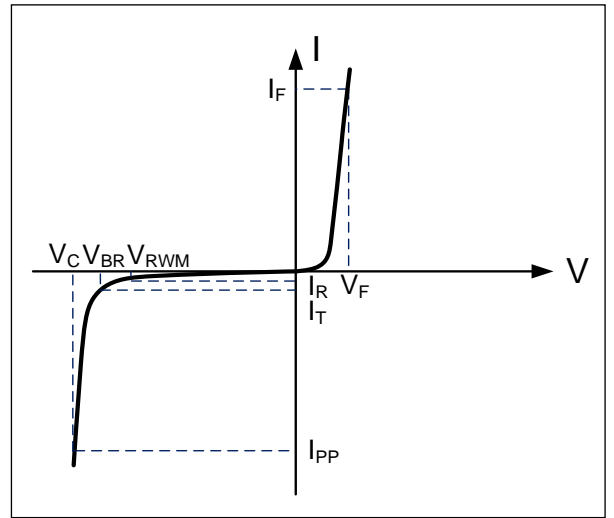
### Schematic & PIN Configuration



Absolute Maximum Rating			
Rating	Symbol	Value	Units
Peak Pulse Power ( $t_p=8/20\mu s$ )	$P_{PP}$	100	Watts
Peak Pulse Current ( $t_p=8/20\mu s$ )	$I_{PP}$	10	A
Operating Temperature	$T_J$	-55 to + 125	$^{\circ}C$
Storage Temperature	$T_{STG}$	-55 to +150	$^{\circ}C$

Electrical Parameters

Symbol	Parameter
$I_{PP}$	Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$V_{RWM}$	Reverse Stand-Off Voltage
$I_R$	Reverse Leakage Current @ $V_{RWM}$
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_T$	Test Current
$I_F$	Forward Current
$V_F$	Forward Voltage @ $I_F$



Electrical Characteristics(T=25 $^{\circ}C$  unless otherwise noted)

WE05-4HT5						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	$V_{RWM}$	Any I/O pin to ground			5.0	V
Breakdown Voltage	$V_{BR}$	$I_T = 1mA$ Any I/O pin to ground	5.5			V
Reverse Leakage Current	$I_R$	$V_{RWM} = 5V$ Any I/O pin to ground			200	nA
Clamping Voltage	$V_C$	$I_{PP}=10A, t_p=8/20\mu s$ Any I/O pin to ground		8	10	V
Dynamic Resistance <sup>1,2</sup>	$R_{DYN}$	TLP=0.2/100ns		0.2		$\Omega$
ESD Clamping Voltage <sup>1</sup>	$V_C$	$I_{PP} = 4A,$ $t_p = 0.2/100ns$ (TLP)		5.9		V
ESD Clamping Voltage <sup>1</sup>	$V_C$	$I_{PP} = 16A,$ $t_p = 0.2/100ns$ (TLP)		7.7		V
Junction Capacitance	$C_j$	$V_R = 0V, f = 1MHz$ I/O pin to GND		20	25	pF
		$V_R = 0V, f = 1MHz$ Between I/O pins		10	15	pF

Notes : 1、 TLP Setting :  $t_p=100ns, t_r=0.2ns, I_{TLP}$  and  $V_{TLP}$  sample window: $t_1=70ns$  to  $t_2=90ns$ .  
 2、 Dynamic resistance calculated from  $I_{PP}=4A$  to  $I_{PP}=16A$  using "Best Fit".

Typical Characteristics

Figure 1: Peak Pulse Power Vs Pulse Time

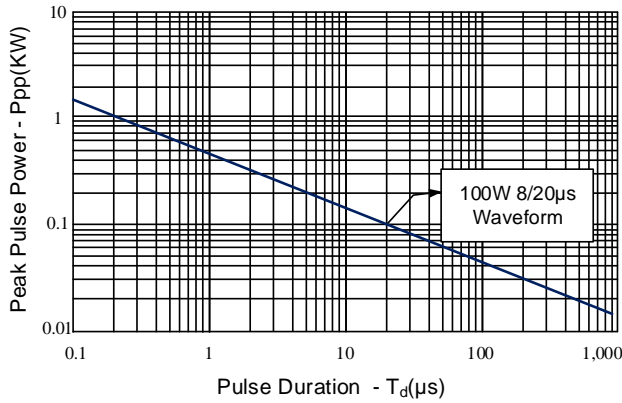


Figure 2: Power Derating Curve

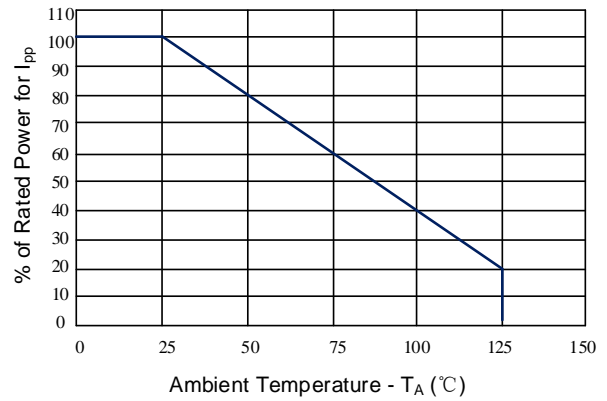


Figure 3: Clamping Voltage vs. Peak Pulse Current

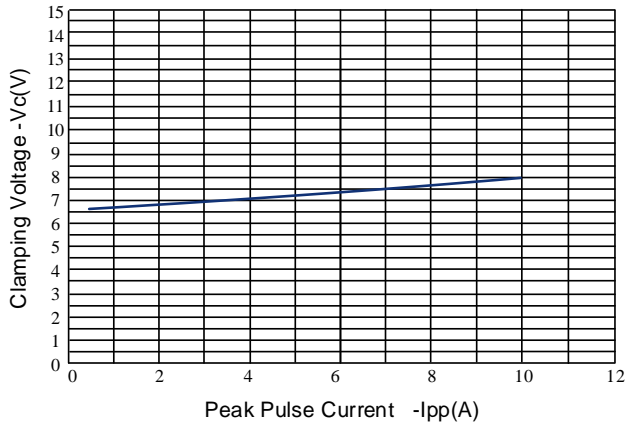


Figure 4: Normalized Junction Capacitance vs. Reverse Voltage

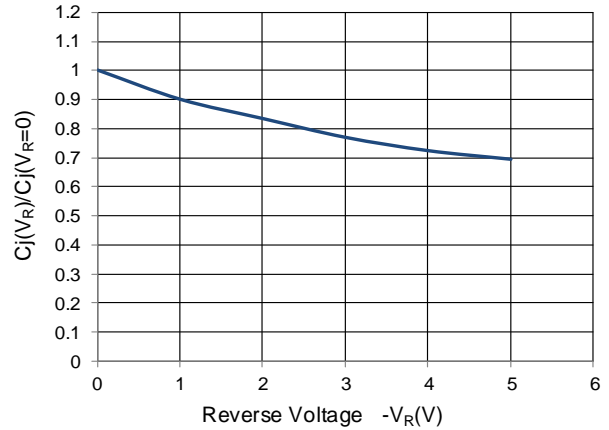


Figure 5: TLP Positive I-V Curve

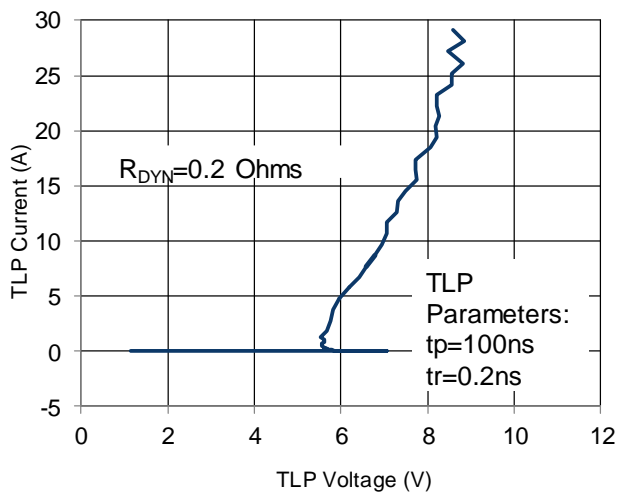
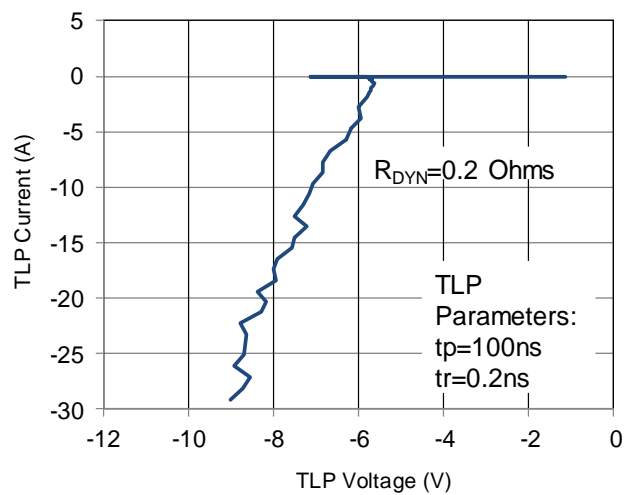
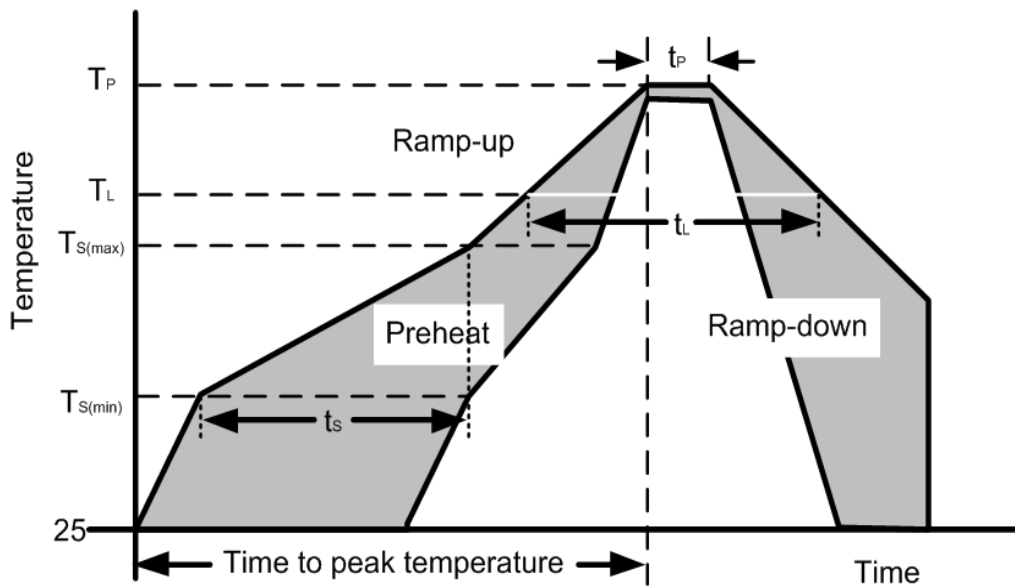


Figure 6: TLP Negative I-V Curve



Soldering Parameters

Reflow Condition		Pb – Free assembly
Pre Heat	Temperature Min ( $T_{S(min)}$ )	150°C
	Temperature Max ( $T_{S(max)}$ )	200°C
	Time (min to max) ( $t_s$ )	60 – 190 secs
Average ramp up rate (Liquidus Temp) ( $T_L$ ) to peak		5°C/second max
$T_{S(max)}$ to $T_L$ —Ramp-up Rate		5°C/second max
Reflow	Temperature ( $T_L$ ) (Liquidus)	217°C
	Temperature ( $t_L$ )	60 – 150 seconds
Peak Temperature ( $T_P$ )		260+0/-5 °C
Time within actual peak Temperature ( $t_p$ )		20 – 40 seconds
Ramp-down Rate		5°C/second max
Time 25°C to peak Temperature ( $T_P$ )		8 minutes Max.
Do not exceed		280°C



Outline Drawing – SOT-23-5L

**PACKAGE OUTLINE**

**SOT-23-5L**

Symbol	MILLIMETERS	
	MIN	MAX
L	2.82	3.02
B	1.50	1.70
C	0.90	1.30
L1	2.60	3.00
E	1.80	2.00
E1	0.85	1.05
α	0.35	0.50
c	0.10	0.20
b	0.35	0.55
F	0	0.15

DIMENSIONS		
DIM	INCHES	MILLIMETERS
C	0.098	2.50
G	0.055	1.40
P	0.037	0.95
X	0.024	0.60
Y	0.043	1.10
Z	0.141	3.60

**Notes:**  
Controlling Dimension: Millimeter.

**Marking Codes**

Part Number	WE05-4HT5
Marking Code	

**Package Information**

Qty: 3k/Reel

**CONTACT INFORMATION**

No.1001, Shiwan (7) Road, Pudong District, Shanghai, P.R.China.201207

Tel: 86-21-68969993 Fax: 86-21-50757680 Email: [market@way-on.com](mailto:market@way-on.com)

WAYON website: <http://www.way-on.com>

For additional information, please contact your local Sales Representative.

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Specifications are subject to change without notice.  
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.  
Users should verify actual device performance in their specific applications.

单击下面可查看定价，库存，交付和生命周期等信息

[>>WAY-ON\(维安\)](#)