# WP25P21XX-B

# **Adjustable Current Limited Load Switch**

#### **General Description**

The WP25P21XX-B is current limited P-channel MOSFET power switch designed for high-side load switching applications. This switch operates with inputs ranging from 2.5V to 5.5V, making it ideal for both 3.3V and 5V systems. An integrated current-limiting circuit protects the input supply against large currents which may cause the supply to fall out of regulation. The WP25P21XX-B is also protected from thermal overload which limits power dissipation and junction temperatures. It can be used to control loads that require from 0.4A to 2A. Current limit threshold is programmed with a resistor from SET to ground. The quiescent supply current in active mode is only 28µA. In shutdown mode, the supply current decreases to less than 1µA.

The WP25P21XX-B is available in Pb-free packages and is specified over the -40°C to +85°C ambient temperature range.

#### **Features**

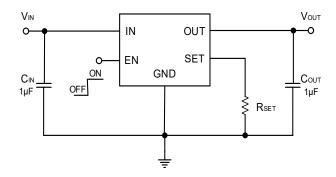
- Input Voltage Range: 2.5V to 5.5V
- Programmable Current Limit (0.4A to 2A)
- Reverse Current Blocking

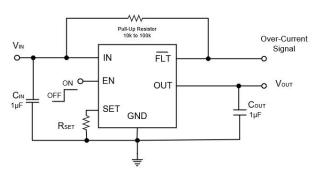
- Short-Circuit Response: 2µs
- Very Low Quiescent Current: 28µA (Typ)
- 1µA Max Shutdown Supply Current
- Built-in Pull-up Resistor for EN Pin
- Automatic Output Discharge at Shutdown
- Under-Voltage Lockout
- Thermal Shutdown
- 2kV ESD Rating
- Ambient Temperature Range: -40°C to +85°C

#### **Applications**

- Laptop/Desktop Computers and Netbooks
- 3G Wireless Cards
- Smart Phones and PDAs
- LCD TVs and Monitors
- Set-Top-Boxes
- MP3/MP4
- Printers
- Portable Game Players
- Portable Media Players and MIDs
- USB Keyboards
- USB Hard Disk Drives
- USB Memory Drives
- USB Hubs

#### **Typical Application**



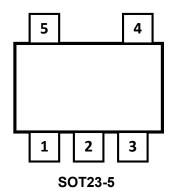


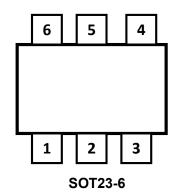
Note: Tantalum or Aluminum Electrolytic capacitors ( $C_{\text{IN}}$  and  $C_{\text{OUT}}$ ) may be required for USB applications

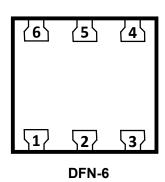


## Pin Configuration

(Top View)







### **Pin Description**

Pin Number			<b>-</b>	
SOT23-5	SOT23-6	DFN-6	Pin Name	Pin Function
1	1	2	OUT	Power output.
2	2	3	GND	Ground pin.
3	3	1	SET	Current limit programming pin, with a resistor from ISET to ground.
4	4	6	EN	Enable input, High enable.
5	6	4	IN	Power supply input.
-	5	5	FLT	Open drain fault flag.



## **Absolute Maximum Ratings**

Paramete	er	Rating	Unit
IN, EN, SET V	oltage	-0.3 to 6	V
OUT Volta	ge	-0.3 to V <sub>IN</sub> + 0.3	V
OUT Curre	ent	ADJ	Α
	SOT23-5	400	
Power Dissipation	SOT23-6	400	mW
	DFN-6	606	
	SOT23-5	250	
Package Thermal	SOT23-6	250	°C/W
Resistance(θ <sub>JA</sub> )	DFN-6	165	
Operating Junction 1	emperature	-40 to 125	°C
Storage Tempe	erature	-55 to 150	°C
Lead Temperature (Sol	dering, 10 sec)	300	°C



#### **Electrical Characteristics**

( $V_{IN}$ =+5.0V,  $T_A$ =25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
V <sub>IN</sub>	Input Voltage Range		2.5		5.5	V
I <sub>SHDN</sub>	Shutdown Input Current	Shutdown Input Current Disabled, OUT floating or shorted to ground		0.3	1	μΑ
$I_Q$	Quiescent Supply Current	Enabled, I <sub>OUT</sub> = 0A		28	60	μA
$R_{DS(ON)}$	Switch on-resistance	$V_{IN} = 5V, I_{OUT} = 0.6A$		80	120	mΩ
		$V_{IN}$ = 5V, $V_{OUT}$ = 4.5V, $R_{SET}$ =10k $\Omega$	1600	2000	2400	mA
		$V_{IN}$ = 5V, $V_{OUT}$ = 4.5V, $R_{SET}$ =12.7k $\Omega$	1200	1500	1800	mA
	Current Limit	$V_{IN} = 5V$ , $V_{OUT} = 4.5V$ , $R_{SET} = 19.6k\Omega$	800	1000	1200	mA
		$V_{IN}$ = 5V, $V_{OUT}$ = 4.5V, $R_{SET}$ =47k $\Omega$	320	400	480	mA
I <sub>LMT</sub>		$V_{IN}$ = 5V, $V_{OUT}$ = 4.5V, open $R_{SET}$	800	1000	1200	mA
ILMI		$V_{IN} = 5V$ , $V_{OUT} = 0V$ , $R_{SET} = 10k\Omega$	1800	2300	2800	mA
	Current Limit	$V_{IN}$ = 5V, $V_{OUT}$ = 0V, $R_{SET}$ =12.7k $\Omega$	1500	1900	2300	mA
	(short circuit)	$V_{IN}$ = 5V, $V_{OUT}$ = 0V, $R_{SET}$ =19.6k $\Omega$	900	1200	1500	mA
	(chart on out)	$V_{IN}$ = 5V, $V_{OUT}$ = 0V, $R_{SET}$ =47k $\Omega$	400	500	600	mA
		$V_{IN}$ = 5V, $V_{OUT}$ = 0V, open $R_{SET}$	900	1200	1500	mA
$V_{IL}$	EN Input Logic Low Voltage	Note 1			0.5	V
V <sub>IH</sub>	EN Input Logic High Voltage	Note 1	1.5			V
	ENI (I I	V <sub>EN</sub> = 5V		0.01	1	μA
I <sub>SINK</sub>	EN Input leakage	V <sub>EN</sub> =0V	-2	-0.25		μA
$V_{UVLO}$	Input UVLO Threshold		1.4	1.8	2.2	V
V <sub>UVLOHys</sub>	Input UVLO Hysteresis			0.1		V
I <sub>REV</sub>	Reverse Leakage Current	$V_{IN}$ = 0V, $V_{OUT}$ = 5V, $I_{REV}$ at $V_{IN}$		0.1	1	μA
Rdischarge	OUT Shutdown Discharge Resistance	Disabled, V <sub>IN</sub> = 5V, V <sub>OUT</sub> =1V	50	120	300	Ω
$R_{\overline{FLT}}$	FLT Low Resistance			250		Ω
T <sub>FLT_Delay</sub>	FLT Delay Time			15		ms
T <sub>SHDN</sub>	Thermal Shutdown Temperature	V <sub>IN</sub> = 5V		150		°C
T <sub>HYS</sub>	Thermal Shutdown Hysteresis	V <sub>IN</sub> = 5V		20		°C
T <sub>ON</sub>	Output turn-on delay time	$V_{IN}=5V$ , $C_L=1\mu F$ , $R_L=100\Omega$	0.2	0.5	1	ms
T <sub>R</sub>	Output turn-on rise time	$V_{IN}=5V$ , $C_L=1\mu F$ , $R_L=100\Omega$	0.2	0.4	0.8	ms
T <sub>OFF</sub>	Output turn-off delay time	$V_{IN}=5V$ , $C_L=1\mu F$ , $R_L=100\Omega$	0.2	0.5	0.8	ms



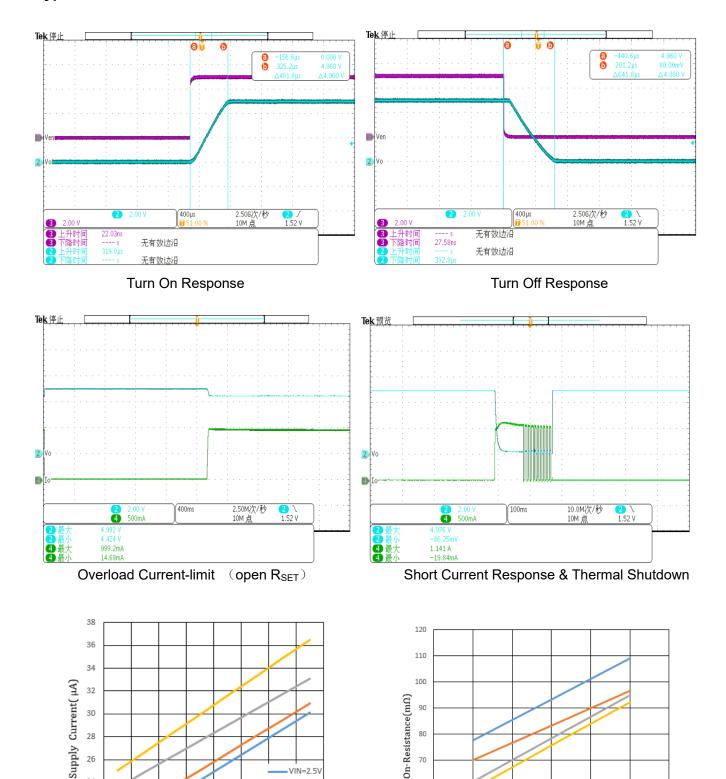
# **WP25P21XX-B**

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
$T_F$	Output turn-off fall time	$V_{IN}$ =5V, $C_L$ =1 $\mu$ F, $R_L$ =100 $\Omega$	100	350	500	μs
ESD HBM	Human Body Model ESD					
EOD UDIVI	Protection			2000		V

Note1: When EN pin is floating, the chip is enabled.



# WP25P21XX-B Typical Performance Characteristics



Quiescent Supply Current vs. Ambient Temperature

25

40

Ambient Temperature (°C)

28 26

22

20

-40

Switch On-Resistance vs. Ambient Temperature

Ambient Temperature (°C)

VIN=3.3V

VIN=5V

VIN=5.5V

VIN=2.5\

VIN=5.5V

85

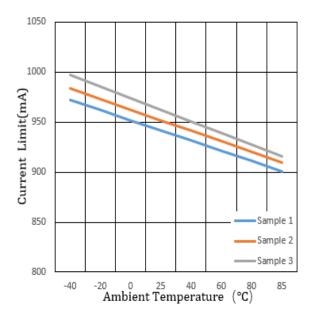
80

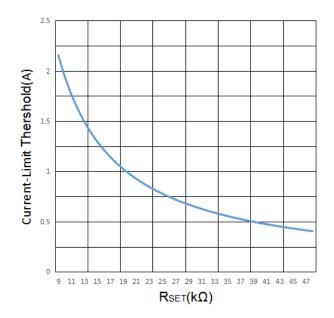
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60

50





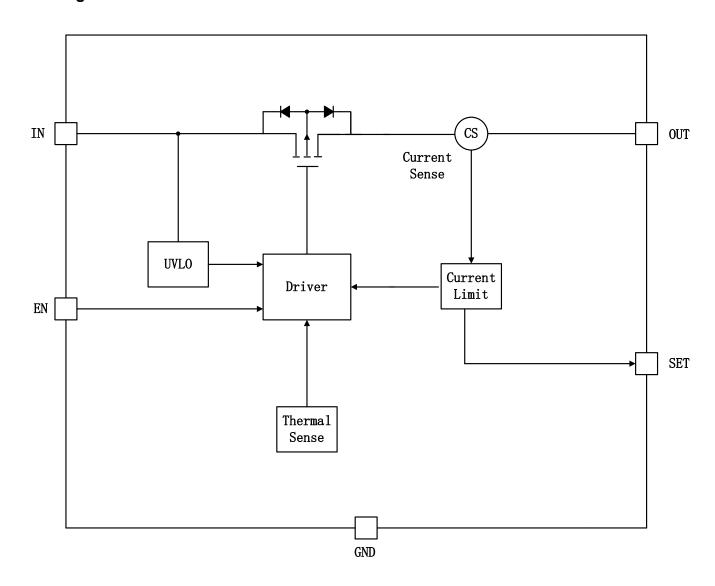


Current-Limit Threshold vs. Ambient Temperature  $(open R_{SET})$ 

Current-Limit Threshold vs. R<sub>SET</sub>



## **Block Diagram**





#### Operation

WP25P21XX-B is an integrated power switch with a low R<sub>DS(ON)</sub> P-channel MOSFET, internal gate rive circuit, programmable current limiting. When the WP25P21XX-B turns on, it can deliver up to 1.4A continuous current to load. When the device is active, if there is no load, the device only consumes 28µA supply current, which makes the device suitable for battery powered applications.

#### **Power Supply Considerations**

A 0.1µF to 1µF ceramic bypass capacitor between IN and GND, close to the device, is recommended. Placing a high-value electrolytic capacitor on the output pin(s) is recommended when the output load is heavy. This precaution reduces power-supply transients that may cause ringing on the input and minimize the input voltage droops. Additionally, bypassing the output with a 0.1µF to 1µF ceramic capacitor improves the immunity of the device to short-circuit transients.

#### **Power Dissipation and Junction Temperature**

The low on-resistance on the P-channel MOSFET allows the small surface-mount packages to pass large currents. It is good design practice to check power dissipation and junction temperature for each application. Begin by determining the RDS(ON) of the P-channel MOSFET relative to the input voltage and operating temperature. Using the highest operating ambient temperature of interest and RDS(ON), the power dissipation per switch can be calculated by:

$$P_D = R_{DS(ON)} \times I^2$$

Finally, calculate the junction temperature:

$$T_J = P_D x R_{\theta JA} + T_A$$

Where:

T<sub>A</sub>= Ambient temperature

 $R_{\theta JA}$  = Thermal resistance

P<sub>D</sub> = Total power dissipation

Compare the calculated junction temperature with the maximum junction temperature which is 125°C. If they are within degrees, either the maximum load current needs to be reduced or another package option will be required.

#### **Over Current**

A sense FET is employed to check for overcurrent conditions. When an overcurrent condition is detected, the device maintains a constant output current and reduces the output voltage accordingly. WP25P21XX-B will limit the current until the overload condition is removed or the device begins to thermal cycle.

Three possible overload conditions can occur. In the first condition, the output has been shorted before the device is enabled or before  $V_{IN}$  has been applied. The WP25P21XX-B senses the short and immediately switches into a constant-current output. In the second condition, a short or an overload occurs while the device is enabled. At the instant the overload occurs, high currents may flow for a short period of time before the current-limit circuit can react.

After the current-limit circuit reached the overcurrent trip threshold, the device switches into constant-current mode.

In the third condition, the load has been gradually increased beyond the recommended operating current. The current is permitted to rise until the current-limit threshold is reached or until the thermal limit of the device is exceeded. The WP25P21XX-B is capable of delivering current up to the current-limit threshold without damaging the device. Once the threshold has been reached, the device switches into its constant-current mode.

#### Thermal Protection

## **WP25P21XX-B**



Thermal protection prevents damage to the IC when heavy-overload or short-circuit faults are present for extended periods of time. The WP25P21XX-B implements a thermal sensing to monitor the operating junction temperature of the power distribution switch. In an overcurrent or short-circuit condition, the junction temperature rises due to excessive power dissipation. Once the die temperature rises to approximately 150°C due to overcurrent conditions, the internal thermal sense circuitry turns the power switch off, thus preventing the power switch from damage. Hysteresis is built into the thermal sense circuit, and after the device has cooled approximately 20°C, the switch turns back on. The switch continues to cycle in this manner until the load fault or input power is removed.

#### **Current Limiting Setting**

Current limit is programmable to protect the power source from over current and short circuit conditions. Connect the closest 1% resistor RSET from SET pin to GND to program the current limit:

$$I_{LIM}(A) = 19400 / R_{SET}(\Omega)$$

The minimum current limit is 0.4A. Current limit beyond 2A is not recommended.

When R<sub>SET</sub> =  $\infty$  (SET Pin is floating), the current limit is 0.8A $\sim$ 1.2A.

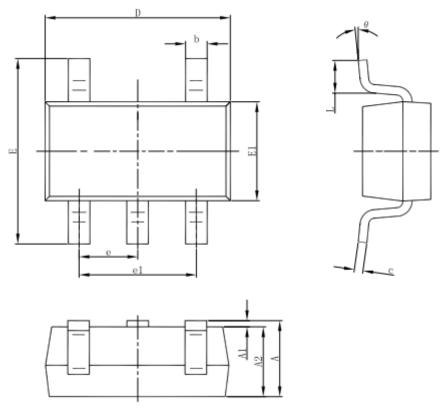




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## Package Information

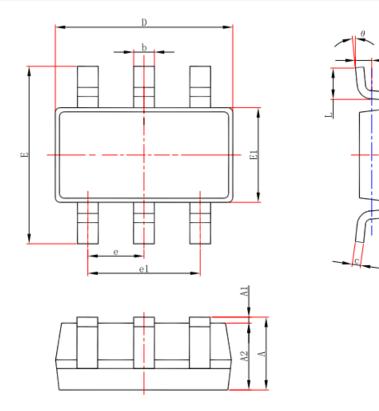


SOT23-5

OVMPOL	DIMENSIONS IN MILLIMETERS		
SYMBOL	MIN	MAX	
Α	1.000	1.350	
<b>A</b> 1	0.000	0.150	
A2	1.000	1.200	
b	0.300	0.500	
С	0.100	0.200	
D	2.820	3.020	
E1	1.500	1.700	
E	2.600	3.000	
е	0.950	(BSC)	
e1	1.800	2.000	
L	0.300	0.600	
θ	0°	8°	

0.200

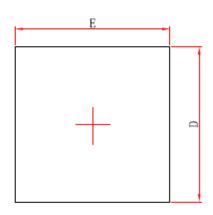




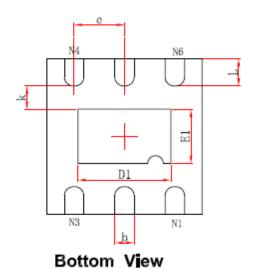
SOT23-6

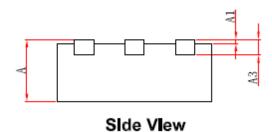
OVMPOL	DIMENSIONS IN MILLIMETERS			
SYMBOL	MIN	MAX		
Α	1.050	1.250		
<b>A</b> 1	0.000	0.100		
A2	1.050	1.150		
b	0.300	0.500		
С	0.100	0.200		
D	2.820	3.020		
E1	1.500	1.700		
E	2.650	2.950		
е	0.950	(BSC)		
e1	1.800	2.000		
L	0.300	0.600		
θ	0°	8°		





Top Vlew





DFN-6

	DIMENSIONS IN MILLIMETERS			
SYMBOL	MIN	MAX		
Α	0.700	0.800		
<b>A</b> 1	0.000	0.050		
A3	0.203	BREF		
D	1.924	2.076		
E	1.924	2.076		
D1	1.100	1.300		
E1	0.600	0.800		
k	0.200MIN.			
b	0.200	0.300		
е	0.650TYP.			
L	0.274	0.426		



#### **Ordering Information**

Part Number	Current Limit	Package	Packing Quantity	Marking*
WP25P21T5-B	ADJ	SOT23-5	3k/Reel	WP25P21 B CXXXX
WP25P21T6-B	ADJ	SOT23-6	3k/Reel	WP25P21 B CXXXX
WP25P21F6-B	ADJ	DFN-6	3k/Reel	25P21 BXXXX

<sup>\*</sup>XXXX is variable.

#### **Contact Information**

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For additional information, please contact your local Sales Representative.

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Specifications are subject to change without notice.

The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.

Users should verify actual device performance in their specific applications.

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