Product data sheet

1. General description

Planar passivated four quadrant triac in a TO252 (DPAK) surface-mountable plastic package intended for use in bidirectional switching and phase control applications.

2. Features and benefits

- · High blocking voltage capability
- Less sensitive gate for improved noise immunity
- Planar passivated for voltage ruggedness and reliability
- Surface-mountable package
- Triggering in all four quadrants

3. Applications

- General purpose motor control
- · General purpose switching

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Absolute	maximum rating					
V_{DRM}	repetitive peak off-state voltage		-	-	600	V
I _{T(RMS)}	RMS on-state current	full sine wave; $T_{mb} \le 102 ^{\circ}\text{C}$; Fig. 1; Fig. 2; Fig. 3	-	-	8	А
I _{TSM} non-repetitive peak on- state current		full sine wave; $T_{j(init)}$ = 25 °C; t_p = 20 ms; Fig. 4; Fig. 5	-	-	65	А
		full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 16.7 \text{ ms}$	-	-	71	Α
T _j	junction temperature		-40	-	125	°C
Static ch	aracteristics			'		
I _{GT}	gate trigger current	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2+ \text{ G+;}$ $T_J = 25 \text{ °C; } Fig. 7$	-	5	35	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + G-;$ $T_j = 25 \text{ °C; } Fig. 7$	-	8	35	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2- G-;$ $T_j = 25 \text{ °C}; Fig. 7$	-	11	35	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; T2- G+;}$ $T_j = 25 \text{ °C; Fig. 7}$	-	30	70	mA

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _H	holding current	$V_D = 12 \text{ V}; T_j = 25 \text{ °C}; Fig. 9$	-	2.5	20	mA
V _T	on-state voltage	I _T = 10 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.3	1.65	V
Dynamic	characteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T_j = 125 °C; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit	100	250	-	V/µs
dV _{com} /dt	rate of change of commutating voltage	$V_D = 400 \text{ V}; T_j = 95 \text{ °C}; dI_{com}/dt = 3.6 \text{ A/}$ ms; $I_T = 8 \text{ A};$ gate open circuit	-	20	-	V/µs

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	Γ	Z
2	T2	main terminal 2	(8)	T2 T1
3	G	gate		sym051
mb	T2	mounting base; main terminal 2		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
BT137S-600	TO252	BT137S-600,118	Reel	2500	TO252N	14-Nov-2016

7. Marking

Table 4. Marking codes

Type number	Marking codes
BT137S-600	137S6

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	600	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; T _{mb} ≤ 102 °C; Fig 1; Fig 2; Fig 3	-	8	А
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25$ °C; $t_p = 20$ ms; Fig 4; Fig 5	-	65	А
		full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 16.7 \text{ ms}$	-	71	Α
l ² t	I ² t for fusing	t _p = 10 ms; SIN	-	21	A ² s
dl _⊤ /dt	rate of rise of on-state current	I _G = 140 mA	-	50	A/µs
I _{GM}	peak gate current		-	2	Α
P_GM	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
T _j	junction temperature		-40	125	°C

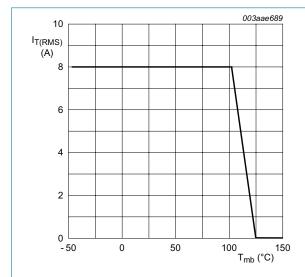
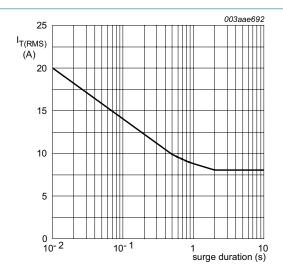
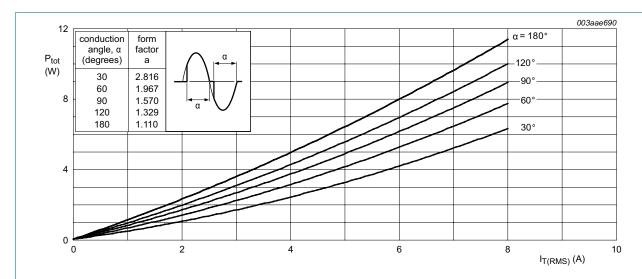


Fig. 1. RMS on-state current as a function of mounting base temperature; maximum values



 $f = 50 \text{ Hz}; T_{mb} \le 102 \text{ }^{\circ}\text{C}$

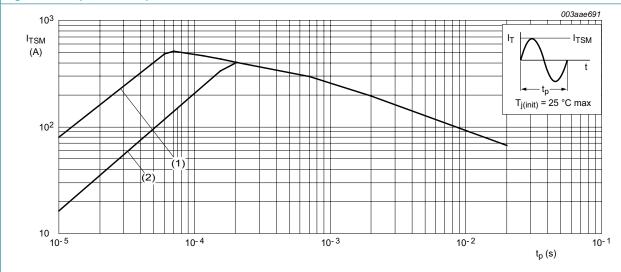
Fig. 2. RMS on-state current as a function of surge duration; maximum values



 α = conduction angle

a = form factor = $I_{T(RMS)}/I_{T(AV)}$

Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values



 $t_p \le 20 \text{ ms}$

(1) dl_⊤/dt limit

(2) T2- G+ quadrant limit

Fig. 4. Non-repetitive peak on-state current as a function of pulse width; maximum values

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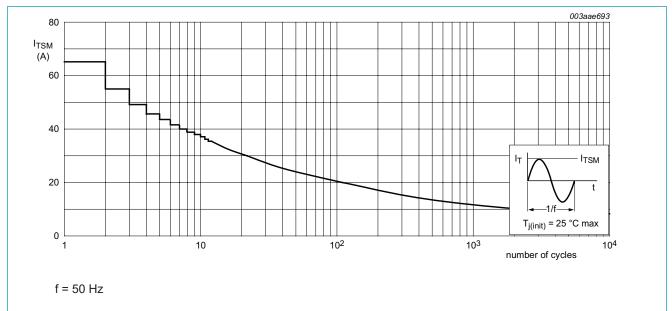
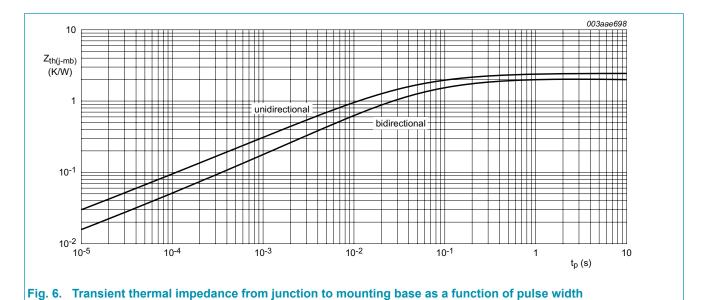


Fig. 5. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

9. Thermal characteristics

Table 6. Thermal characteristics

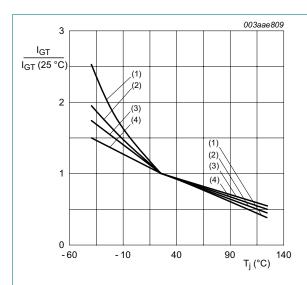
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance	half cycle; Fig 6	-	-	2.4	K/W
from junction to mounting base		full cycle; Fig 6	-	-	2	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	PCB (FR4) mounted; minimum pad sizes	-	75	-	K/W



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics		·			
I _{GT}	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; Fig. 7$	-	5	35	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ \text{ G-};$ $T_j = 25 \text{ °C}; Fig. 7$	-	8	35	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2- \text{G-};$ $T_j = 25 \text{ °C}; Fig. 7$	-	11	35	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2- G+;$ $T_j = 25 \text{ °C}; Fig. 7$	-	30	70	mA
I _L	latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; Fig. 8$	-	7	30	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G-;$ $T_j = 25 \text{ °C}; Fig. 8$	-	16	45	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 ^{\circ}\text{C}; \text{ Fig. 8}$	-	5	30	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G+};$ $T_j = 25 ^{\circ}\text{C}; \text{ Fig. 8}$	-	7	45	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	5	20	mA
V _T	on-state voltage	I _T = 10 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.3	1.65	V
V _{GT} gate trigger voltage		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 11	-	0.7	1	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 \text{ °C}$	0.25	0.4	-	V
I _D	off-state current	V _D = 600 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic	characteristics		'			
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T_j = 125 °C; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit	100	250	-	V/µs
dV _{com} /dt	rate of change of commutating voltage	V_D = 400 V; T_j = 95 °C; dI_{com}/dt = 3.6 A/ms; I_T = 8 A; gate open circuit	-	20	-	V/µs
t _{gt}	gate-controlled turn-on time	$I_{TM} = 12 \text{ A}; V_D = 600 \text{ V}; I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu\text{s}$	-	2	-	μs



- (1) T2- G+
- (2) T2- G-
- (3) T2+ G-
- (4) T2+ G+

Fig. 7. Normalized gate trigger current as a function of junction temperature

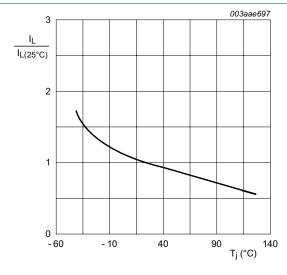


Fig. 8. Normalized latching current as a function of junction temperature

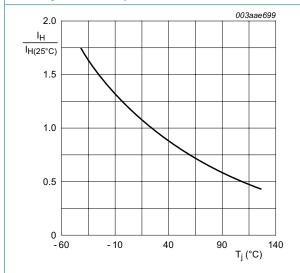
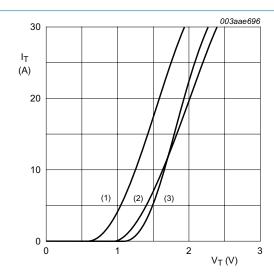


Fig. 9. Normalized holding current as a function of junction temperature

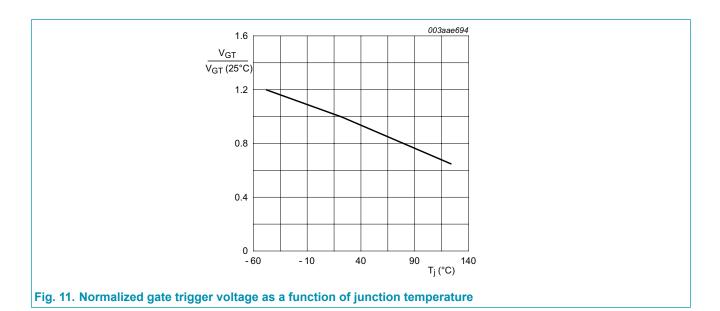


 V_o = 1.264 V; R_s = 0.038 Ω

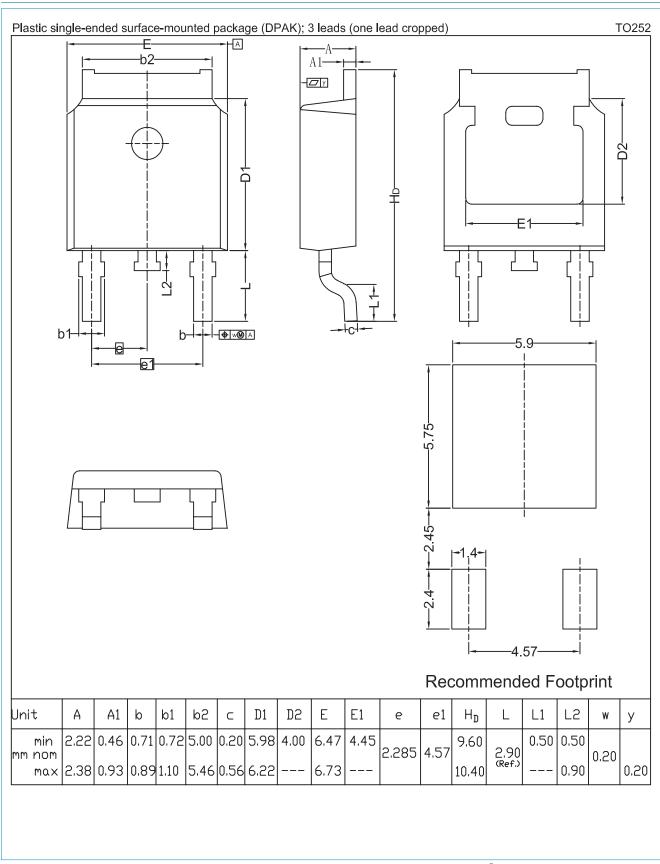
(1) T_j = 125 °C; typical values (2) T_j = 125 °C; maximum values

(3) T_i = 25 °C; maximum values

Fig. 10. On-state current as a function of on-state voltage



11. Package outline



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12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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