

General Description

The WSP4608 is the highest performance trench N-ch and P-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

Features

- Advanced high cell density Trench technology
- Gate Charge Super Low
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

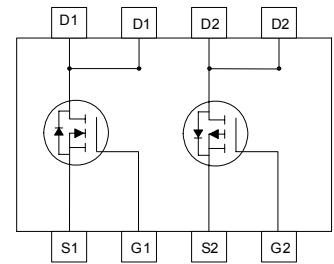
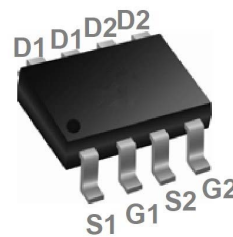
Product Summary

BVDSS	RDSON	ID
30V	22mΩ	6.4A
-30V	28mΩ	-5.6A

Applications

- Power management in half bridge and inverters
- DC-DC Converter
- Load Switch

SOP-8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	7	-6	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	6	-6	A
I_{DM}	Pulsed Drain Current ²	30	-30	A
EAS	Single Pulse Avalanche Energy ³	72	59	mJ
I_{AS}	Avalanche Current	21	-19	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	1	1	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	83	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	51	$^\circ C/W$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30	33	---	V
ΔBV _{DSS} /ΔT _J	BVDSS Temperature Coefficient	Reference to 25 °C, I _D =1mA	---	0.035	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =6.4A	---	22	---	mΩ
		V _{GS} =4.5V, I _D =5.2A	---	34	---	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	---	1.5	---	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-5.8	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V, V _{GS} =0V, T _J =25 °C	---	---	1	uA
		V _{DS} =24V, V _{GS} =0V, T _J =55 °C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =15V, I _D =5A	---	7.3	---	S
R _g	Gate Resistance	V _{DS} =20V, V _{GS} =0V, f=1MHz	---	2.4	---	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =20V, V _{GS} =4.5V, I _D =6A	---	7.2	---	nC
Q _{gs}	Gate-Source Charge		---	1.4	---	
Q _{gd}	Gate-Drain Charge		---	2.2	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V, V _{GS} =10V, R _G =3Ω I _D =5A	---	4.1	---	ns
T _r	Rise Time		---	9.8	---	
T _{d(off)}	Turn-Off Delay Time		---	15.5	---	
T _f	Fall Time		---	6.0	---	
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	407	---	pF
C _{oss}	Output Capacitance		---	113	---	
C _{rss}	Reverse Transfer Capacitance		---	57	---	

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V, L=0.1mH, I _{AS} =10A	16	---	---	mJ

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V _G =V _D =0V, Force Current	---	---	6	A
I _{SM}	Pulsed Source Current ^{2,6}		---	---	23	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =5A, T _J =25 °C	---	---	1.1	V

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper, t<10sec.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=10A
- The power dissipation is limited by 150 °C junction temperature
- The Min. value is 100% EAS tested guarantee.
- The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-30	-34	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.085	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-5.6A	---	28	---	mΩ
		V _{GS} =-4.5V, I _D =-5.05A	---	36	---	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	---	-1.5	---	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	0.375	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-24V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =-24V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±24V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-6A	---	12	---	S
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-20V, V _{GS} =-4.5V, I _D =-6A	---	6.4	---	nC
Q _{gs}	Gate-Source Charge		---	2.7	---	
Q _{gd}	Gate-Drain Charge		---	3.1	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-15V, V _{GS} =-10V, R _G =3Ω, R _L =2.5Ω	---	8	---	ns
T _r	Rise Time		---	16.6	---	
T _{d(off)}	Turn-Off Delay Time		---	22	---	
T _f	Fall Time		---	21.6	---	
C _{iss}	Input Capacitance	V _{DS} =-15V, V _{GS} =0V, f=1MHz	---	950	---	pF
C _{oss}	Output Capacitance		---	137	---	
C _{rss}	Reverse Transfer Capacitance		---	118	---	

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =-25V, L=0.1mH, I _{AS} =-10A	16	---	---	mJ

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V _G =V _D =0V, Force Current	---	---	-6	A
I _{SM}	Pulsed Source Current ^{2,6}		---	---	-12	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =-6A, T _J =25°C	---	---	-1.2	V

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10sec.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
3. The EAS data shows Max. rating. The test condition is V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-10A
4. The power dissipation is limited by 150°C junction temperature
5. The Min. value is 100% EAS tested guarantee.
6. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

N-Channel Typical Characteristics

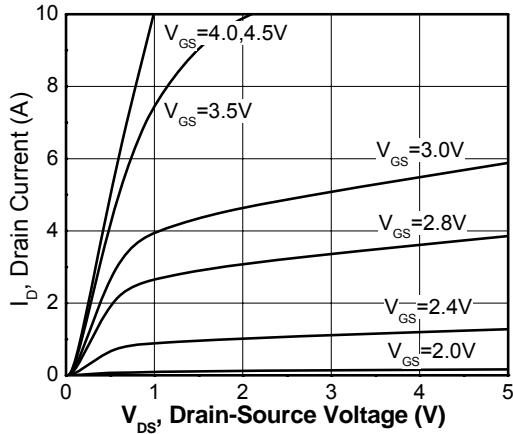


Figure 1. Output Characteristics

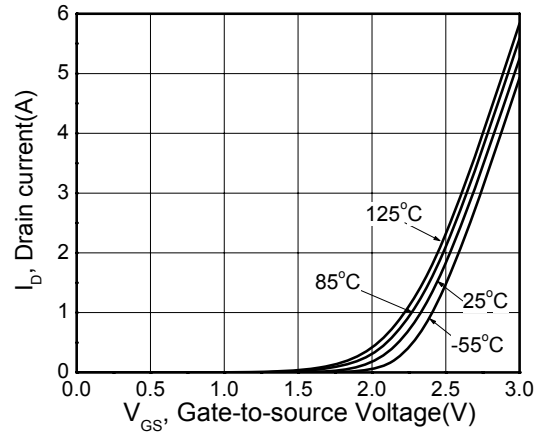


Figure 2. Transfer Characteristics

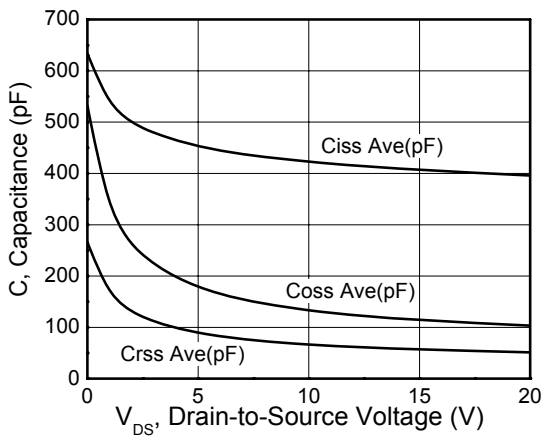


Figure 3. Capacitance

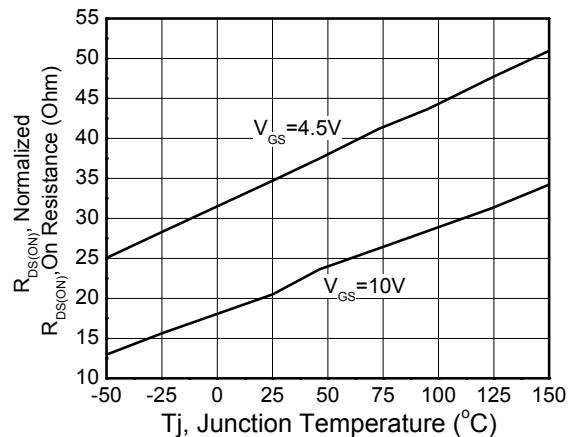


Figure 4. On Resistance Vs. Temperature

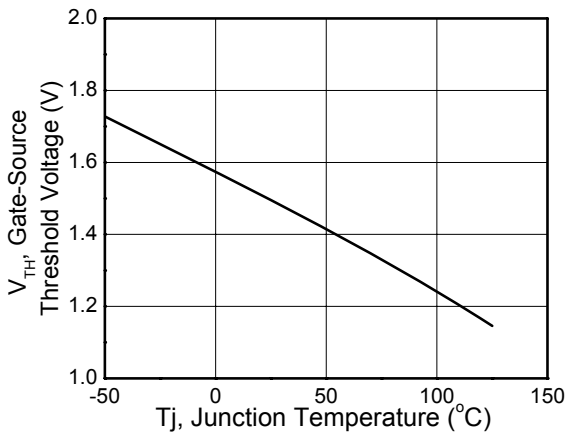


Figure 5. Gate Threshold Vs. Temperature

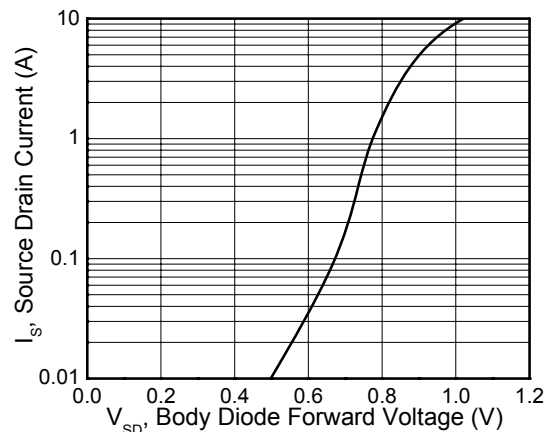


Figure 6. Body Diode Forward Voltage Vs. Source Current

P-Channel Typical Characteristics

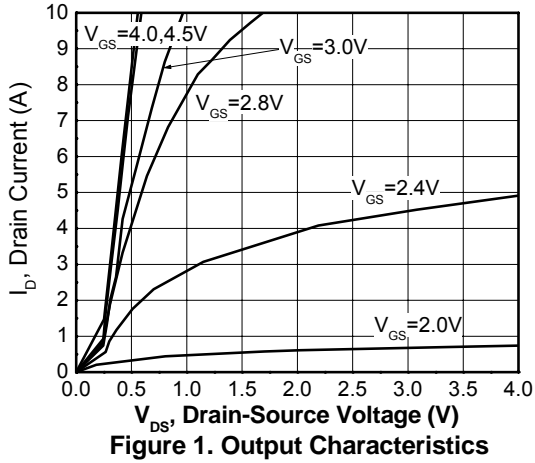


Figure 1. Output Characteristics

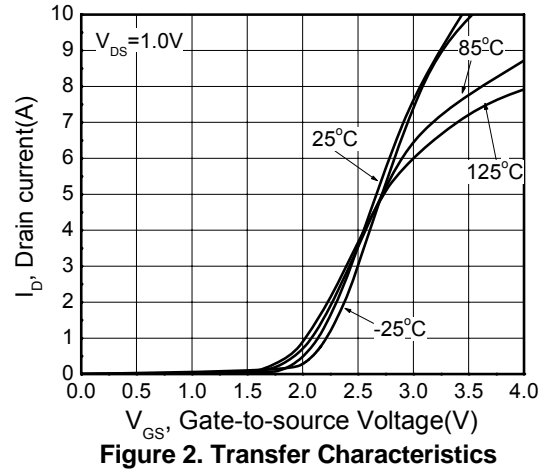


Figure 2. Transfer Characteristics

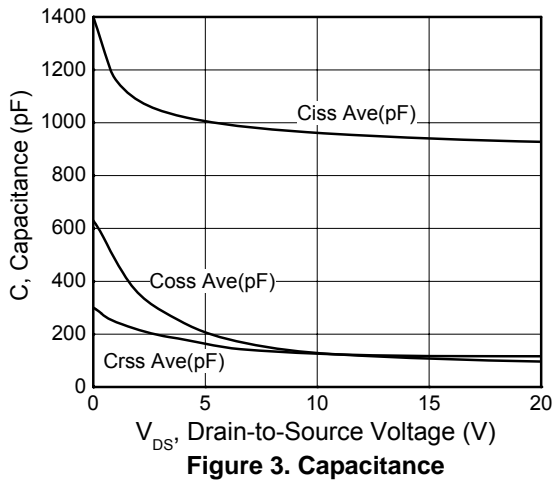


Figure 3. Capacitance

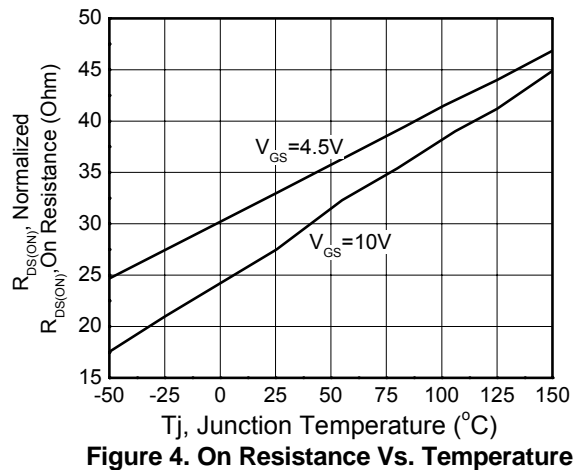


Figure 4. On Resistance Vs. Temperature

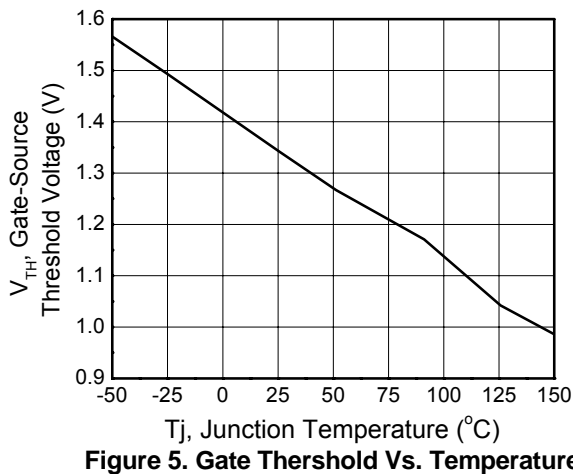


Figure 5. Gate Threshold Vs. Temperature

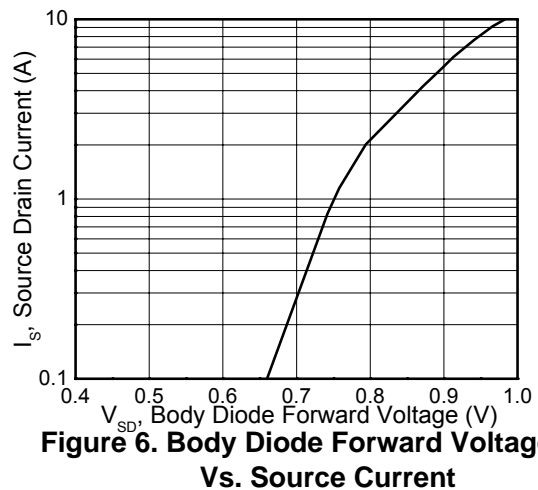


Figure 6. Body Diode Forward Voltage Vs. Source Current

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