

General Description

The WST2026 is the highest performance trench N-ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WST2026 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

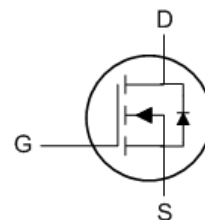
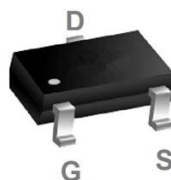
Product Summary

BVDSS	RDSON	ID
20V	65mΩ	2.0A

Applications

- High Frequency Point-of-Load Synchronous Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

SOT-323 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	2.0	A
$I_D@T_C=70^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	1.0	A
I_{DM}	Pulsed Drain Current ²	16	A
$P_D@T_A=25^\circ C$	Total Power Dissipation ³	0.4	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	125	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	85	$^\circ C/W$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20	---	---	V
ΔBV _{DSS} /ΔT _J	BVDSS Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.022	---	V/°C
R _{DS(on)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =2A	---	65	85	mΩ
		V _{GS} =4.5V, I _D =1A	---	80	100	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	0.5	0.8	1.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-2.33	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =16V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =16V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±12V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =2A	---	15	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.5	3	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =15V, V _{GS} =4.5V, I _D =2A	---	7.6	10	nC
Q _{gs}	Gate-Source Charge		---	1.3	2.0	
Q _{gd}	Gate-Drain Charge		---	1.7	3.8	
T _{d(on)}	Turn-On Delay Time	V _{DD} =10V, V _{GS} =4.5V, R _G =6.0Ω, I _D =2A	---	11	22	ns
T _r	Rise Time		---	3.2	6.5	
T _{d(off)}	Turn-Off Delay Time		---	22	45	
T _f	Fall Time		---	3.0	6.0	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	391	---	pF
C _{oss}	Output Capacitance		---	87	---	
C _{rss}	Reverse Transfer Capacitance		---	60	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	2.0	A
I _{SM}	Pulsed Source Current ^{2,4}		---	---	15	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.0	V
t _{rr}	Reverse Recovery Time	I _F =2A, di/dt=100A/μs, T _J =25°C	---	11	25	nS
Q _{rr}	Reverse Recovery Charge		---	6.8	14	nC

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10sec.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- The power dissipation is limited by 150°C junction temperature
- The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Characteristics

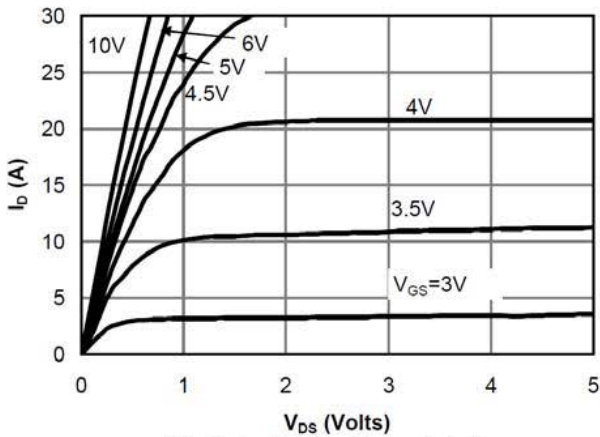


Fig 1: On-Region Characteristics

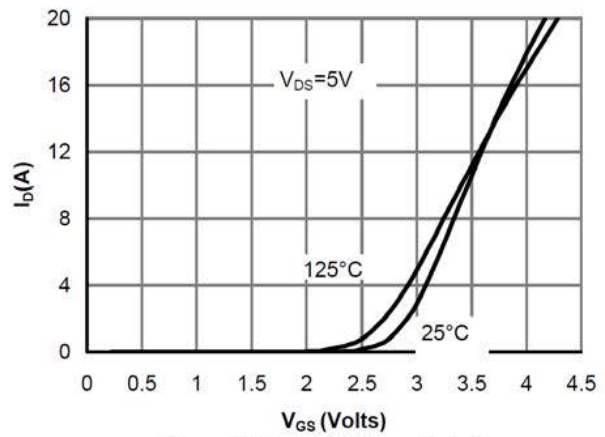


Figure 2: Transfer Characteristics

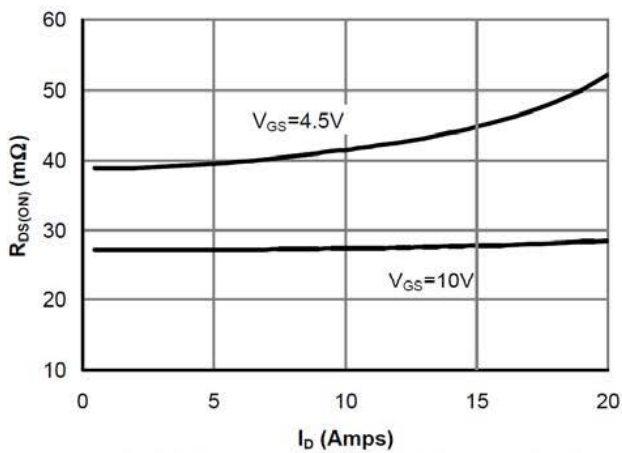


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

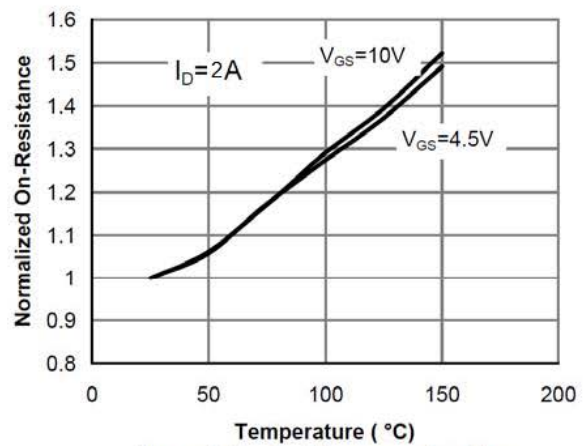


Figure 4: On-Resistance vs. Junction Temperature

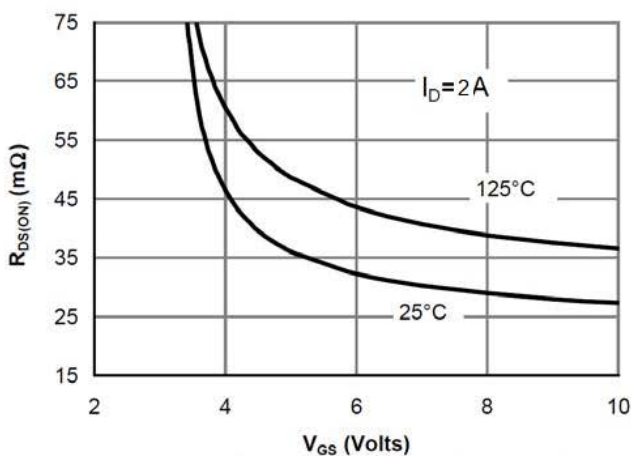


Figure 5: On-Resistance vs. Gate-Source Voltage

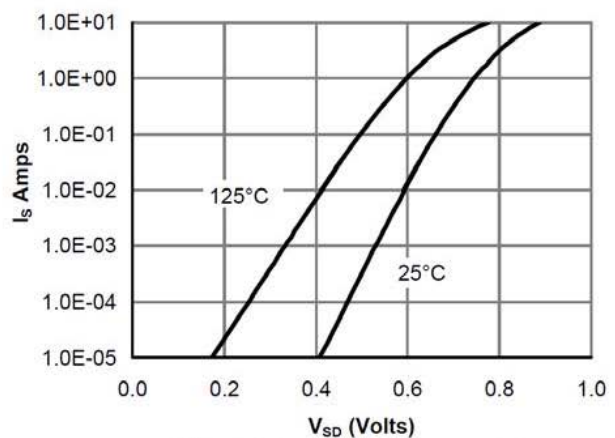


Figure 6: Body diode characteristics

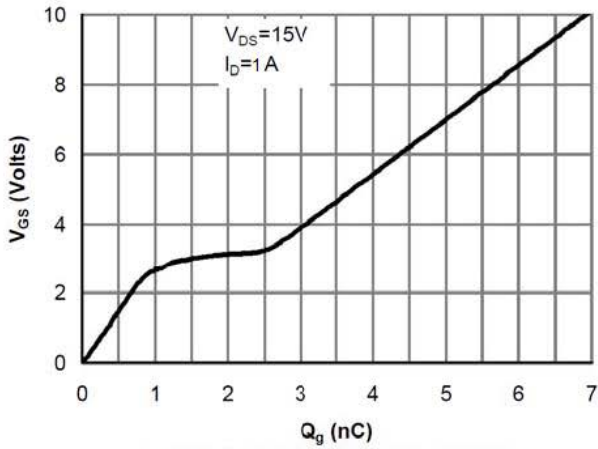


Figure 7: Gate-Charge characteristics

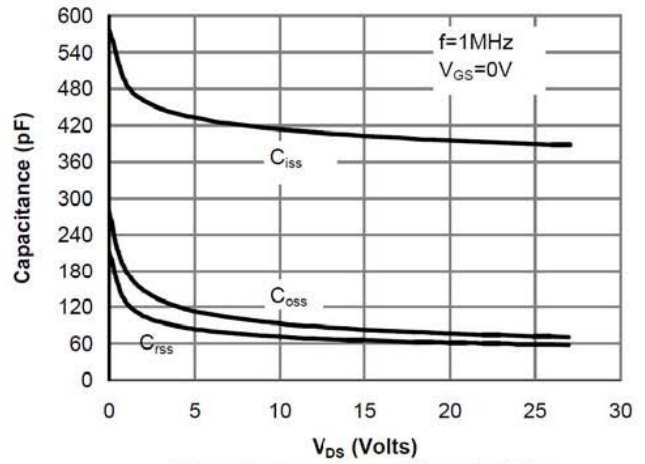


Figure 8: Capacitance Characteristics

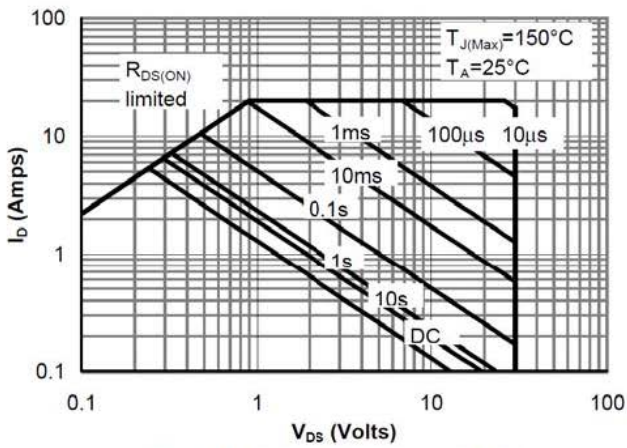


Figure 9: Maximum Forward Biased Safe Operating Area

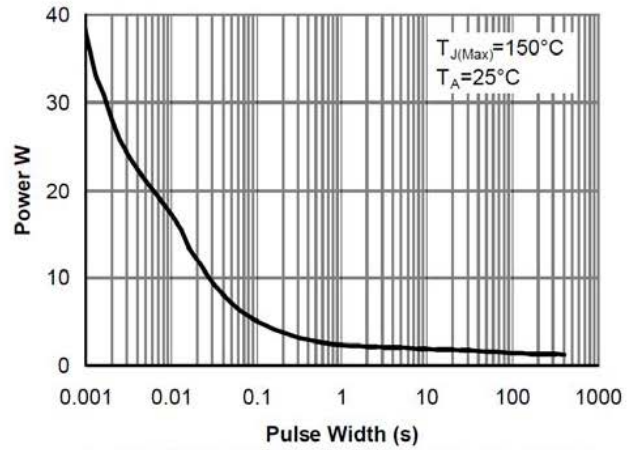


Figure 10: Single Pulse Power Rating Junction-to-Ambient

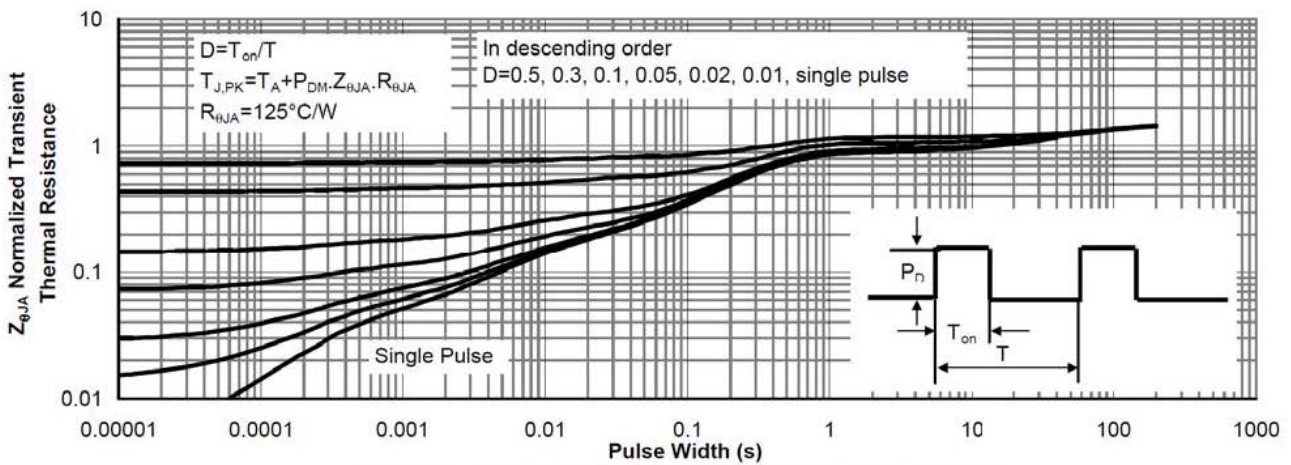


Figure 11: Normalized Maximum Transient Thermal Impedance

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