

General Description

The WST6008 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WST6008 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Low Gate Charge for Fast Switching
- Small 1.6 X 1.6 mm Footprint
- ESD Protected Gate
- We declare that the material of product is ROHS compliant and halogen free.

Product Summery

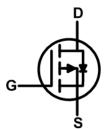
BVDSS	RDSON	ID
30V	140mΩ	154mA

Applications

- Power Management Load Switch
- Level Shift
- Portable Applications such as Cell Phones, Media Players, Digital Cameras, PDA's, Video Games, Hand Held Computers, etc.

SOT-523 Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±10	V
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	154	mA
I _D @T _A =70℃	Continuous Drain Current, V _{GS} @ 10V ¹	120	mA
I _{DM}	Pulsed Drain Current ²	618	mA
P _D @T _A =25°C	Total Power Dissipation ³	300	mW
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	C

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient ¹	416		°C/W

N-Ch MOSFET

Electrical Characteristics (T_J=25 C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =100uA	30			V	
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25℃ , I _D =1mA		0.05		V/°C	
В	Static Drain-Source On-Resistance ²	V _{GS} =4.5V , I _D =154mA		1.4	7.0	Ω	
R _{DS(ON)}	Static Dialit-Source Off-Resistance	V_{GS} =2.5 V , I_D =154 mA		2.3	7.5		
V _{GS(th)}	Gate Threshold Voltage	\/ -\/ -100\\A	0.5	1.0	1.5	V	
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$-V_{GS}=V_{DS}$, $I_D=100uA$		0.9		mV/℃	
	V _{DS} =30V , V _{GS} =0V , T _J =25°C				1.0		
I _{DSS}	Drain-Source Leakage Current	V_{DS} =20V , V_{GS} =0V , T_{J} =85 $^{\circ}$ C			1.0	uA	
I _{GSS}	Gate-Source Leakage Current	ource Leakage Current $V_{GS}=\pm 5V$, $V_{DS}=0V$			±1.0	uA	
gfs	Forward Transconductance	V _{DS} =5V , I _D =0.1A		80		mS	
$T_{d(on)}$	Turn-On Delay Time			13			
Tr	Rise Time	V _{DS} =5V , V _{GS} =4.5V ,		15		no	
$T_{d(off)}$	Turn-Off Delay Time $R_G=10\Omega$, $I_D=75mA$			98		ns	
T_f	Fall Time			60			
C _{iss}	Input Capacitance			11.5			
C _{oss}	Output Capacitance	V_{DS} =5V , V_{GS} =0V , f=1MHz		10		pF	
C _{rss}	Reverse Transfer Capacitance			3.5			

Diode Characteristics

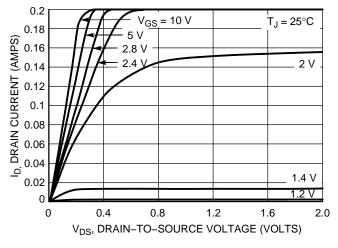
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,4}	V =V =0V Force Current			100	mA
I _{SM}	Pulsed Source Current ^{2,4}	V _G =V _D =0V , Force Current			0.4	Α
V_{SD}	Diode Forward Voltage ²	V_{GS} =0V , I_{S} =0.154mA , T_{J} =25 $^{\circ}$ C		0.77	0.9	V

Note:

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\,\leq\,$ 300us , duty cycle $\,\leq\,$ 2%
- 3.The power dissipation is limited by 150 ℃ junction temperature.
- 4. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



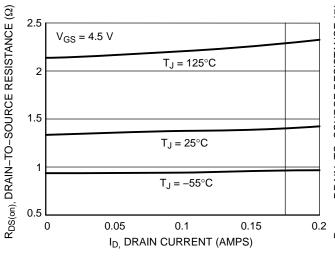
Typical Performance Characteristics

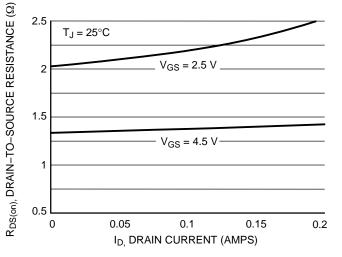


0.2 $V_{DS} = 5 V$ ID, DRAIN CURRENT (AMPS) 0.16 0.12 0.08 0.04 $T_J = 25^{\circ}C$ $T_J = -55^{\circ}C$ 0.8 1.8 2 0.6 1.2 1.4 1.6 V_{GS}, GATE-TO-SOURCE VOLTAGE (VOLTS)

On-Region Characteristics

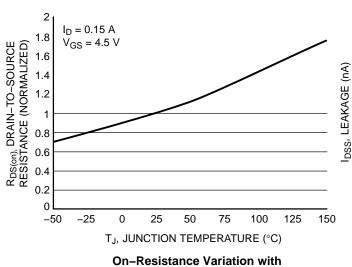
Transfer Characteristics



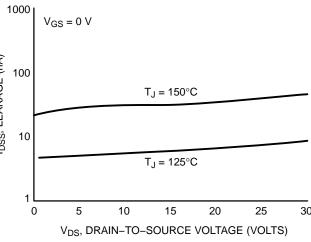


On-Resistance vs. Drain Current and Temperature

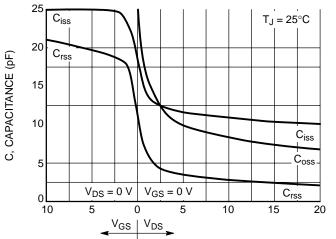
On-Resistance vs. Drain Current and Gate Voltage



Temperature



Drain-to-Source Leakage Current vs. Voltage

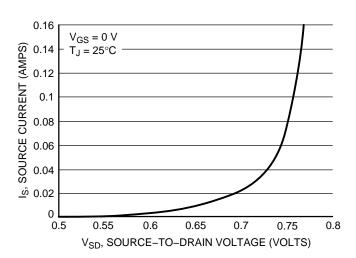


1000 V_{DD} = 5.0 V V_{GS} = 75 mA V_{GS} = 4.5 V V_{GS} = 4.5

GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Capacitance Variation

Resistive Switching Time Variation vs. Gate Resistance

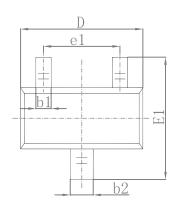


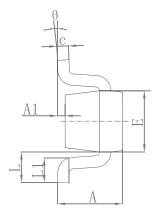
Diode Forward Voltage vs. Current

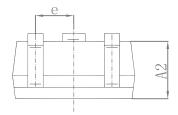


N-Ch MOSFET

SOT-523 Package Outline Dimensions

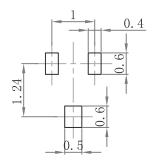






Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	0.700	0.900	0.028	0.035	
A1	0.000	0.100	0.000	0.004	
A2	0.700	0.800	0.028	0.031	
b1	0.150	0.250	0.006	0.010	
b2	0.250	0.350	0.010	0.014	
С	0.100	0.200	0.004	0.008	
D	1.500	1.700	0.059	0.067	
E	0.700	0.900	0.028	0.035	
E1	1.450	1.750	0.057	0.069	
е	0.500 TYP.		0.020 TYP.		
e1	0.900	1.100	0.035	0.043	
L	0.400 REF.		0.016	REF.	
L1	0.260	0.460	0.010	0.018	
θ	0°	8°	0°	8°	

SOT-523 Suggested Pad Layout





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