

**WSF07N10** 

N-Ch MOSFET

### **General Description**

The WSF07N10 is the highest performance trench N-ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSF07N10 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

## **Product Summery**

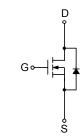
| BVDSS | RDSON | ID |
|-------|-------|----|
| 100V  | 195mΩ | 7A |

## Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch

### **TO-252 Pin Configuration**





| Symbol                              | Parameter Rating   |   | Units |
|-------------------------------------|--|---|-------|
| V <sub>DS</sub>                     | Drain-Source Voltage 100                                       |   | V     |
| V <sub>GS</sub>                     | Gate-Source Voltage ±20  |   | V     |
| I <sub>D</sub> @T <sub>C</sub> =25℃ | Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup> 7 |   | А     |
| I₀@Tc=100℃                          | Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup> 4 |   | А     |
| I <sub>DM</sub>                     | Pulsed Drain Current <sup>2</sup>                              | 21  |       |
| P <sub>D</sub> @T <sub>A</sub> =25℃ | Total Power Dissipation <sup>3</sup>                           | Total Power Dissipation <sup>3</sup> 1.25 |       |
| T <sub>STG</sub>                    | Storage Temperature Range                                      | -55 to 170                                | °C    |
| TJ                                  | T <sub>J</sub> Operating Junction Temperature Range -55 to 170 |   | °C    |

#### **Thermal Data**

| Symbol           | Parameter Typ. Max.                              |  | Unit |      |
|------------------|--|--|------|------|
| R <sub>θJA</sub> | Thermal Resistance Junction-ambient <sup>1</sup> |  | 70   | °C/W |
| R <sub>θJC</sub> | Thermal Resistance Junction-Case <sup>1</sup>    |  | 2.5  | °C/W |

# Absolute Maximum Ratings



**N-Ch MOSFET** 

## Electrical Characteristics (T<sub>J</sub>=25<sup>-1</sup>C, unless otherwise noted)

| Symbol                               | Parameter                                      | Conditions   | Min. | Тур.  | Max. | Unit |
|--------------------------------------|--|--|------|-------|------|------|
| BV <sub>DSS</sub>                    | Drain-Source Breakdown Voltage                 | V <sub>GS</sub> =0V , I <sub>D</sub> =250uA                        | 100  |       |      | V    |
| $\triangle BV_{DSS} / \triangle T_J$ | BVDSS Temperature Coefficient                  | Reference to 25 $^\circ\!\!\mathrm{C}$ , I_D=1mA                   |      | 0.098 |      | V/℃  |
| Б                                    | Static Drain-Source On-Resistance <sup>2</sup> | V <sub>GS</sub> =10V , I <sub>D</sub> =1A                          |      | 195   | 250  | mΩ   |
| R <sub>DS(ON)</sub>                  |  | V <sub>GS</sub> =6V , I <sub>D</sub> =1A                           |      | 240   | 320  | mΩ   |
| V <sub>GS(th)</sub>                  | Gate Threshold Voltage                         |  | 1.5  | 2.0   | 3.0  | V    |
| $	riangle V_{GS(th)}$                | V <sub>GS(th)</sub> Temperature Coefficient    | — V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA         |      | -4.57 |      | mV/℃ |
|                                      | Drain-Source Leakage Current                   | $V_{DS}$ =80V , $V_{GS}$ =0V , $T_{J}$ =25 $^{\circ}$ C            |      |       | 1    |      |
| I <sub>DSS</sub>                     |  | V <sub>DS</sub> =80V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃   |      |       | 5    | uA   |
| I <sub>GSS</sub>                     | Gate-Source Leakage Current                    | $V_{GS}=\pm20V$ , $V_{DS}=0V$                                      |      |       | ±100 | nA   |
| gfs                                  | Forward Transconductance                       | V <sub>DS</sub> =5V , I <sub>D</sub> =5A                           |      | 1     |      | S    |
| Rg                                   | Gate Resistance                                | V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz                 |      | 2     | 4    | Ω    |
| Qg                                   | Total Gate Charge (10V)                        | V <sub>DS</sub> =50V , V <sub>GS</sub> =10V , I <sub>D</sub> =1.3A |      | 5.2   |      |      |
| Q <sub>gs</sub>                      | Gate-Source Charge                             |  |      | 0.75  |      | nC   |
| Q <sub>gd</sub>                      | Gate-Drain Charge                              |  |      | 1.4   |      |      |
| T <sub>d(on)</sub>                   | Turn-On Delay Time                             |  |      | 6     |      |      |
| Tr                                   | Rise Time                                      | $V_{DD}$ =30V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$                |      | 10    |      | 1    |
| T <sub>d(off)</sub>                  | Turn-Off Delay Time                            | I <sub>D</sub> =1A , RL=30Ω  |      | 10    |      | ns   |
| T <sub>f</sub>                       | Fall Time                                      |  |      | 6     |      |      |
| Ciss                                 | Input Capacitance                              | V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , f=1MHz                |      | 320   |      |      |
| C <sub>oss</sub>                     | Output Capacitance                             |  |      | 22    |      | pF   |
| C <sub>rss</sub>                     | Reverse Transfer Capacitance                   |  |      | 13    |      |      |

### **Diode Characteristics**

| Symbol         | Parameter                                | Conditions   | Min. | Тур. | Max. | Unit |
|----------------|--|--|------|------|------|------|
| I <sub>S</sub> | Continuous Source Current <sup>1,6</sup> | $V_G = V_D = 0V$ , Force Current                               |      |      | 3    | А    |
| $V_{SD}$       | Diode Forward Voltage <sup>2</sup>       | V <sub>GS</sub> =0V , I <sub>S</sub> =3A , T <sub>J</sub> =25℃ |      |      | 1.2  | V    |

## Notes:

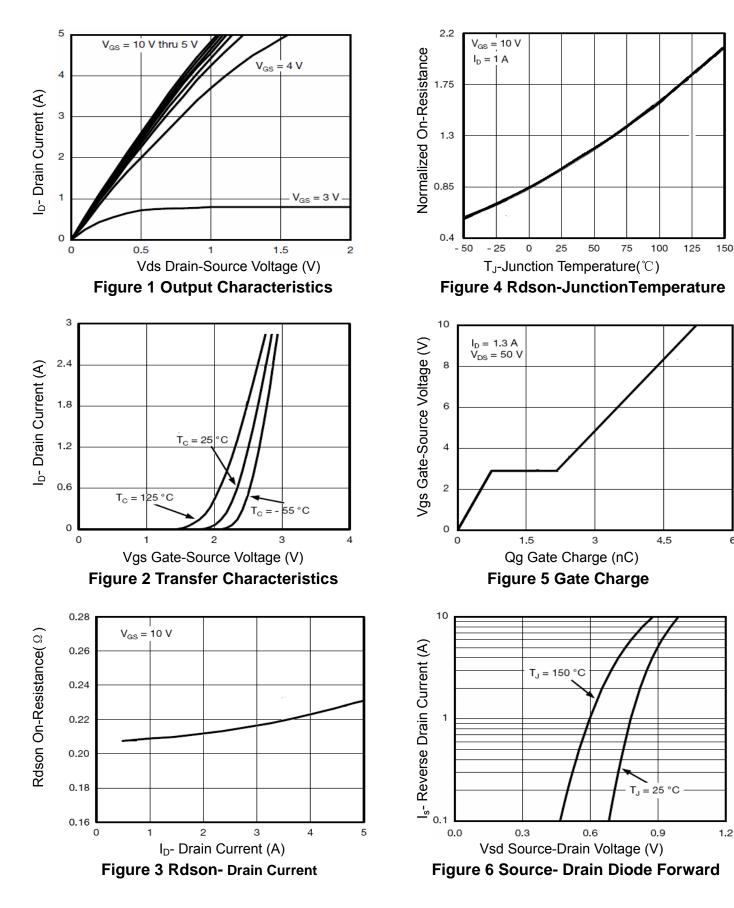
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, t  $\leq$  10 sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production



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# **Typical Characteristics**



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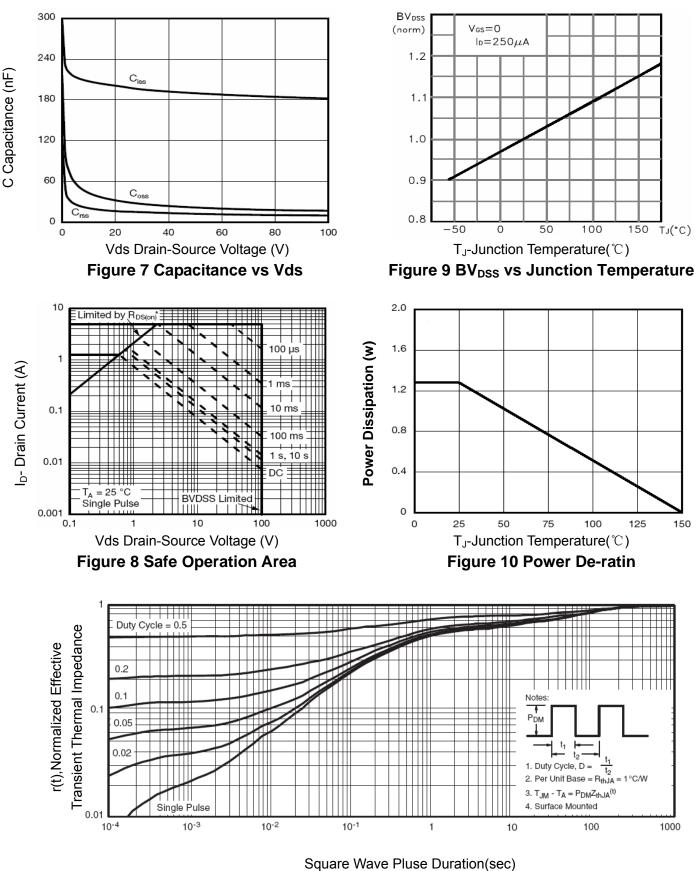


Figure 11 Normalized Maximum Transient Thermal Impedance



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