

## **General Description**

The WSF4060 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications . The WSF4060 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

## **Product Summery**

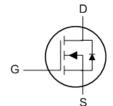
BVDSS	RDSON	ID
40V	7.2mΩ	60A

## **Applications**

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Power Tool Application

## **TO-252 Pin Configuration**





## **Absolute Maximum Ratings**

Symbol	Parameter Rating		Units	
$V_{DS}$	Drain-Source Voltage	40	V	
$V_{GS}$	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V	60	Α	
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current, V <sub>GS</sub> @ 10V	48	Α	
I <sub>DM</sub>	Pulsed Drain Current <sup>a</sup>	160	Α	
EAS	EAS Single Pulse Avalanche Energy <sup>b</sup>		mJ	
I <sub>AS</sub> Avalanche Current		60	Α	
P <sub>D</sub> @T <sub>a</sub> =25℃	P <sub>D</sub> @T <sub>a</sub> =25℃ Total Power Dissipation		W	
T <sub>STG</sub>	T <sub>STG</sub> Storage Temperature Range -55 to 150		$^{\circ}$	
$T_J$	T <sub>J</sub> Operating Junction Temperature Range -55 to 150			

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>		50	°C/W
R <sub>eJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>		2.5	°C/W

#### Notes:

- \* Avalanche single pulse test and avalanche period time tav $\leq$  100  $\mu$  s, duty<1% .
- \*\* Avalanche test condition:  $T_J$ =25°C, L=0.5mH,  $I_{AS}$ =20A,  $V_{DD}$ =30V, and  $V_{GS}$ =10V.
- \*\*\* Current limited by bond wire.



## Electrical Characteristics ( $T_J=25$ $\subset$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA				V	
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25℃, I <sub>D</sub> =1mA		0.043		V/℃	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		7.2	9.0	mΩ	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A		9.2	13	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	-V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	1.6	2.5	V	
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	VGS-VDS, ID -230UA		-6.94		mV/℃	
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =32V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}$ C			2	uA	
DSS	Drain-Source Leakage Current	$V_{DS}$ =32V , $V_{GS}$ =0V , $T_J$ =55 $^{\circ}$ C			10	uA	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA	
gfs	Forward Transconductance V <sub>DS</sub> =5V , I <sub>D</sub> =20A			33		S	
$R_g$	Gate Resistance V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz			1.4	2	Ω	
Qg	Total Gate Charge (10V)			32	44		
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =20V , V <sub>GS</sub> =10V , I <sub>D</sub> =40A		3.8		nC	
$Q_{gd}$	Gate-Drain Charge			9.0			
T <sub>d(on)</sub>	Turn-On Delay Time			11	21		
Tr	Rise Time $V_{DD}$ =30V , $V_{GEN}$ =10V , $R_G$ =1 $\Omega$ ,			13	24	no	
T <sub>d(off)</sub>	Turn-Off Delay Time	$I_D$ =1A ,RL=15 $\Omega$ .		37	67	ns	
T <sub>f</sub>	Fall Time			11	21		
C <sub>iss</sub>	Input Capacitance			1460			
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =20V , V <sub>GS</sub> =0V , f=1MHz		180		pF	
C <sub>rss</sub>	Reverse Transfer Capacitance			146			

#### **Diode Characteristics**

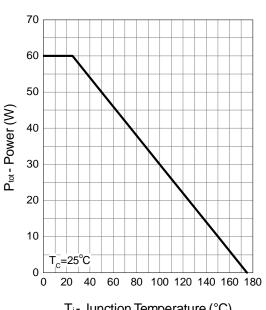
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			40	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>	VG-VD-UV, FOICE Current			100	Α
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =20A , T <sub>J</sub> =25℃		0.8	1.3	V

#### Note:

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper,t<10sec .
- 2.The data tested by pulsed , pulse width  $\, \leqq \,$  300us , duty cycle  $\, \leqq \,$  2%
- 3.The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V, $V_{GS}$ =10V,L=0.5mH, $I_{AS}$ =20A
- 5.The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.
- 7. Package limitation current is 60A.



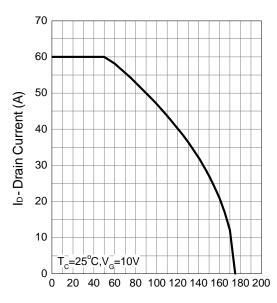
## **Typical Characteristics**



**Power Dissipation** 

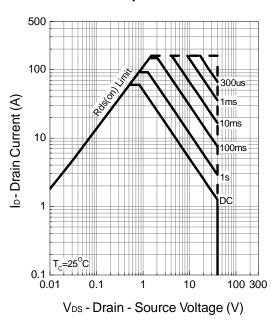
## T<sub>j</sub>- Junction Temperature (°C)

#### **Drain Current**

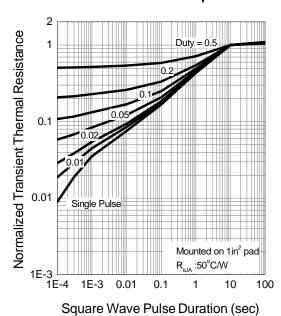


T<sub>j</sub>- Junction Temperature (°C)

## **Safe Operation Area**



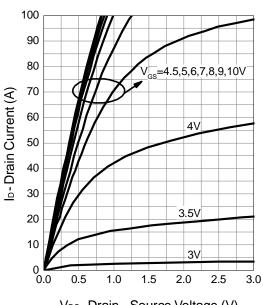
## **Thermal Transient Impedance**





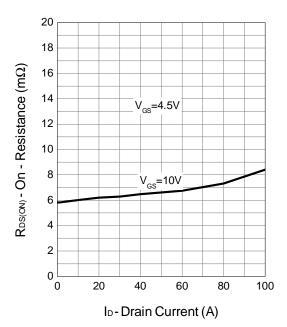
## **Typical Characteristics**

## **Output Characteristics**

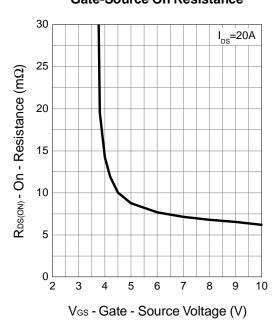


## V<sub>DS</sub>-Drain - Source Voltage (V)

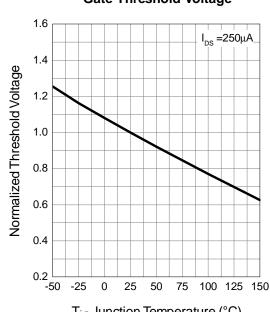
#### **Drain-Source On Resistance**



**Gate-Source On Resistance** 



## **Gate Threshold Voltage**

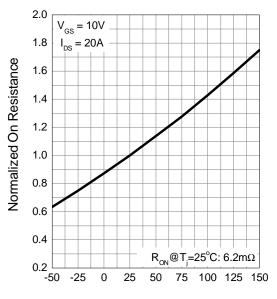


T<sub>j</sub> - Junction Temperature (°C)



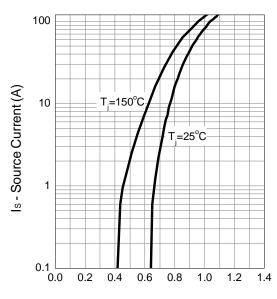
## **Typical Characteristics**

## **Drain-Source On Resistance**



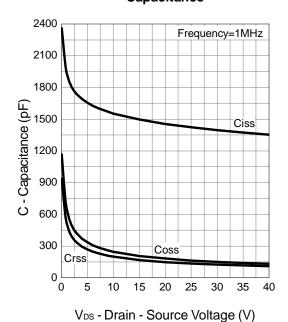
T<sub>j</sub>-Junction Temperature (°C)

#### **Source-Drain Diode Forward**

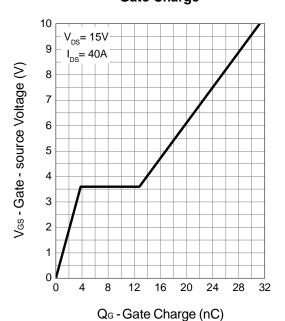


Vsp - Source - Drain Voltage (V)

## Capacitance



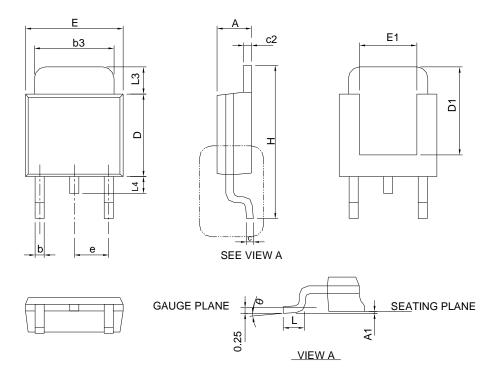
## **Gate Charge**





# **Package Information**

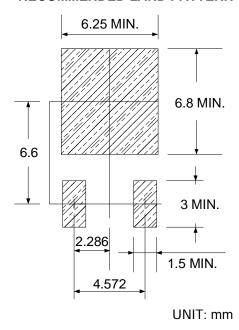
## TO-252-3



Ş		TO-252-3				
s>≥ZBO_	MILLIM	ETERS	INC	HES		
5	MIN.	MAX.	MIN.	MAX.		
Α	2.18	2.39	0.086	0.094		
A1	-	0.13	-	0.005		
b	0.50	0.89	0.020	0.035		
b3	4.95	5.46	0.195	0.215		
С	0.46	0.61	0.018	0.024		
c2	0.46	0.89	0.018	0.035		
D	5.33	6.22	0.210	0.245		
D1	4.57	6.00	0.180	0.236		
Е	6.35	6.73	0.250	0.265		
E1	3.81	6.00	0.150	0.236		
е	2.29 BSC		0.090 BSC			
Н	9.40	10.41	0.370	0.410		
L	0.90	1.78	0.035	0.070		
L3	0.89	2.03	0.035	0.080		
L4	-	1.02	-	0.040		
θ	0°	8°	0°	8°		

## Note: Follow JEDEC TO-252.

## **RECOMMENDED LAND PATTERN**





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