

#### **General Description**

The WSK96N08 is the highest performance trench N-ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSK96N08 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

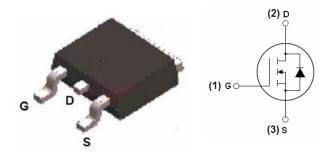
### **Product Summery**

BVDSS	RDSON	ID
80V	7mΩ	96A

**TO-263-2L Pin Configuration** 

### Applications

- Switching application
- Power management for inverter systems



### **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	80	V
V <sub>GS</sub>	Gate-Source Voltage	±25	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	90	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	64	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	360**	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	416***	mJ
I <sub>AS</sub>	Avalanche Current	200	A
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>3</sup>	185	W
P <sub>D</sub> @T <sub>C</sub> =100°C	Total Power Dissipation <sup>3</sup>	92	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	°C
TJ	Operating Junction Temperature Range	-55 to 175	°C

Note: \* Repetitive rating; pulse width limited by max.junction temperature.

- \*\* Surface mounted on 1in2 FR-4 board.
- \*\*\* Limited by TJmax , starting TJ=25°C , L = 0.5mH, RG= 25 $\Omega$ , VGs =10V.



### Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	80			V	
$\triangle BV_{DSS} / \triangle T_J$	BVDSS Temperature Coefficient	Reference to $25^{\circ}$ C , I <sub>D</sub> =1mA		0.0		V/℃	
Р	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =45A		7	9	mΩ	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =6V , I <sub>D</sub> =10A		10	12	mΩ	
V <sub>GS(th)</sub>	Gate Threshold Voltage		2	3	4	V	
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> -V <sub>DS</sub> , I <sub>D</sub> -2500A		-6.57		mV/℃	
la a a	Durain Courses Lookana Current	$V_{\text{DS}}\text{=}80\text{V}$ , $V_{\text{GS}}\text{=}0\text{V}$ , $T_{\text{J}}\text{=}25^\circ\!\mathrm{C}$		-	1		
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{\text{DS}}\text{=}80\text{V}$ , $V_{\text{GS}}\text{=}0\text{V}$ , $T_{\text{J}}\text{=}55^\circ\!\mathrm{C}$		-	2	uA	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm20V$ , $V_{DS}$ = $0V$		-	±100	nA	
gfs	orward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =20A	18			S	
Qg	Total Gate Charge (10V)			86			
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =50V , V <sub>GS</sub> =10V , I <sub>D</sub> =120A		16		nC	
Q <sub>gd</sub>	Gate-Drain Charge			28			
T <sub>d(on)</sub>	Turn-On Delay Time			25			
Tr	Rise Time	$V_{DD}$ =30V , $V_{GS}$ =10V ,		42		- ns	
T <sub>d(off)</sub>	Turn-Off Delay Time	$R_G=6\Omega I_D=145A$ ,		62			
T <sub>f</sub>	Fall Time	R∟=30Ω		19			
C <sub>iss</sub>	Input Capacitance			3800			
Coss	Output Capacitance	$V_{DS}$ =30V , $V_{GS}$ =0V , f=1MHz		389		pF	
C <sub>rss</sub>	Reverse Transfer Capacitance			250			
Vsd	Diode Forward Voltage	Isd=45A,Vgs=0V		0.8		V	
trr	Reverse Recovery Time			60		ns	
Qrr	Reverse Recovery Charge	IsD=45A,dlsD/dt=100A/µs		125		nC	

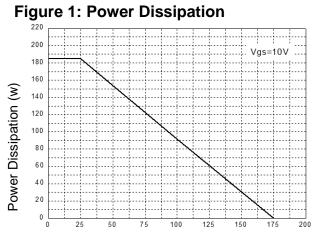
Note: \*Pulse test, pulse width  $\leq$  300us, duty cycle  $\leq$  2%

# WSK96N08

N-Ch MOSFET

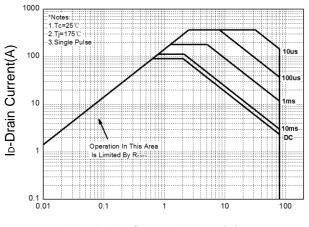


## **Typical Operating Characteristics**



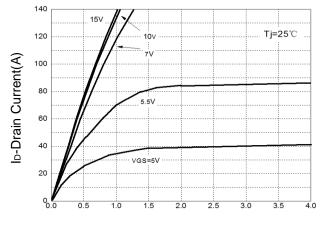
Tc-Case Temperature(℃)

### Figure 3: Safe Operation Area



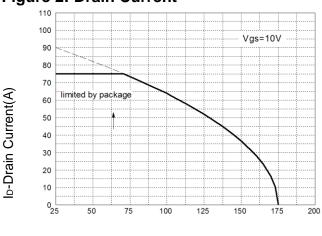
VDS-Drain-Source Voltage(V)

### Figure 5: Output Characteristics



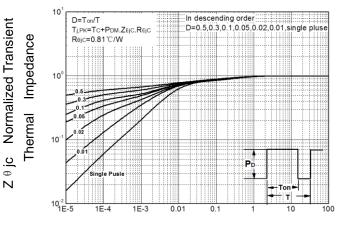
VDS-Drain-Source Voltage (V)

## Figure 2: Drain Current



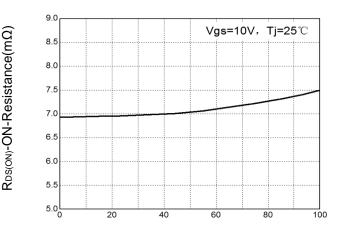
Tc-Case Temperature(℃)

### Figure 4: Thermal Transient Impedance



Maximum Effective Transient Thermal Impedance, Junction-to-Case

### Figure 6: Drain-Source On Resistance



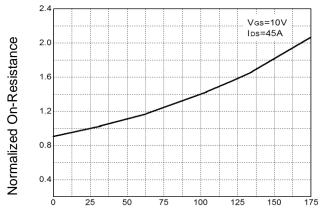
ID-Drain Current(A)

## **WSK96N08**



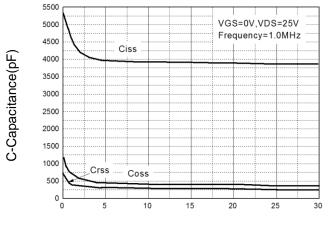
**Typical Operating Characteristics** 

Figure 7: On-Resistance vs. Temperature



Tj-Junction Temperature ( $^{\circ}C$ )

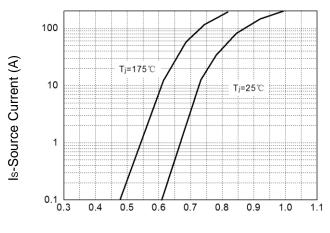
### **Figure 9: Capacitance Characteristics**



VDS-Drain-Source Voltage (V)

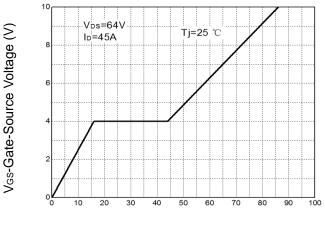
N-Ch MOSFET

### Figure 8: Source-Drain Diode Forward



Vsp-Source-Drain Voltage(V)

### Figure 10: Gate Charge Characteristics



QG-Gate Charge (nC)



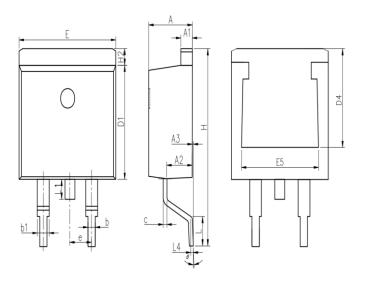


## **Device Per Unit**

Package Type	Unit	Quantity
TO-263-2L	Reel	50

Package Information

### TO-263-2L



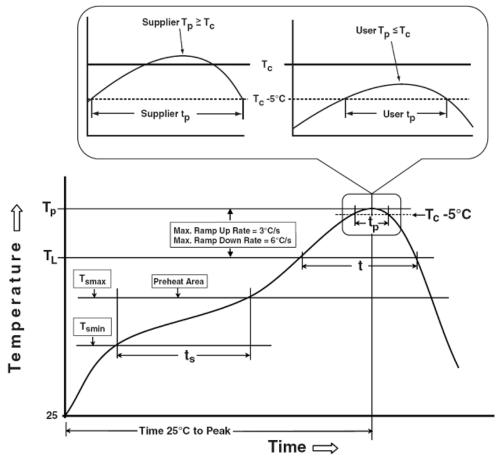
### COMMON DIMENSIONS

SYMBOL	mm			
STINDUL	MIN	NOM	MAX	
A	4.37	4.57	4.77	
A1	1.22	1.27	1.42	
A2	2.49	2.69	2.89	
A3	0	0.13	0.25	
b	0.7	0.81	0.96	
b1	1.17	1.27	1.47	
С	0.3	0.38	0.53	
D1	8.5	8.7	8.9	
D4	6.6	-	-	
E	9.86	10.16	10.36	
E5	7.06	-	-	
е	2.54 BSC			
Н	14.7	15.1	15.5	
H2	1.07	1.27	1.47	
L	2	2.3	2.6	
L1	1.4	1.55	1.7	
L4	0.25 BSC			
θ	0°	5°	9°	





## **Classification Profile**



## **Classification Reflow Profiles**

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
Preheat & Soak	100 °C	150 °C	
Temperature min (T <sub>smin</sub> )	150 °C	200 °C	
Temperature max (T <sub>smax</sub> )	60-120 seconds	60-120 seconds	
Time (Tsmin to Tsmax) (t <sub>s</sub> )	00-120 seconds		
Average ramp-up rate	3 °C/second max.	3°C/second max.	
(T <sub>smax</sub> to T <sub>P</sub> )	3 C/second max.		
Liquidous temperature (TL)	183 °C	217 °C	
Time at liquidous (t∟)	60-150 seconds	60-150 seconds	
Peak package body Temperature	Cas Classification Temp in table 4	SeeClassification Tempin table 2	
(T <sub>p</sub> )*	See Classification Temp in table 1		
Time (t <sub>P</sub> )** within 5°C of the specified	20** accorda	30** seconds	
classification temperature (T <sub>c</sub> )	20** seconds		
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.	
Time 25°C to peak temperature	6 minutes max.	8 minutes max.	
*Tolerance for peak profile Temperature	$(T_p)$ is defined as a supplier minimum	n and a user maximum.	
** Tolerance for time at peak profile tem	perature ( $t_P$ ) is defined as a supplier m	ninimum and a user maximum.	



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