General Description

The WSD80130DN56 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSD80130DN56 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

Product Summery

BV_{DSS}	R_{DSON}	I_D
80V	2.7mΩ	130A

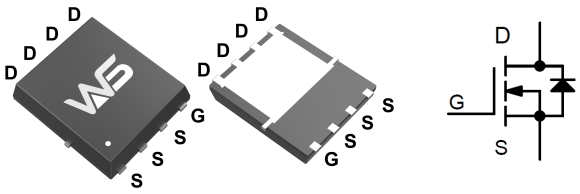
Applications

- DC-DC converter switching for Networkong
- General purpose switching

Features

- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

DFN5X6-8 Pin Configuration



Absolute Maximum Ratings (T_A=25°C Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	80	V
V_{GS}	Gate-Source Voltage	±20	V
T_J	Maximum Junction Temperature	150	°C
I_D	Storage Temperature Range	-55 to 150	°C
I_D	Continuous Drain Current, V_{GS}=10V,T_C=25°C	130	A
	Continuous Drain Current, V_{GS}=10V,T_C=70°C	89	A
I_{DM}	Pulsed Drain Current ,T_C=25°C	400	A
P_D	Maximum Power Dissipation,T_C=25°C	200	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case	1.25	°C

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	80	---	---	V
$\triangle BV_{DSS}/\triangle T_J$	BV_{DSS} Temperature Coefficient	Reference to 25℃ , I_D=1mA	---	0.043	---	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V , I_D=40A	---	2.7	3.6	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	2.0	3.0	4.0	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-6.94	---	mV/℃
I_{DSS}	Drain-Source Leakage Current	V_{DS}=48V , V_{GS}=0V , T_J=25℃	---	---	2	uA
		V_{DS}=48V , V_{GS}=0V , T_J=55℃	---	---	10	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
Q_g	Total Gate Charge (10V)	V_{DS}=30V , V_{GS}=10V , I_D=30A	---	48.6	---	nC
Q_{gs}	Gate-Source Charge		---	17.5	---	
Q_{gd}	Gate-Drain Charge		---	10.4	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=30V , V_{GS}=10V , R_G=2.5Ω, I_D=2A ,RL=15Ω.	---	20	---	ns
T_r	Rise Time		---	10	---	
$T_{d(off)}$	Turn-Off Delay Time		---	35	---	
T_f	Fall Time		---	12	---	
C_{iss}	Input Capacitance	V_{DS}=25V , V_{GS}=0V , f=1MHz	---	4150	---	pF
C_{oss}	Output Capacitance		---	471	---	
C_{rss}	Reverse Transfer Capacitance		---	20	---	

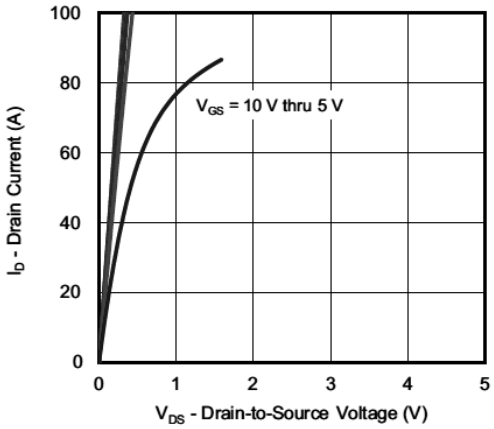
Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1,6]	V_G=V_D=0V , Force Current	---	---	83.3	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1.4	V

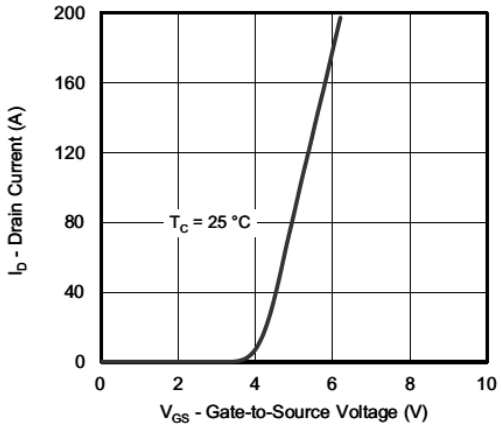
Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition：Tj=25℃,V_{DD}=40V,V_G=10V,L=0.5mH,Rg=25Ω

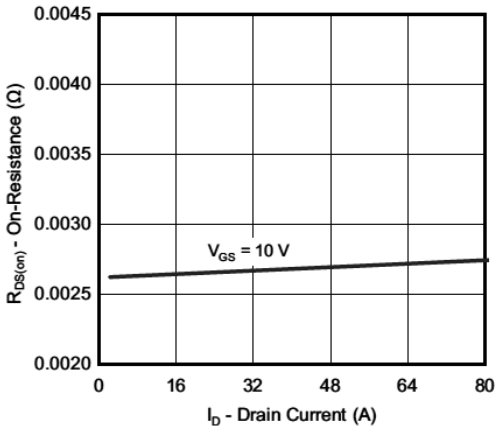
Typical Performance Characteristics（(TJ = 25 ℃, unless otherwise noted)）



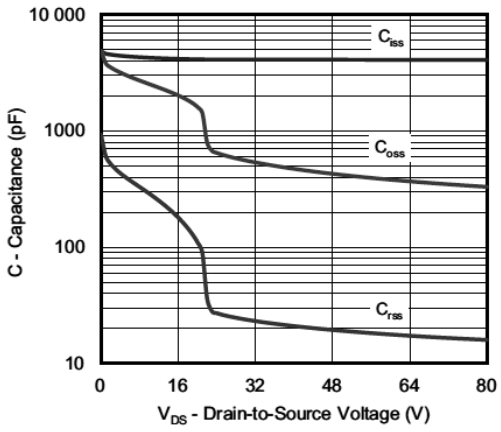
Output Characteristics



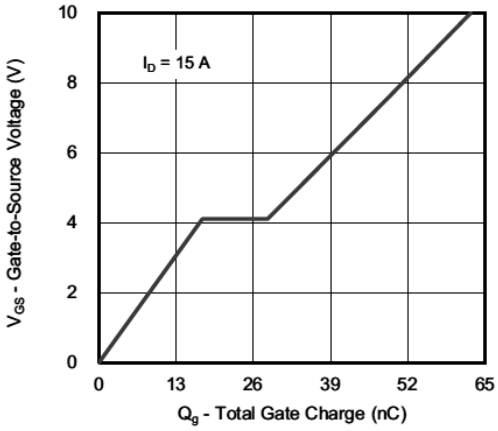
Transfer Characteristics



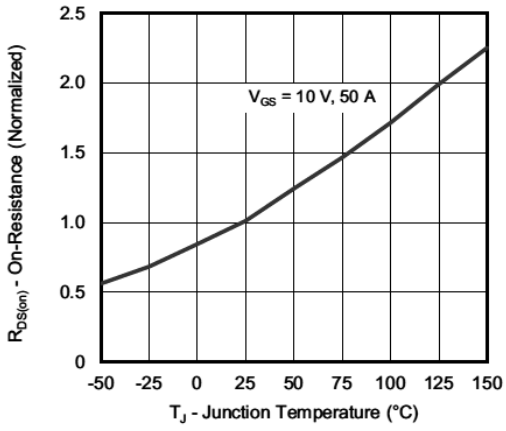
On-Resistance vs. Drain Current and Gate Voltage



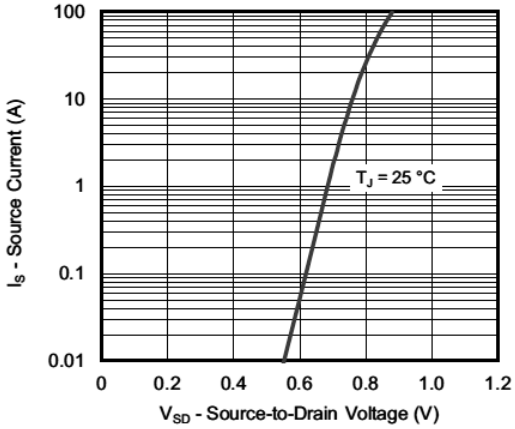
Capacitance



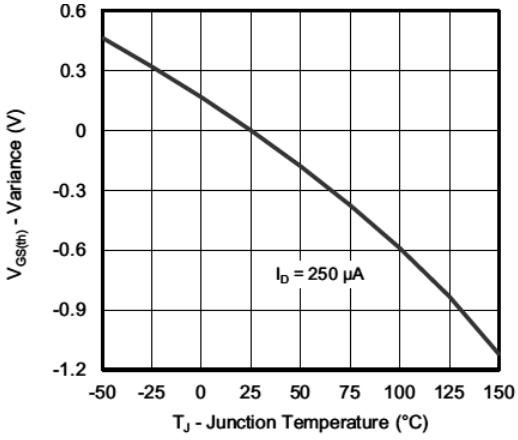
Gate Charge



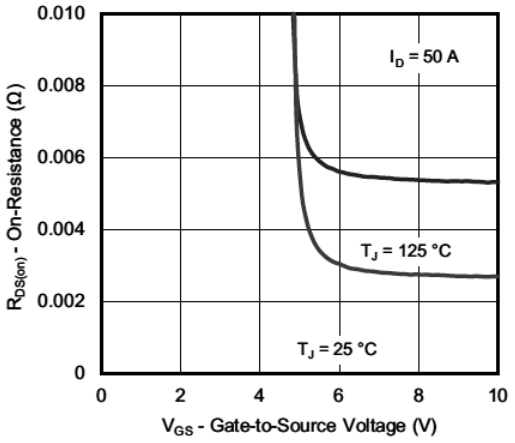
On-Resistance vs. Junction Temperature



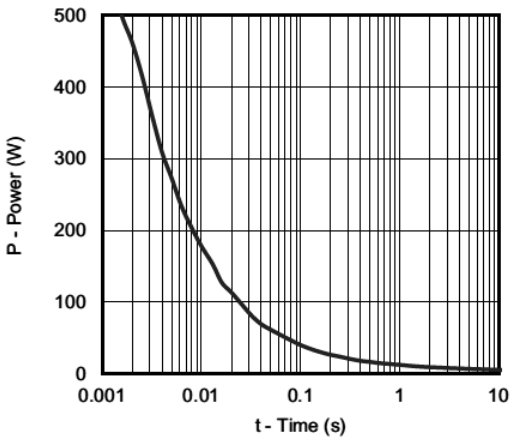
Source-Drain Diode Forward Voltage



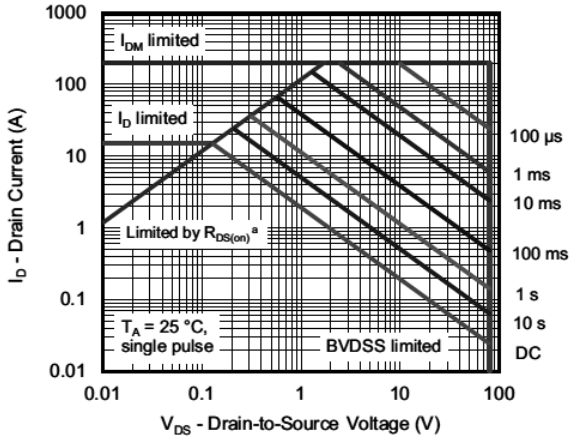
Threshold Voltage



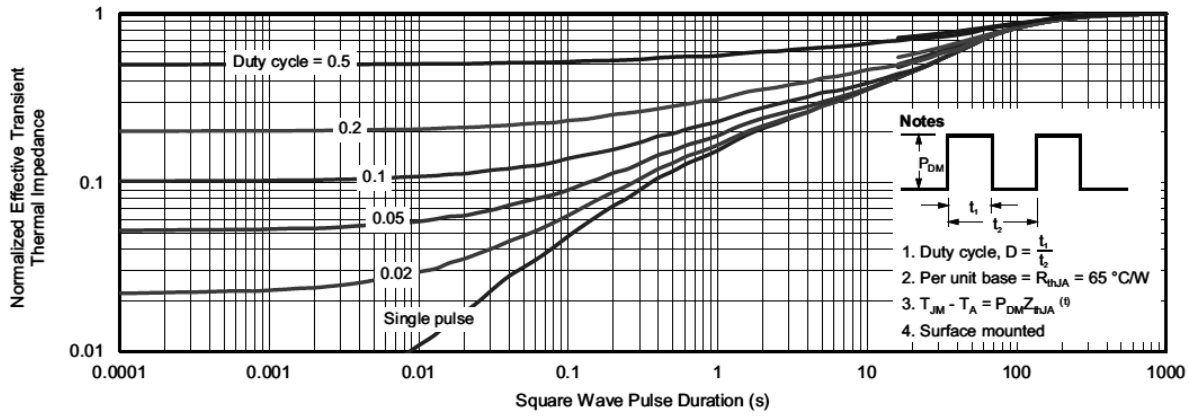
On-Resistance vs. Gate-to-Source Voltage



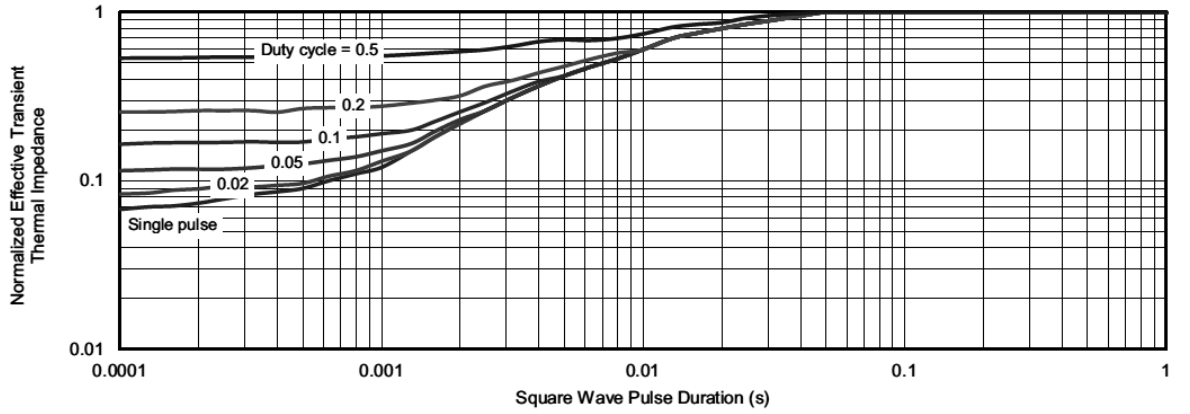
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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