

### General Description

WSF80N06H use advanced VD MOST technology to provide low RDS(ON), low gate charge, fast switching This device is specially designed to get better ruggedness and suitable to use in Low RDS(on) & FOM Extremely low switching loss Excellent stability and uniformity or Invertors

### Features

- Advanced high cell density Trench technology
- Green Device Available
- Excellent Cdv/dt effect decline
- Super Low Gate Charge

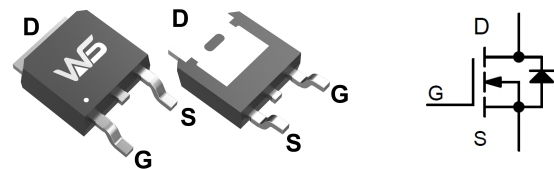
### Product Summary

BVDSS	RDS(ON)	ID
60V	8.0mΩ	70A

### Applications

- Load switch.
- Battery protection.
- Uninterruptible power supply.

### TO-252 Pin Configuration



### Absolute Maximum Ratings@T<sub>J</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	60	V
VGS	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sub>1</sub>	70	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sub>1</sub>	36	A
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sub>1</sub>	10.2	A
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sub>1</sub>	9.5	A
IDM	Pulsed Drain Current <sub>2</sub>	100	A
EAS	Single Pulse Avalanche Energy <sub>3</sub>	72.2	mJ
IAS	Avalanche Current	38	A
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sub>4</sub>	52	W
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sub>4</sub>	2	W
TSTG	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient <sub>1</sub>	62	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sub>1</sub>	2.4	°C/W

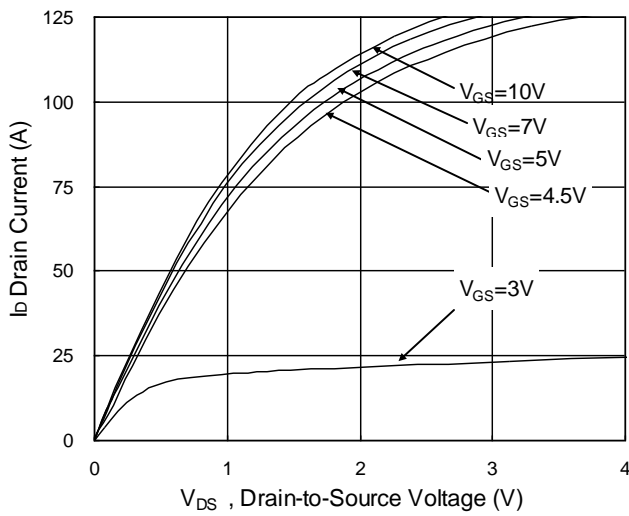
**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	65	---	V
ΔBVDSS/ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA	---	0.052	---	V/°C
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =8A	---	8.0	10	mΩ
VGS(th)	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2.0	2.9	4.0	V
ΔVGS(th)	V <sub>GS(th)</sub> Temperature Coefficient		---	-5.76	---	mV/°C
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5	
IGSS	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =30A	---	42	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	1.5	---	Ω
Q <sub>g</sub>	Total Gate Charge (4.5V)	V <sub>DS</sub> =48V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	---	28.7	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	10.5	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	9.9	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =30V, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω, I <sub>D</sub> =15A	---	10.4	---	ns
T <sub>r</sub>	Rise Time		---	9.2	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	63	---	
T <sub>f</sub>	Fall Time		---	4.8	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	---	3240	---	pF
C <sub>oss</sub>	Output Capacitance		---	210	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	146	---	
I <sub>S</sub>	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	47	A
I <sub>SM</sub>	Pulsed Source Current <sup>2,5</sup>		---	---	100	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =15A, dI/dt=100A/μs, T <sub>J</sub> =25°C	---	18	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge		---	14	---	nC

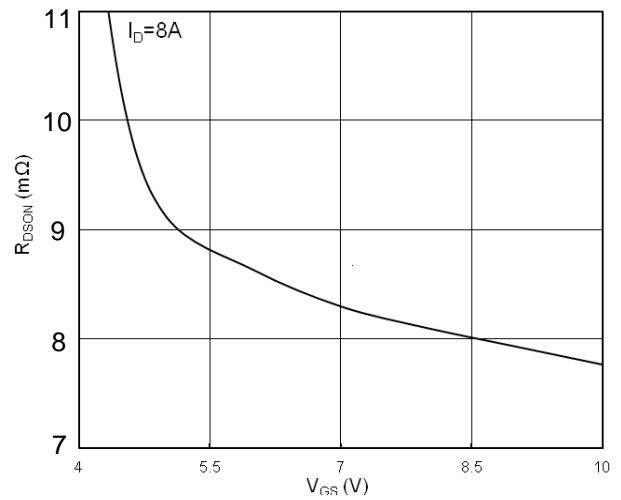
**Note :**

- 1、 The data tested by surface mounted on a 1 inch 2 FR-4 board with 20Z copper.
- 2、 The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%
- 3、 The EAS data shows Max. rating . The test condition is V<sub>DD</sub> =25V, V<sub>GS</sub> =10V, L=0.1mH, I<sub>AS</sub> =38A
- 4、 The power dissipation is limited by 150°C junction temperature
- 5、 The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

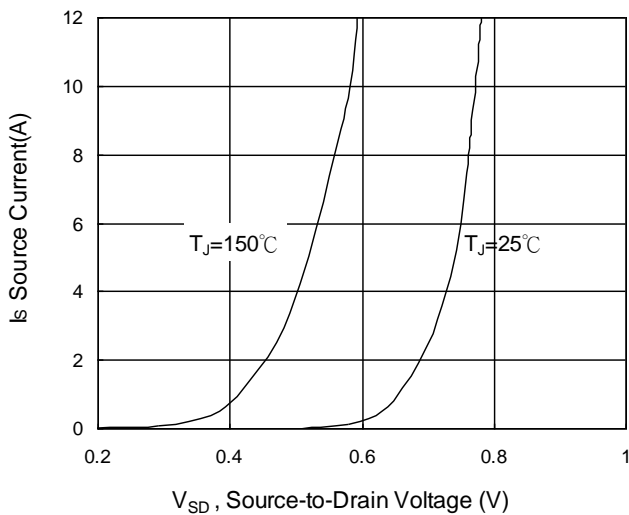
**Typical Characteristics**



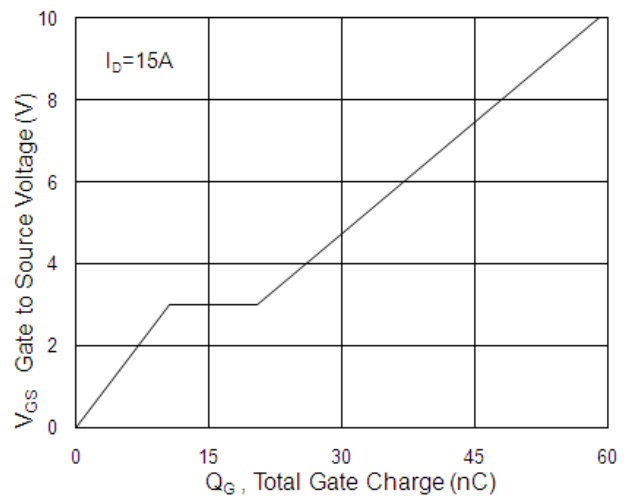
**Fig.1 Typical Output Characteristics**



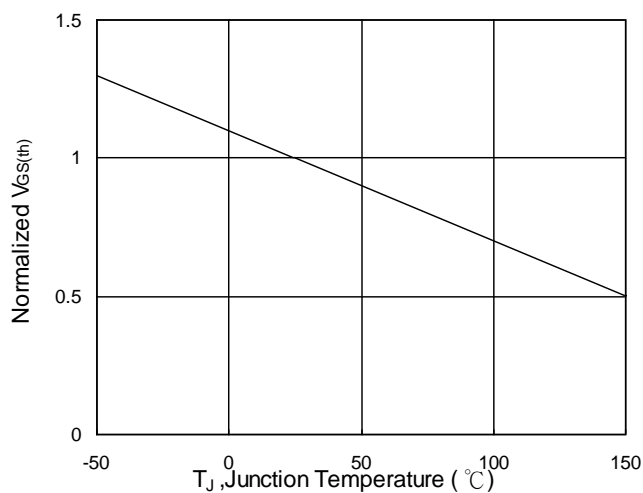
**Fig.2 On-Resistance v.s Gate-Source**



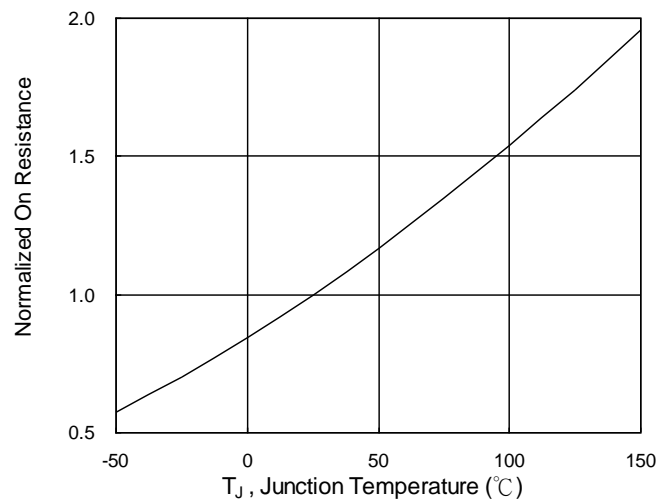
**Fig.3 Forward Characteristics of Reverse**



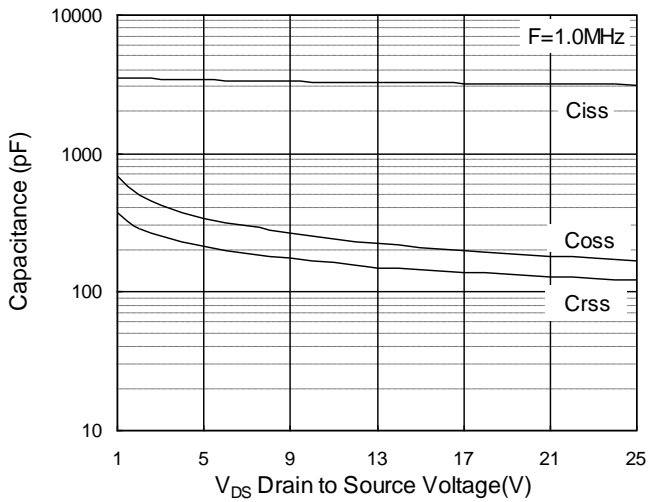
**Fig.4 Gate-Charge Characteristics**



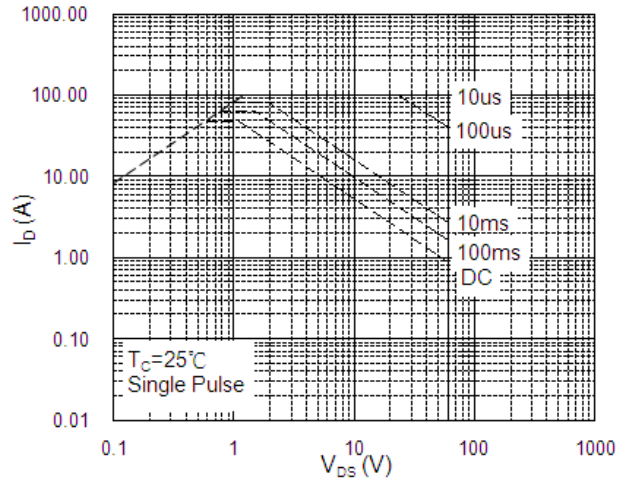
**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



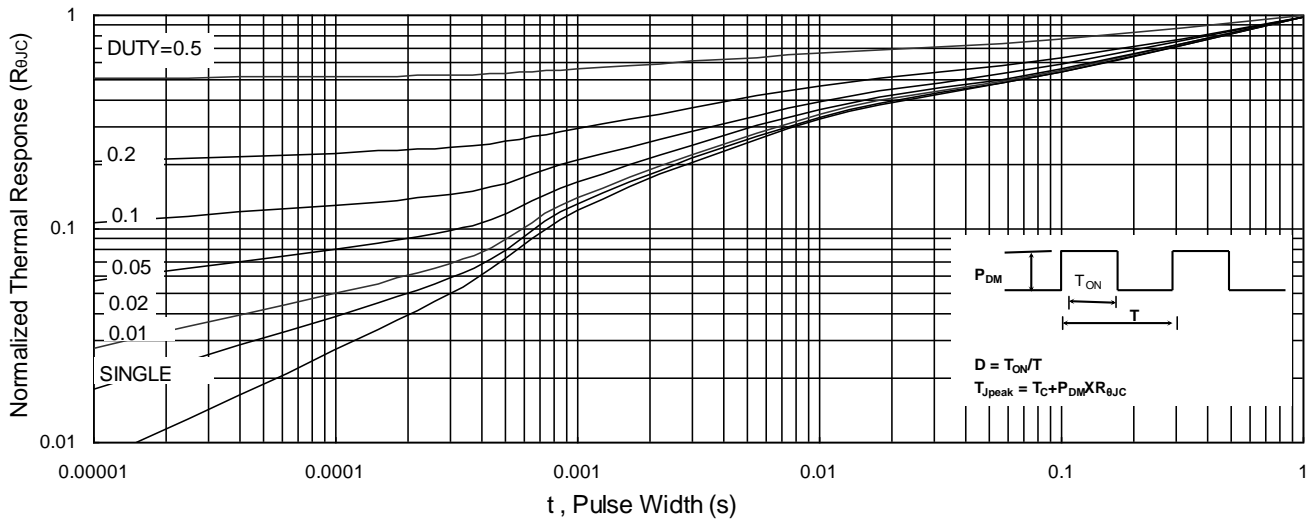
**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**



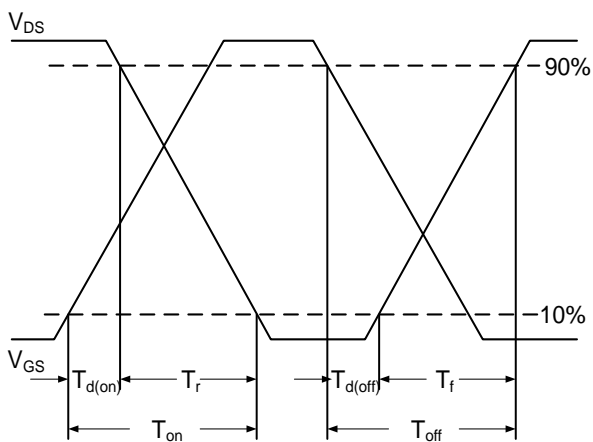
**Fig.7 Capacitance**



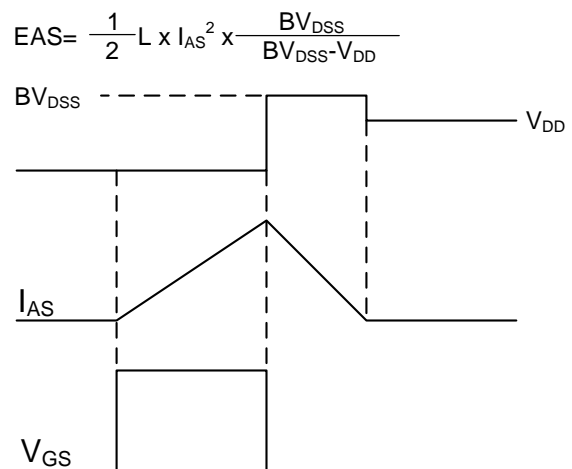
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

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