

**N-Ch MOSFET** 

# **General Description**

The WSK140N03 is the highest performance trench N-ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSK140N03 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

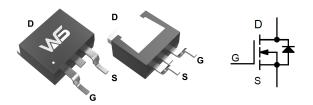
## **Product Summery**

BVDSS	RDSON	ID
30V	4mΩ	140A

# **Applications**

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System

## **TO-263-2L Pin Configuration**



## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	30	V	
$V_{GS}$	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	140	Α	
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	72	Α	
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	225	Α	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	145	mJ	
I <sub>AS</sub>	Avalanche Current	53.8	Α	
P <b></b> _@T <sub>C</sub> =25°C	Total Power Dissipation <sup>3</sup>	86.8	W	
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation <sup>3</sup>	2	W	
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	$^{\circ}$	
TJ	Operating Junction Temperature Range -55 to 150		$^{\circ}$	



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# Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.098		V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =12A			4	mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =12A			6	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1		2.5	٧
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient			-6.57		mV/℃
I <sub>DSS</sub>	Drain Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			2	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =30A		26.5		S
Qg	Total Gate Charge (10V)			31.6		
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =20V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =12A		6.1		nC
Q <sub>gd</sub>	Gate-Drain Charge			13.8		
T <sub>d(on)</sub>	Turn-On Delay Time			11.2		
Tr	Rise Time	V <sub>DD</sub> =15V,V <sub>GS</sub> =10V,		49		ns
T <sub>d(off)</sub>	Turn-Off Delay Time	$R_G$ =1.5 $\Omega$ , $I_D$ =20A.		35		
T <sub>f</sub>	Fall Time			7.8		
C <sub>iss</sub>	Input Capacitance			3075		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		400		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			315		

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			110	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>				225	Α
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃			1.2	V

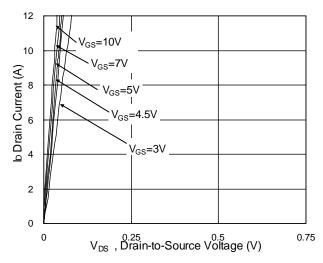
#### Note

- 1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper,t≤10sec.
- 2.The data tested by pulsed , pulse width  $\,\leq\,300\text{us}$  , duty cycle  $\,\leq\,2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V, $V_{GS}$ =10V,L=0.1mH, $I_{AS}$ =53.8A
- 4.The power dissipation is limited by 150 ℃ junction temperature
- 5. The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



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# **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

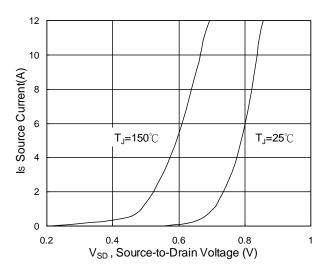


Fig.3 Forward Characteristics of Reverse

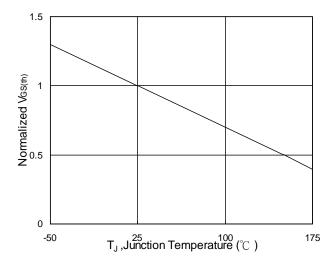


Fig.5 Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>

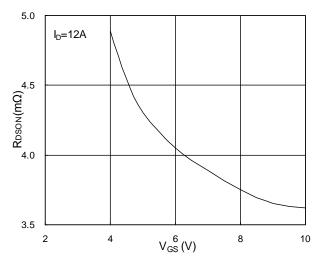


Fig.2 On-Resistance vs. G-S Voltage

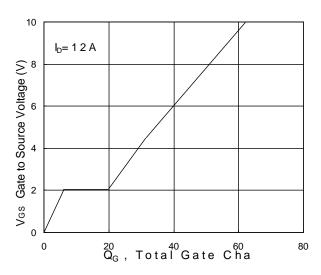


Fig.4 Gate-Charge Characteristics

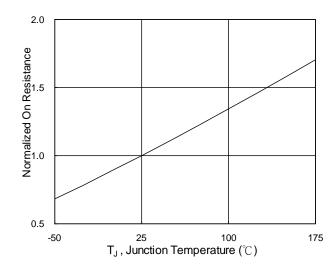
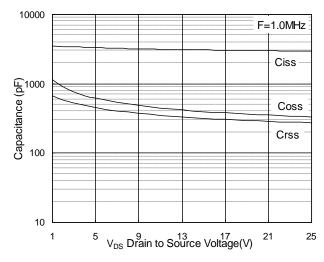


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>





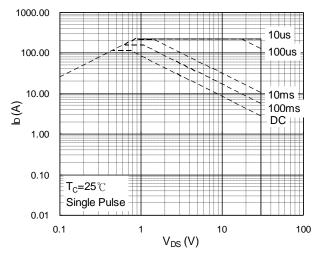


Fig.7 Capacitance

Fig.8 Safe Operating Area

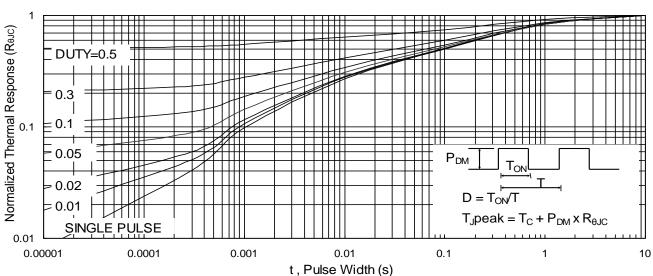
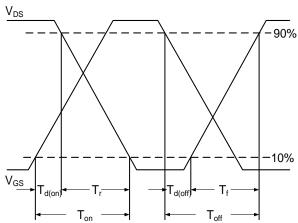
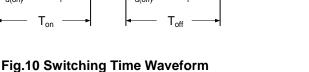


Fig.9 Normalized Maximum Transient Thermal Impedance





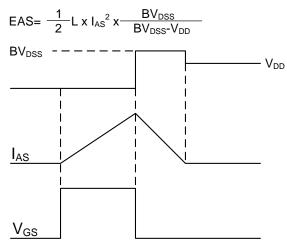


Fig.11 Unclamped Inductive Switching Waveform



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