



General Description

The WSP6956 is the highest performance trench Dual N-ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

Features

- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

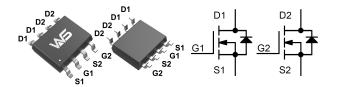
Product Summery

BVDSS	RDSON	ID
60V	15m Ω	10A

Applications

- SMPS Synchronous Rectification.
- DC-DC Conversion.
- Load Switch.

SOP-8 Pin Configuration



Absolute Maximum Ratings (T A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit						
Common Ratings									
V _{DSS}	Drain-Source Voltage		60	V					
V _{GSS}	Gate-Source Voltage	±20							
TJ	Maximum Junction Temperature		150	°C					
T _{STG}	Storage Temperature Range	-55 to 150	°C						
Is	Diode Continuous Forward Current	T _A =25°C	5						
I _D	Continuous Proin Current	T _A =25°C	10	A					
	Continuous Drain Current	T _A =70°C	8						
I _{DM} ^a	Pulsed Drain Current	T _A =25°C	38						
P _D	Maximum Dowar Dissination	T _A =25°C	3.5	- w					
	Maximum Power Dissipation	T _A =70°C	2.2						
R _{⊕JA} °	The word Designation to Ambient	t ≤ 10s	35	°C/W					
	Thermal Resistance-Junction to Ambient	Steady State	70						
I _{AS} b	Avalanche Current, Single pulse	L=0.1mH	27	Α					
E _{AS} b	Avalanche Energy, Single pulse	L=0.1mH	36	mJ					

Note a : Pulse width limited by max. junction temperature.

Note b: UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature Tj=25°C).

Note c: Surface Mounted on 1in² pad area.





Electrical Characteristics (T $_{A}$ = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit		
Static Characteristics								
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	60		-	V		
	Zoro Coto Voltago Droin Current	V _{DS} =48V, V _{GS} =0V	-	-	1	^		
I _{DSS}	Zero Gate Voltage Drain Current	T _J =85°C	-	-	30	μΑ		
V _{GS(th)}	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_{DS}=250\mu A$	1	1.5	2.5	V		
I _{GSS}	Gate Leakage Current	$V_{GS}=\pm20V, V_{DS}=0V$	-	-	±100	nA		
Rps(ON) d	Drain Course On state Resistance	V _{GS} =10V, I _{DS} =10A	-	15	20	mΩ		
R _{DS(ON)} d	Drain-Source On-state Resistance	V _{GS} =4.5V, I _{DS} =9A	-	18	24			
Diode Cha	Diode Characteristics							
V _{SD} d	Diode Forward Voltage	I _{SD} =10A, V _{GS} =0V	-	8.0	1.3	V		
t _{rr}	Reverse Recovery Time	1 400 dl /dt 4000/	-	21	-	ns		
Q_{rr}	Reverse Recovery Charge	I_{SD} =10A, dI_{SD}/dt =100A/ μ S	-	22	-	nC		
Dynamic	Characteristics ^e	•						
R _G	Gate Resistance	V _{GS} =0V,V _{DS} =0V,f=1MHz	-	2.5	-	Ω		
C _{iss}	Input Capacitance	V _{GS} =0V,	-	1370	1780	pF		
C _{oss}	Output Capacitance	V _{DS} =30V,	-	135	-			
C _{rss}	Reverse Transfer Capacitance	Frequency=1.0MHz	-	60	-			
$t_{d(ON)}$	Turn-on Delay Time	V -20V D -200	-	14	26			
t _r	Turn-on Rise Time	V_{DD} =30V, R_L =30 Ω , I_{DS} =1A, V_{GEN} =10V,	-	8	15	ns		
t _{d(OFF)}	Turn-off Delay Time	$R_G=6\Omega$	-	38	69			
t _f	Turn-off Fall Time		-	12	22			
Gate Cha	rge Characteristics ^e							
Qg	Total Gate Charge	V _{DS} =30V, V _{GS} =4.5V, I _{DS} =10A	-	12	-			
Qg	Total Gate Charge		-	26	37	nC		
Q_{gs}	Gate-Source Charge	V _{DS} =30V, V _{GS} =10V, I _{DS} =10A	-	5	-			
Q_{gd}	Gate-Drain Charge	7.05 . 57.1	-	5	-			

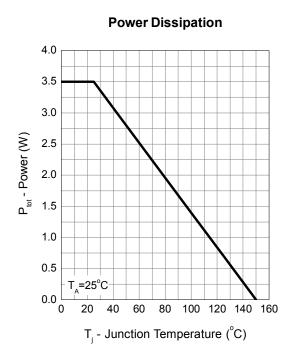
Note d : Pulse test ; pulse width $\leq 300 \mu s$, duty cycle $\leq 2\%$.

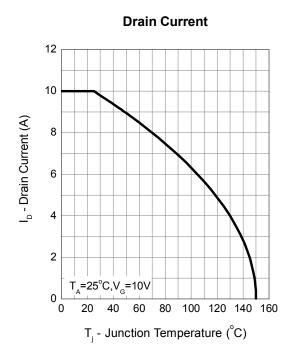
Note e: Guaranteed by design, not subject to production testing.



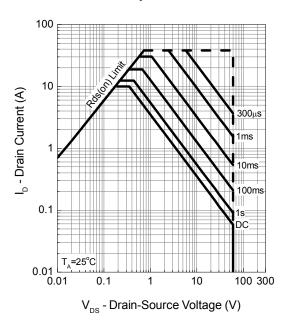


Typical Operating Characteristics

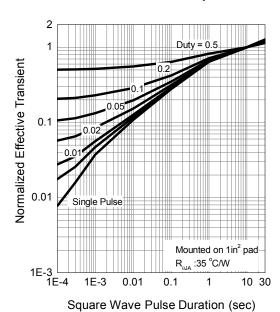




Safe Operation Area

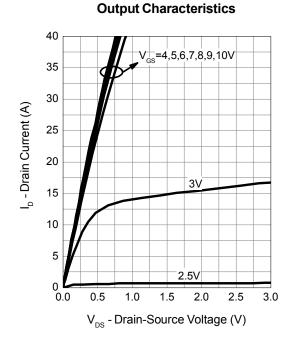


Thermal Transient Impedance

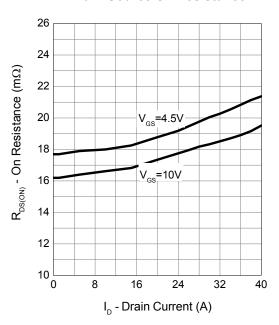




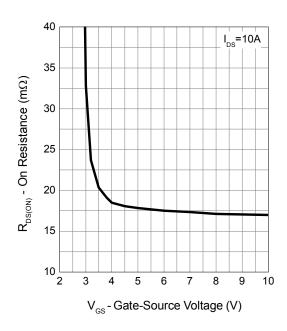
Typical Operating Characteristics (Cont.)



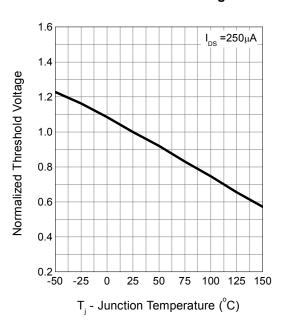
Drain-Source On Resistance



Gate-Source On Resistance



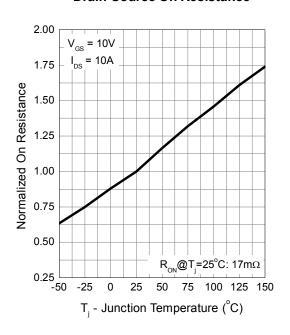
Gate Threshold Voltage



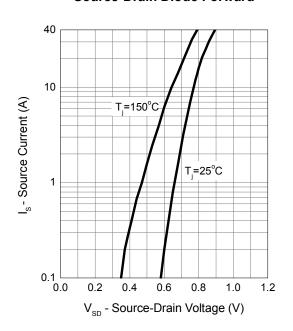


Typical Operating Characteristics (Cont.)

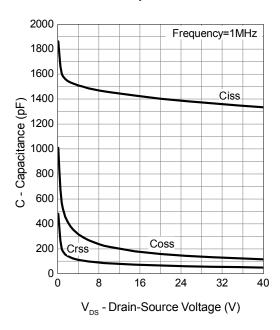
Drain-Source On Resistance



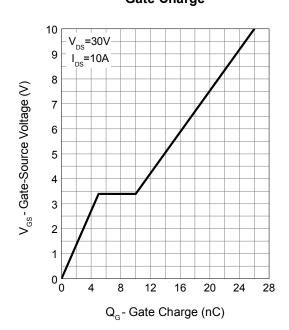
Source-Drain Diode Forward



Capacitance



Gate Charge





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