



General Description

The WST2002 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WST2002 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- High-speed switching
- Green Device Available
- ESD Protected:2KV

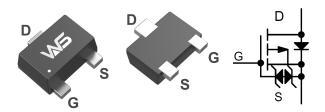
Product Summery

BVDSS	RDSON	ID
30V	7.5Ω	600mA

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC
- Networking DC-DC Power System
- Load Switch

SOT-723 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	600	mA
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	550	mA
I _{DM}	Pulsed Drain Current ²	0.8	А
P _D @T _A =25°C	Total Power Dissipation ³	0.15	W
T _{STG}	Storage Temperature Range	-55 to 150	$^{\circ}$
T _J	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹		625	°C/W



Electrical Characteristics (T_J=25 C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =10uA	30			V	
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =1mA		0.05		V/℃	
В	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =0.5A	7.5		7.5		
R _{DS(ON)}	Static Dialit-Source Off-Resistance	V_{GS} =5 V , I_D =0.05 A			7.5	Ω	
V _{GS(th)}	Gate Threshold Voltage		1	1.85	2.5	V	
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS} = V_{DS}$, $I_D = 250uA$		-3.7		mV/℃	
	Dunin Course Lookers Current	V_{DS} =30V , V_{GS} =0V , T_J =25 $^{\circ}$ C			1		
I _{DSS}	Drain-Source Leakage Current	V _{DS} =30V , V _{GS} =0V , T _J =55℃			3	uA	
I _{GSS}	Gate-Source Leakage Current V _{GS} =±20V , V _{DS} =0V				±10	uA	
gfs	Forward Transconductance	V _{DS} =5V , I _D =0.1A		80		mS	
T _{d(on)}	Turn-On Delay Time	V _{DD} =30V , V _{GS} =10V ,		12	20	20	
T _{d(off)}	Turn-Off Delay Time	R_G =10Ω, I_D =0.2A RL=150Ω		20	30	ns	
C _{iss}	Input Capacitance			25	50		
C _{oss}	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		10	25	pF	
C _{rss}	Reverse Transfer Capacitance			3.0	5.0		

^{*} Pw≤300μs, Duty cycle≤1%

Typical Characteristics

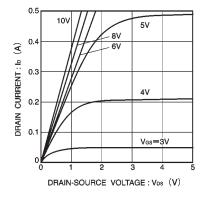


Fig.1 Typical output characteristics

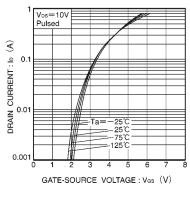


Fig.2 Typical transfer characteristics

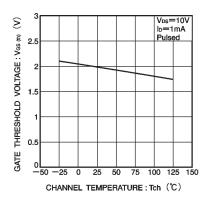
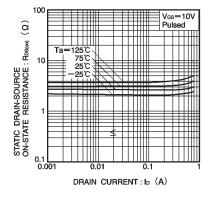
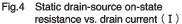


Fig.3 Gate threshold voltage vs. channel temperature







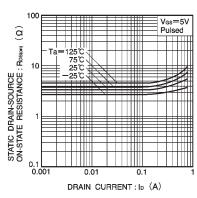


Fig.5 Static drain-source on-state resistance vs. drain current (II)

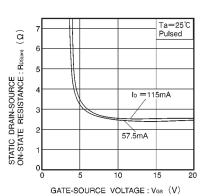


Fig.6 Static drain-source on-state resistance vs. gate-source voltage

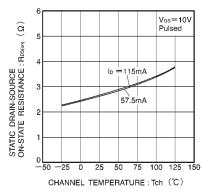


Fig.7 Static drain-source on-state resistance vs. channel temperature

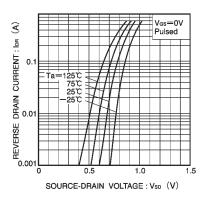


Fig.8 Reverse drain current vs. source-drain voltage (I)

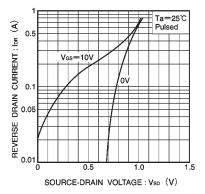


Fig.9 Reverse drain current vs. source-drain voltage (I)

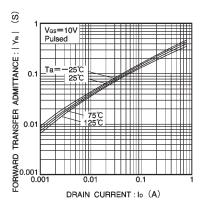


Fig.10 Forward transfer admittance vs. drain current

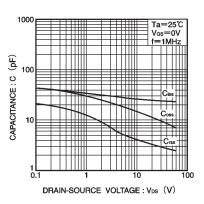


Fig.11 Typical capacitance vs. drain-source voltage

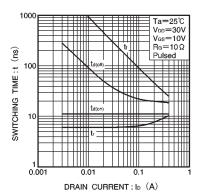
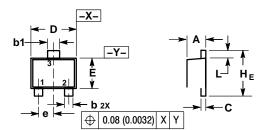


Fig.12 Switching characteristics
(See Figures 13 and 14 for the measurement circuit and resultant waveforms)



N-Ch MOSFET

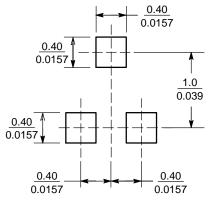
Packaging: SOT-723



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
 4. DIMENSIONS O AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.45	0.50	0.55	0.018	0.020	0.022
b	0.15	0.20	0.27	0.0059	0.0079	0.0106
b1	0.25	0.3	0.35	0.010	0.012	0.014
С	0.07	0.12	0.17	0.0028	0.0047	0.0067
D	1.15	1.20	1.25	0.045	0.047	0.049
Е	0.75	0.80	0.85	0.03	0.032	0.034
е	0.40 BSC			(0.016 BS	С
ΗE	1.15	1.20	1.25	0.045	0.047	0.049
L	0.15	0.20	0.25	0.0059	0.0079	0.0098

SOLDERING FOOTPRINT



 $\left(\frac{\text{mm}}{\text{inches}}\right)$



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