

Description

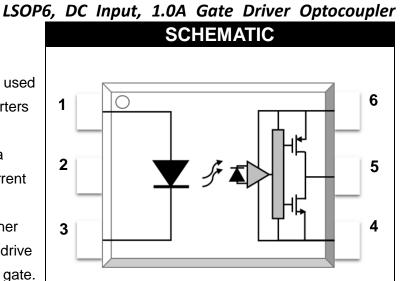
The MPCS-314 series Photocoupler is ideally suited for driving power IGBTs and MOSFETs used in motor control inverter applications and inverters in power supply system. It contains an LED optically coupled to an integrated circuit with a power output stage. The 1.0A peak output current is capable of directly driving most IGBTs with ratings up to 1200 V/50 A. For IGBTs with higher ratings, the MPCS-314 series can be used to drive a discrete power stage which drives the IGBT gate.

Features

- 1.0 A maximum peak output current
- Rail-to-rail output voltage
- 110 ns maximum propagation delay
- Under Voltage Lock-Out protection (UVLO) with hysteresis
- Wide operating range: 10 to 30 Volts (V_{cc})
- Guaranteed performance over temperature -40°C ~ +110°C.
- Regulatory Approvals
 - UL UL1577
 - VDE EN60747-5-5(VDE0884-5)
 - CQC GB4943.1, GB8898

Applications

- Isolated IGBT/Power MOSFET gate drive
- Industrial Inverter
- AC brushless and DC motor drives
- Induction Heating



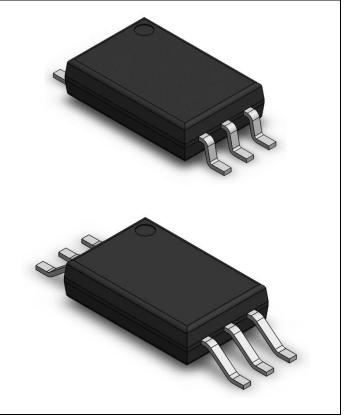
PIN DEFINITION

1. Anode 6. V_{cc}

2. NC

- **5. V**o
- 3. Cathode 4. GND

PACKAGE OUTLINE



Rev: 2.2



LSOP6, DC Input, 1.0A Gate Driver Optocoupler

TRUTH TABLE							
LED	V _{cc} -V _{ss}	V _{CC} -V _{SS}	VO				
LED	(Turn-ON, +ve going)	(Turn-OFF, -ve going)	VO				
Off	0V to 30V	0V to 30V	Low				
On	0V to 6.9V	0V to 5.9V	Low				
On	6.9V to 8.7V	5.9V to 7.5V	Transition				
On	8.7V to 30V	7.5V to 30V	High				

Note: A 0.1μ F bypass capacitor must be connected between Pin 4 and 6.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	MIN.	MAX.	UNIT	NOTE		
Storage Temperature	T _{stg}	-55	125	°C	-		
Operating Temperature	T _{opr}	-40	110	°C	-		
Output IC Junction Temperature	TJ	-	125	°C	-		
Total Output Supply Voltage	(Vcc –Vss)	0	35	V	-		
Average Forward Input Current	lf	-	20	mA	-		
Reverse Input Voltage	V _R	-	5	V	-		
"High" Peak Output Current	Іон(реак)	-	1.0	А	1		
"Low" Peak Output Current	IOL(PEAK)	-	1.0	А	1		
Output Voltage	V _{O(PEAK)}	-0.5	Vcc	V	-		
Power Dissipation	Pı	-	45	mW	-		
Output IC Power Dissipation	Po	-	250	mW	-		
Lead Solder Temperature	T _{sol}	-	260	°C	-		

Note: Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely affect reliability.

Note 1: Exponential waveform. Pulse width \leq 10 µs, f \leq 15 kHz

RECOMMENDED OPERATION CONDITIONS							
PARAMETER	SYMBOL	MIN.	MAX.	UNIT			
Operating Temperature	TA	-40	110	°C			
Supply Voltage	V _{CC}	10	30	V			
Input Current (ON)	I _{F(ON)}	5	16	mA			
Input Voltage (OFF)	V _{F(OFF)}	-3.0	0.8	V			



			LSOP6,	DC Inp	out, 1.0	A Gate Driver Opto	coupler	
ELECTRICAL OPTICAL CHARACTERISTICS								
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE	
	INPUT CHARACTERISTICS							
Forward Voltage	VF	1.6	1.9	2.4	V	I _F = 10 mA	-	
Input Forward Voltage Temperature Coefficient	Δνγ/ Δτ	-	-1.237	-	mV/°C	IF=10mA	-	
Input Reverse Voltage	BV _R	5	-	-	V	IR = 10μΑ	-	
Input Threshold Current (Low to High)	I _{FLH}	-	0.6	2	mA	V _O > 5V, I _O = 0A	-	
Input Threshold Voltage (High to Low)	VFHL	0.8	-	-	v	VCC = 30 V, VO < 5V	-	
Input Capacitance	CIN	-	60	-	pF	VF = 0, f = 1MHz	-	
		OUTPL	JT CHARA	CTERIST	ICS			
High Level Supply Current	Іссн	-	1.55	3	mA	$I_{F} = 10 \text{ mA}, \text{ V}_{CC} = 30 \text{ V},$ $V_{O} = \text{Open}$	-	
Low Level Supply Current	ICCL	-	1.92	3	mA	$I_{F} = 0 \text{ mA}, V_{CC} = 30 \text{ V},$ $V_{O} = \text{Open}$	-	
High Level Output Voltage	Vон	29.4	29.69	-	V	I _F = 10 mA, I _O = -100 mA	1,2	
Low Level Output Voltage	V _{OL}	-	0.17	0.4	V	I _F = 0 mA, I ₀ = 100 mA	-	
High Level Output Current	Іон	1.0	-	-	A	I _F = 10 mA, V _{CC} = 30V V _O = V _{CC} - 4	3	
Low Level Output Current	I _{OL}	1.0	-	-	A	$I_F = 0 \text{ mA}, V_{CC} = 30V$ $V_O = V_{SS} + 4$	3	
Under Voltage Lockout	V _{UVLO+}	6.9	7.8	8.7	V	$V_0 > 5V, I_F = 10 \text{ mA}$	-	
Threshold	Vuvlo-	5.9	6.9	7.5	V	$V_0 < 5V, I_F = 10 \text{ mA}$	-	

ICODC

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Note: All Typical values at $T_A = 25^{\circ}$ C and $V_{CC} - V_{SS} = 30$ V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: In this test V_{OH} is measured with a dc load current. When driving capacitive loads, V_{OH} will approach V_{CC} as I_{OH} approaches zero amps.

Note 2: Maximum pulse width = 1 ms.

Note 3: Maximum pulse width = 10 μ s.



$\begin{array}{ c c c c c c } \hline SWITCHING SPECIFICATION \\ \hline PARAMETER & SYMBOL & MIN. & TYP. & MAX. & UNIT & TEST CONDITION & NOTE \\ \hline Propagation Delay Time to Output Low Level & t_{PHL} & - & 54 & 110 & ns & Rg = 47 \ \Omega, & Cg = 3 \ nF, & f = 10 \ HZ, & Cg = 3 \ nF, & f = 10 \ HZ, & Cg = 3 \ nF, & f = 10 \ HZ, & Cg = 3 \ nF, & f = 10 \ HZ, & Cg = 3 \ nF, & f = 10 \ HZ, & Duty Cycle = 50\%$				LSOP6,	DC Input	t, 1.0A	Gate Driver Optoc	oupler	
SWITCHING CHARACTERISTICSPropagation Delay Time to Output Low Level t_{PHL} -54110ns $Rg = 47 \Omega$, $Cg = 3 nF,$ -Propagation Delay Time to Output High Level t_{PLH} -69110ns $Rg = 47 \Omega,$ $Cg = 3 nF,$ -Pulse Width Distortion Between Any Two Parts P_{WD} -2270ns $Rg = 47 \Omega,$ $Cg = 3 nF,$ $f = 10kHz,$ Duty Cycle = 50%-Rise Time P_{DD} $(t_{PHL} - t_{PLH})$ -100- $+100$ ns $Rs = 10mA,$ $V_{CC} = 30V$ -Rise Time t_r -10-nsCommon Mode Transient Immunity at Logic High CM_H 2040- $kV/\mu s$ IF=7 to 16mA Vcc= 30V, $T_A = 25 °C,$ 1,2Common Mode Transient Immunity at Logic Low CM_L 2040- $kV/\mu s$ IF=0mA Vcc= 30V, $T_A = 25 °C,$ 1,3	SWITCHING SPECIFICATION								
$ \begin{array}{ c c c c c } \hline Propagation Delay Time to Output Low Level & t_{PHL} & - & 54 & 110 & ns & Rg = 47 \ \Omega, \\ \hline Propagation Delay Time to Output High Level & t_{PLH} & - & 69 & 110 & ns & Rg = 47 \ \Omega, \\ \hline Cg = 3 \ nF, & f = 10 \ Hz, & f = 10 \ Hz, & Duty \ Cycle = 50\% & f = 10 \ Hz, & Duty \ Cycle = 50\% & IF = 10 \ Hz, & Duty \ Cycle = 50\% & IF = 10 \ Hz, & Duty \ Cycle = 50\% & IF = 10 \ Hz, & Duty \ Cycle = 50\% & IF = 10 \ Hz, & Duty \ CM_H & CM_H & 20 & 40 & - & ns & IF = 7 \ to 16 \ Mz, & Cg = 30V, \\ \hline Common \ Mode \ Transient \ Immunity \ at \ Logic \ High & CM_L & 20 & 40 & - & kV/\mus & IF = 0 \ Mz, & IF = 0 \ Mz, & Cg = 30V, \\ \hline Ta = 25 \ ^{\circ}C, & 1,3 \end{array} $	PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $:	SWITCHI	NG CHAR	ACTERISTIC	CS			
to Output Low LevelNormalizationNormalizationNormalizationRg = 47 Ω , Cg = 3 nF,Rg = 47 Ω , Cg = 3 nF,Rewer, Cg = 3 nF,Rewer, Ch Hz,Rewer, PoloRewer, Cg = 3 nF,Rewer, Cg	Propagation Delay Time	to: "	_	54	110	ne		_	
to Output High LeveltPLH-69110nsCg = 3 nF, f = 10kHz, Duty Cycle = 50%-Pulse Width Distortion P_{WD} -2270ns $f = 10kHz,$ Duty Cycle = 50%-Propagation Delay Difference Between Any Two Parts P_{DD} (tPHL - tPLH)-100-+100nsns $I_F = 10mA,$ Vcc = 30V-Rise Timetr-10-nsFall Timetr-100-nsIF=7 to 16mA Vcc= 30V, VcM It=7 to 16mA Vcc= 30V, T_A = 25 °C,Common Mode Transient Immunity at Logic HighCML2040-kV/µsIF=0mA Vcc= 30V, T_A = 25 °C,1,3	to Output Low Level	(PHL		- 54	110	115		_	
to Output High LevelImage: Constraint of the second s	Propagation Delay Time	touu	_	69	110	ne	Rg = 47 Ω,	_	
Pulse Width Distortion P_{WD} - 22 70nsDuty Cycle = 50%-Propagation Delay Difference P_{DD} (tPHL - tPLH)-100- $+100$ ns $I_F = 10mA$, $V_{CC} = 30V$ -Rise Timetr-10-ns $V_{CC} = 30V$ -Fall Timetr-10-ns-Common Mode Transient Immunity at Logic HighCML2040- $kV/\mu s$ IF=7 to 16mA Vcc= 30V, $T_A = 25 °C,$ 1,2Common Mode Transient Immunity at Logic LowCML2040- $kV/\mu s$ IF=0mA Vcc= 30V, $T_A = 25 °C,$ 1,3	to Output High Level	UPLH		03	110	113	Cg = 3 nF,		
Image: Normal stateImage: Normal	Pulse Width Distortion	Pwp	-	22	70	ns		_	
Between Any Two Parts $(t_{PHL} - t_{PLH})$ -100 $ +100$ ns $V_{CC} = 30V$ $-$ Rise Timetr $ 10$ $-$ ns $V_{CC} = 30V$ $-$ Fall Timetr $ 10$ $-$ ns $V_{CC} = 30V$ $-$ Common Mode Transient Immunity at Logic High CM_H 20 40 $ kV/\mu s$ $I_F=7$ to $16mA V_{CC}= 30V,$ $T_A= 25 °C,$ $1,2$ Common Mode Transient Immunity at Logic Low CM_L 20 40 $ kV/\mu s$ $I_F=0mA V_{CC}= 30V,$ $T_A= 25 °C,$ $1,3$									
Between Any Two Parts $(t_{PHL} - t_{PLH})$ Image: Second se	Propagation Delay Difference	P _{DD}	-100	_	+100	ns	I _F = 10mA,	-	
Fall Time t_{f} -10-ns-Common Mode Transient Immunity at Logic High CM_{H} 2040- $kV/\mu s$ IF=7 to 16mA Vcc= 30V, T_A= 25 °C, V_{CM} = 1kV1,2Common Mode Transient Immunity at Logic Low CM_{L} 2040- $kV/\mu s$ IF=0mA Vcc= 30V, T_A= 25 °C, T_A= 25 °C,1,3	Between Any Two Parts	(t _{PHL} - t _{PLH})	100				$V_{CC} = 30V$		
Common Mode Transient Immunity at Logic HighCMH2040- $kV/\mu s$ IF=7 to 16mA Vcc= 30V, T_A= 25 °C, V_{CM}= 1kV1,2Common Mode Transient Immunity at Logic LowCML2040- $kV/\mu s$ IF=0mA Vcc= 30V, T_A= 25 °C,1,3	Rise Time	tr	-	10	-	ns		-	
Common Mode Transient Immunity at Logic HighCMH2040- $kV/\mu s$ $T_A=25 ^{\circ}C,$ $V_{CM}=1kV$ 1,2Common Mode Transient Immunity at Logic LowCML2040- $kV/\mu s$ $I_F=0mA V_{CC}=30V,$ $T_A=25 ^{\circ}C,$ 1,3	Fall Time	t _f	-	10	-	ns		-	
Immunity at Logic High CM_H 20 40 $ kV/\mu s$ $T_A=25 ^{\circ}C,$ $1,2$ Common Mode Transient Immunity at Logic Low CM_L 20 40 $ kV/\mu s$ $I_F=0mA V_{CC}=30V,$ $T_A=25 ^{\circ}C,$ $1,3$	Common Mode Transient						IF=7 to 16mA Vcc= $30V$,		
Common Mode Transient Immunity at Logic LowCML2040- $kV/\mu s$ $T_A=25 ^{\circ}C$,1,3		CM _H 20	20	40	-	- kV/µs	T _A = 25 °C,	1,2	
Common Mode TransientCML2040-kV/µsTA= 25 °C,1,3Immunity at Logic LowCML2040-kV/µsTA= 25 °C,1,3	Infiniturity at Logic Figh						V _{CM} = 1kV		
Immunity at Logic Low CM_L 20 40 - $kV/\mu s$ $T_A= 25 °C$, 1,3							IF=0mA Vcc= 30V,		
		CM∟	20	40	-	kV/µs	T _A = 25 °C,	1,3	
V _{CM} = 1kV							V _{CM} = 1kV		

Note: All Typical values at $T_A = 25^{\circ}$ C and $V_{CC} - V_{SS} = 30$ V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Pin 2 needs to be connected to LED common.

Note 2: Common mode transient immunity in the high state is the maximum tolerable dVCM/dt of the common mode pulse, VCM, to assure that the output will remain in the high state (meaning VO > 10.0V). Note 3: Common mode transient immunity in a low state is the maximum tolerable dVCM/dt of the common mode pulse, VCM, to assure that the output will remain in a low state (meaning VO < 1.0V).



LSOP6, DC Input, 1.0A Gate Driver Optocoupler

ISOLATION CHARACTERISTIC								
PARAMETER	SYMBOL	DEVICE	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
Withstand Insulation	Viso	MPCS-314P	5000	-	-	V	RH ≤ 40%-60%,	1,2
Test Voltage	VISO	MPCS-314W					t = 1min, T _A = 25 °C	
Input-Output	R _{I-0}			10 ¹²		Ω	V _{I-0} = 500V DC	1
Resistance	r .1-0	-	-	1012	-	12	$v_{1-0} = 500 v DC$	I

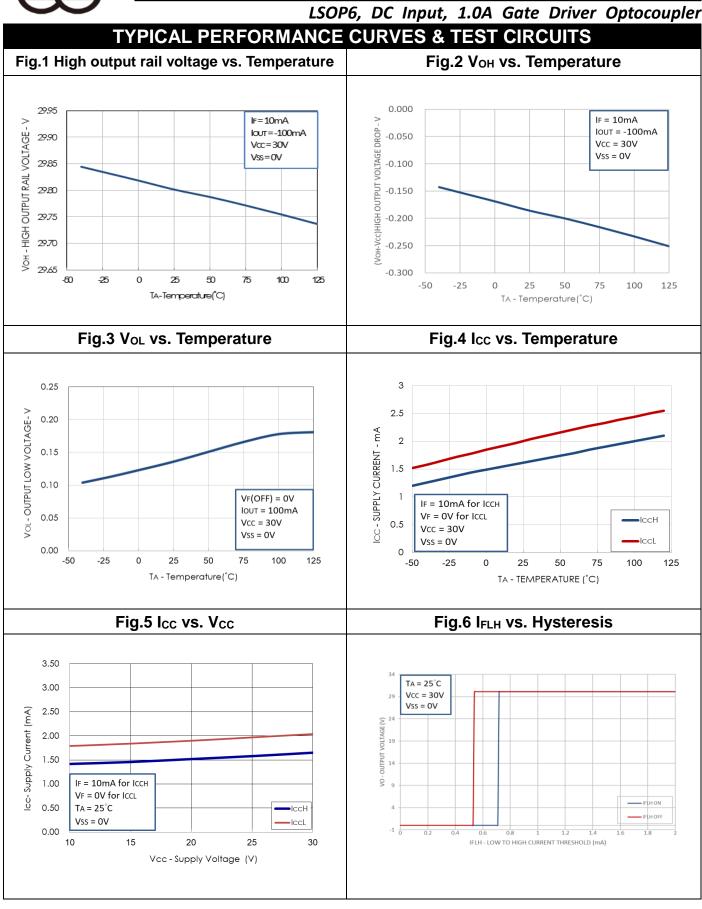
Note: All Typical values at $T_A = 25^{\circ}$ C and $V_{CC} - V_{SS} = 30$ V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Device is considered a two terminal device: pins 1, 2, 3 are shorted together and pins 4, 5, 6 are shorted together.

Note 2: According to UL1577, each photocoupler is tested by applying an insulation test voltage

6000VRMS for one second. This test is performed before the 100% production test for partial discharge.

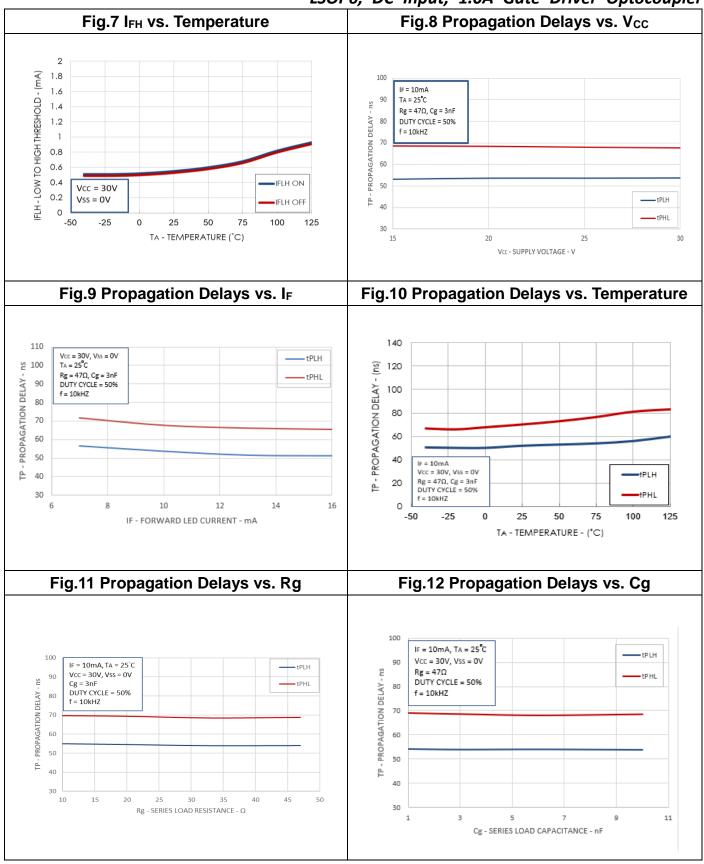




Rev: 2.2

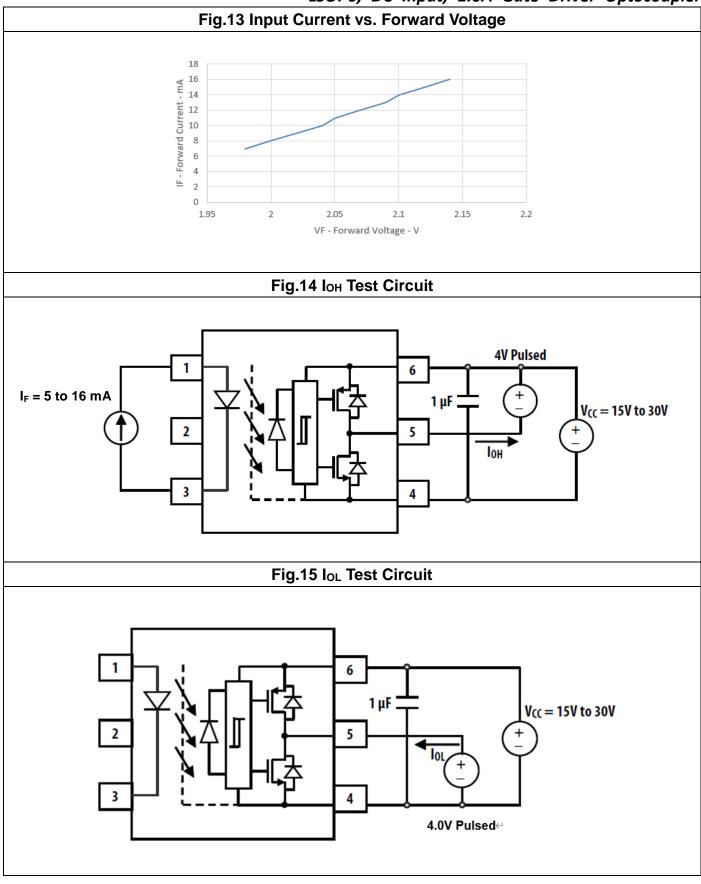


LSOP6, DC Input, 1.0A Gate Driver Optocoupler



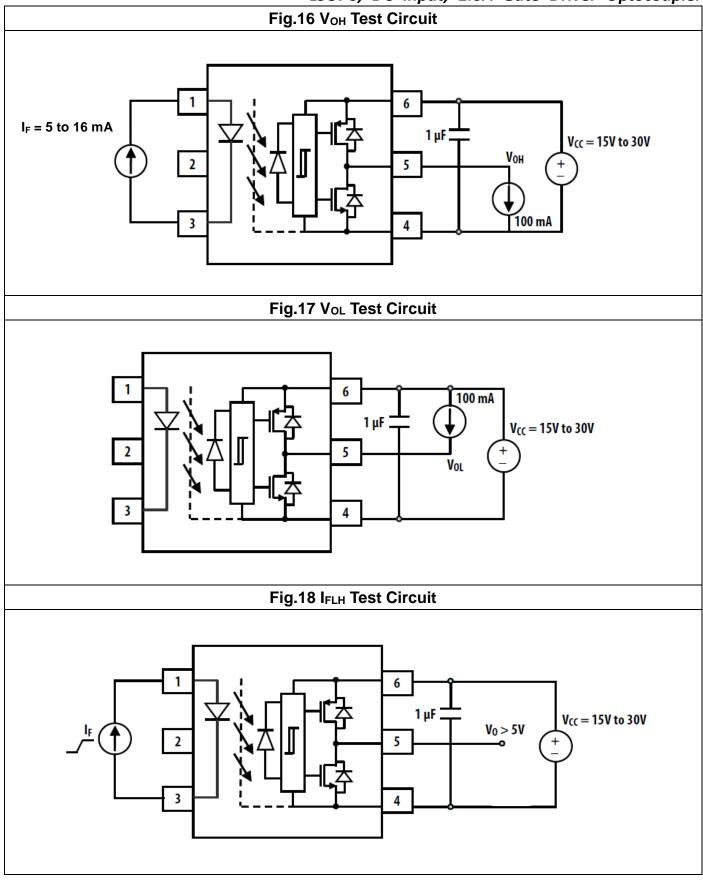








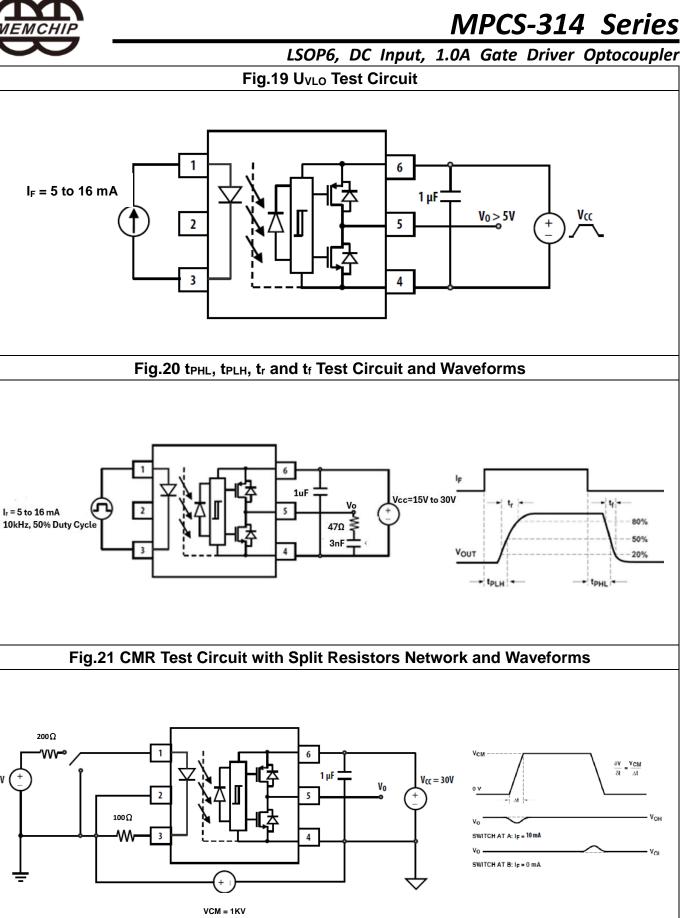






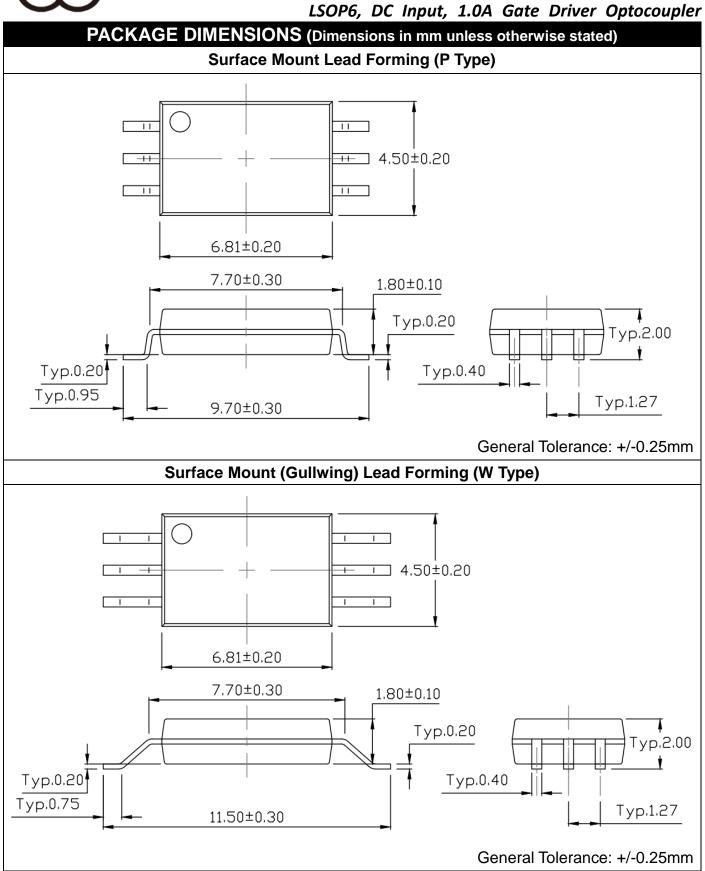
5V

MPCS-314 Series



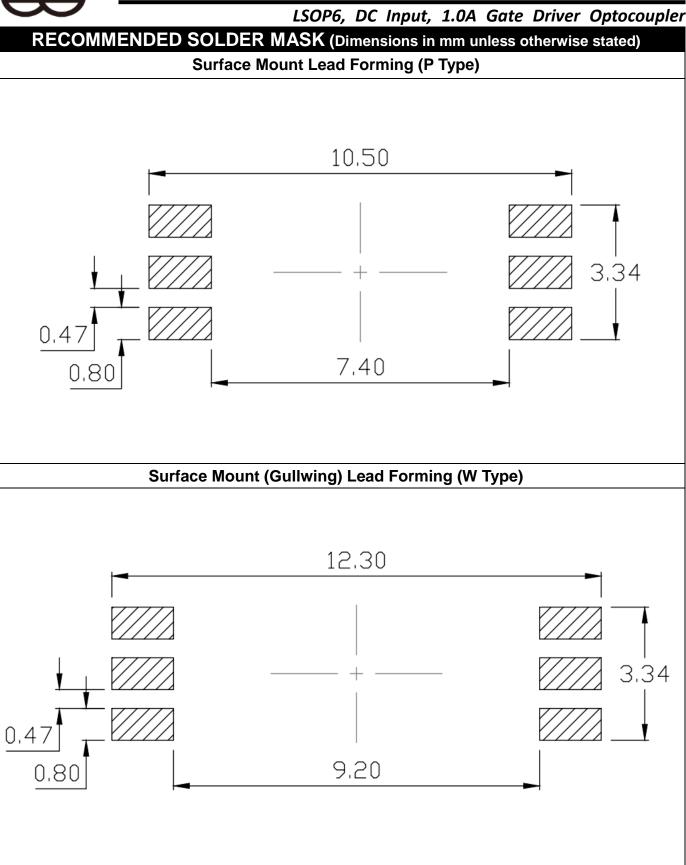
Rev: 2.2





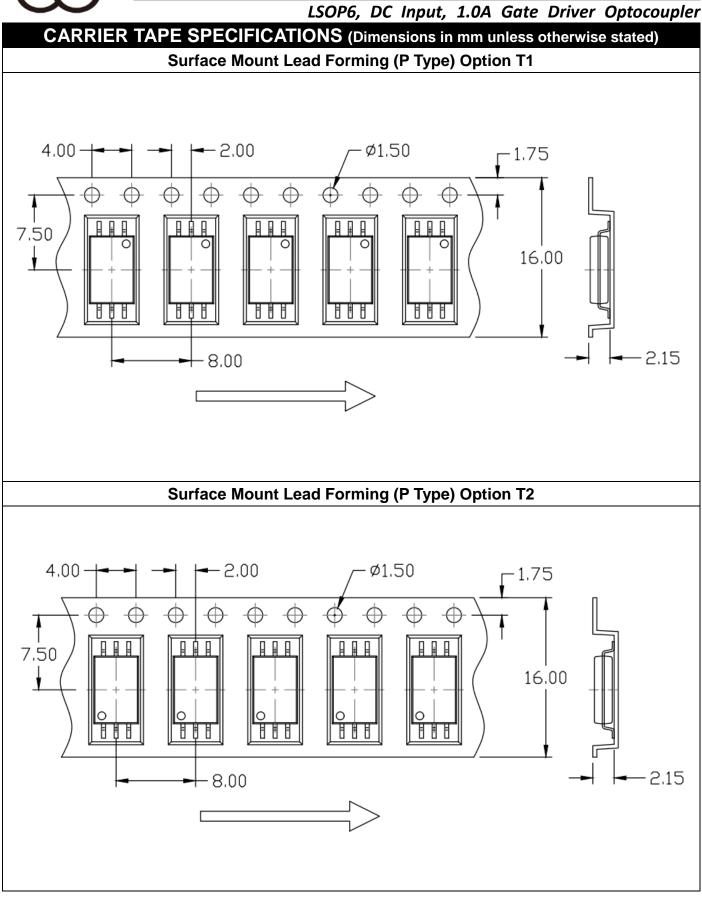
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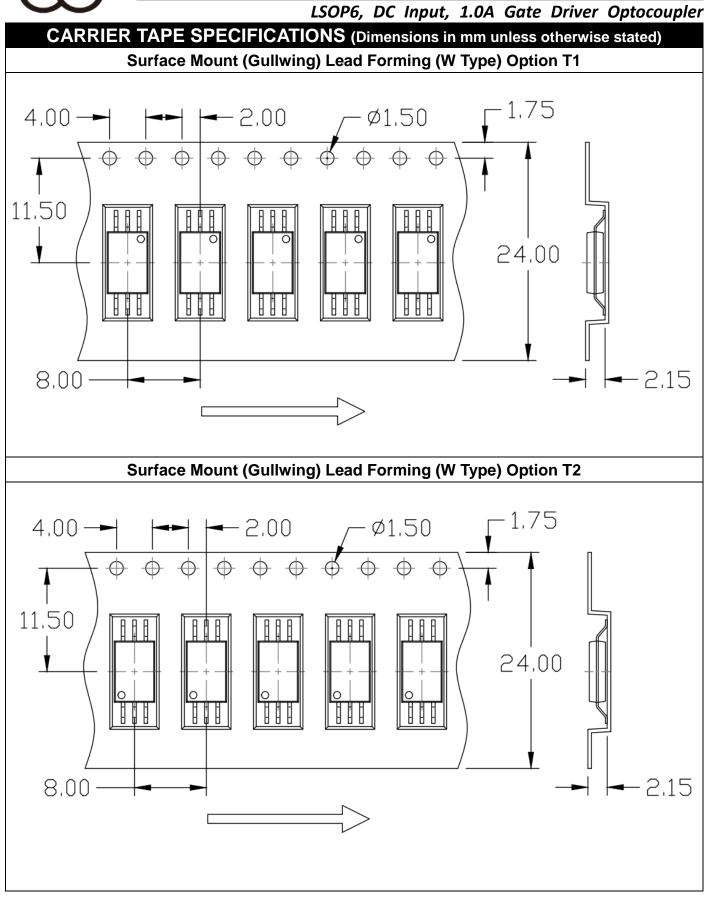


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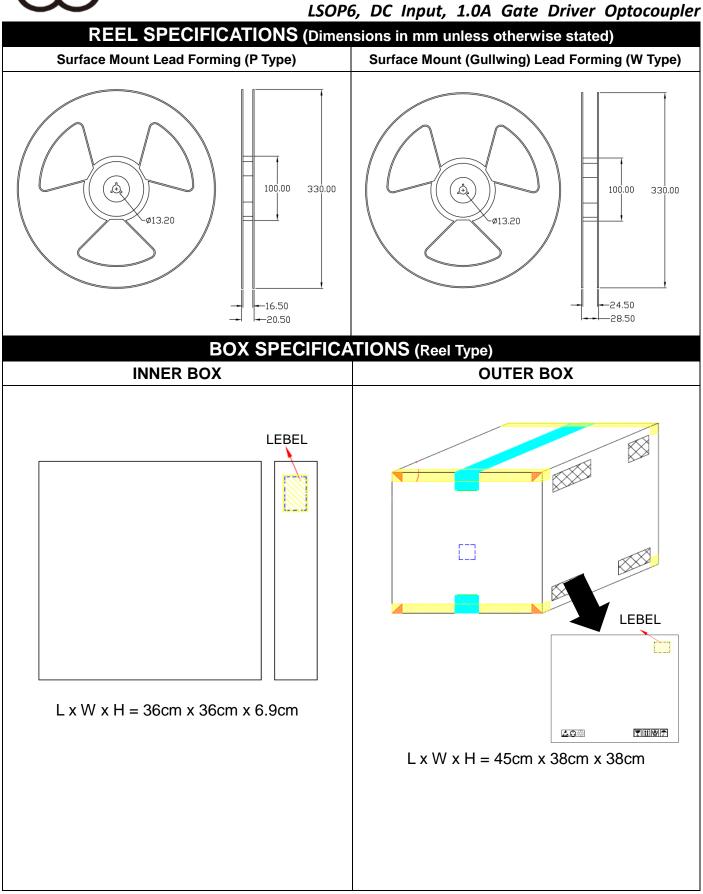














Option W T1/T2

3000 Units/Reel

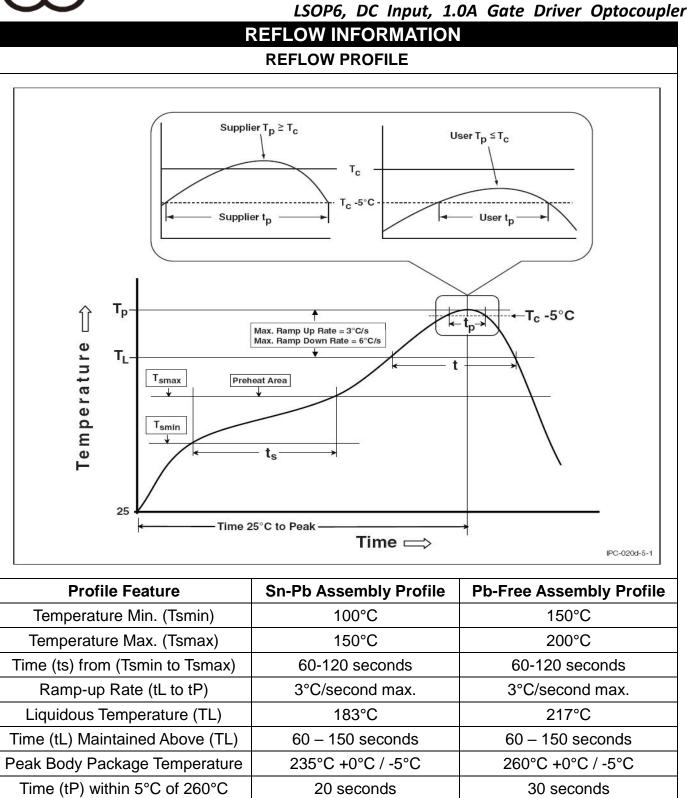
MPCS-314 Series

\mathbf{D}	LSOP6, DC Input, 1.0A Gate Driver Optocoupler						
ORDERING AND MARKING INFORMATION							
MARKING INFORMATION							
MYYWW 314 TV				: Company Abbr. : Year date code : 2-digit work week : Part Number H : Factory identification mark : VDE Identification(Option)			
ORD	ERING INFORMAT	ION		LABEL INFORMATION			
MPCS-314(P/W)-ZV MPC – Company Abbr. S – Stack 314 – Part Number P/W – Lead Form Option (P-9mm Clearance or W-11mm Clearance) Z – Tape and Reel Option (T1/T2) V –VDE Option (V or None)			Lot Date Q'ty	赴 新光照明光電股份有限公司 WISELITE Optronics Co., Ltd x No : XXXXXXXXXXX Bin Code : X No : XXXXXXXXXXXX No : XXXXXXXXXXX x Code : XXXX x XXX pcs			
	1	NTITY	1				
Option	Quantity	Quantity – Inner	r box	Quantity – Outer box			
Option P T1/T2	3000 Units/Reel	3 Reels/Inner box		5 Inner box/Outer box = 45k Units			

5 Inner box/Outer box = 30k Units

2 Reels/Inner box





6°C/second max

8 minutes max.

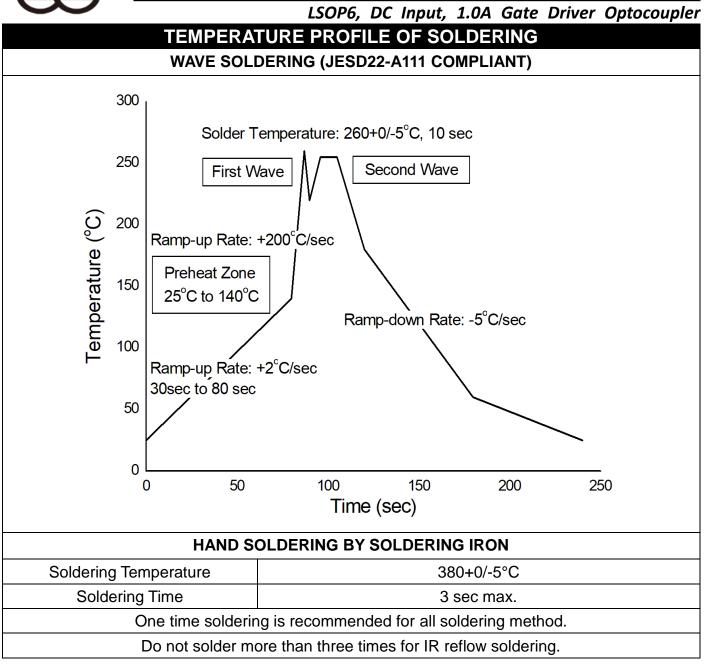
6°C/second max

6 minutes max.

Ramp-down Rate (TP to TL)

Time 25°C to Peak Temperature





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LSOP6, DC Input, 1.0A Gate Driver Optocoupler DISCLAIMER

- WISELITE is continually improving the quality, reliability, function and design. WISELITE reserves the right to make changes without further notices.
- The characteristic curves shown in this datasheet are representing typical performance which are not guaranteed.
- WISELITE makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, WISELITE disclaims (a) any and all liability arising out of the application or use of any product, (b) any and all liability, including without limitation special, consequential or incidental damages, and (c) any and all implied warranties, including warranties of fitness for particular.
- The products shown in this publication are designed for the general use in electronic applications such as office automation, equipment, communications devices, audio/visual equipment, electrical application and instrumentation purpose, non-infringement and merchantability.
- This product is not intended to be used for military, aircraft, medical, life sustaining or lifesaving applications or any other application which can result in human injury or death.
- Please contact WISELITE sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated in each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify WISELITE's terms and conditions of purchase, including but not limited to the warranty expressed therein.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.

单击下面可查看定价,库存,交付和生命周期等信息

>>WISELITE(喆光)