

# **Product Specification**

## **XBLW** UA741

General-purpose Operational Amplifiers

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#### Description

The UA741 is a general-purpose operational amplifiers.

The amplifiers offer many features which make their application nearly foolproof: overload protection on the input and output, no latch-up when the common-mode range is exceeded, as well as freedom from oscillations.

#### Features

- > Overload Protection on the Input and Output
- > No Latch-Up When the Common-Mode Range is Exceeded

## Apply

- > Comparators
- Multivibrators
- DC Amplifiers
- Summing Amplifiers
- > Integrator or Differentiators
- Active Filters

#### **Ordering Information**

Product Model	Package Type	Marking	Packing	Packing Qty
UA741CP	DIP-8	UA741CP	Tube	2000Pcs/Box
UA741CDTR	SOP-8	UA741C	Таре	2500Pcs/Reel

#### **Typical Applications**





### **Pin Configuration and Functions**



Pin		I/O	Description	
Name	No.			
INVERTING INPUT	2	I	Inverting signal input	
NC	8	N/A	No Connect, should be left floating	
NONINVERTING INPUT	3	I	Noninverting signal input	
OFFSET NULL	1,5	I	Offset null pin used to eliminate the offset voltage and balance the input	
OFFSET NULL			voltages.	
OUTPUT	6	0	Amplified signal output	
V+	7	I	Positive supply voltage	
V-	4	I	Negative supply voltage	

### Absolute Maximum rating

over operating free-air temperature range (unless otherwise noted)(1)(2)(3)

		Min	Max	Unit
Supply voltage			±22	V
Power dissipation			500	mW
Differential input voltage			±30	V
Input voltage		±15	V	
Output short circuit duration	Continuous			
Operating temperature		0	70	°C
Junction temperature			150	°C
Soldering information	DIP package (10 seconds)		260	°C
Storage temperature, Tstg		-65	150	°C



## XBLW UA741 General-purpose Operational Amplifiers

### **Electrical characteristics**

-11035 OUTCHWISC SDCCINCU, UTCSC SDCCINCUTORIS ADDIVITOR VOT $-115$ V
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Parame	eter	Test Conditions		Min	Тур	Max	Unit
Input c	offset voltage	RS≤10KΩ TA=25℃			2	6	mV
			TAMIN≤TA≤TAMAX			7.5	mV
Input o adjustr	offset voltage nent range	Ta=25°C,Vs=±20V			±15		mV
Input c	offset current	Ta=25℃			20	200	nA
		Tamin≤Ta≤Tamax				300	
Input b	oias current	Ta=25℃			80	500	nA
		Tamin≤Ta≤Tamax				0.8	μA
Input r	esistance	Ta=25°C,Vs=±20V		0.3	2		MΩ
Input v	oltage range	Ta=25℃		±12	±13		V
Large s	signal voltage	Vs=±15V,VO=±10V, RL≥2KΩ	Ta=25℃	20	200		V/mV
gain			Tamin≤Ta≤Tamax	15			
Output	voltage swing	Vs=±15V	R∟≥10KΩ	±12	±14		V
			R∟≥2KΩ	±10	±13		
Output current	short circuit	Ta=25℃			25		mA
Commo	on-mode on ratio	Rs≤10KΩ ,Vcm= ±12V,Tamin≤Ta≤Tamax		70	90		dB
Supply rejectio	pply voltage $V_{s=\pm 20V}$ to $V_{s=\pm 5V,R_{s} \le 10\Omega}$ , TAMIN $\le T_{A} \le T_{AMAX}$		77	96		dB	
Trans	Rise time	T₄=25℃,unity gain			0.3		μs
ient	Overshoot				5%		
respo							
nse							
Slew rate		T₄=25℃,unity gain			0.5		V/ µs
Supply	current	Ta=25℃			1.7	2.8	mA
Power consumption		Vs=±15V,Ta=25℃			50	85	mW

## **Functional Block Diagram**





#### **Feature Description**

#### **Overload Protection**

The UA741 features overload protection circuitry on the input and output. This prevents possible circuit damage to the device.

#### Latch-up Prevention

The UA741 is designed so that there is no latch-up occurrence when the common-mode range is exceeded. This allows the device to function properly without having to power cycle the device.

#### **Typical Application**



Figure 1. UA741 Noninverting Amplifier Circuit

#### **Design Requirements**

As shown in Figure 1, the signal is applied to the noninverting input of the UA741. The gain of the system is determined by the feedback resistor and input resistor connected to the inverting input. The gain can be calculated by Equation 1:

Gain = 1 + (R2/R1)

(1) The gain is set to 2 for this application. R1 and R2 are 4.7-k resistors with 5% tolerance.

#### **Detailed Design Procedure**

The UA741 can be operated in either single supply or dual supply. This application is configured for dual supply with the supply rails at  $\pm 15$  V. The input signal is connected to a function generator. A 1-Vpp, 10-kHz sine wave was used as the signal input. 5% tolerance resistors were used, but if the application requires an accurate gain response, use 1% tolerance resistors.



## Package Outline Dimensions

SOP-8





SYMPOL	MILLIMETER					
STMBOL	MIN	NOM	МАХ			
A	1.55	1.65	1.75			
A1	0.10	0.20				
A2	1.35	1.35 1.45 1.55				
A3	0.60	0.70	0.80			
b	0.30	0.40	0.50			
c	0.17	0.20	0.25			
D	4.80	4.80 4.90 5.0				
E	5.80	5.80 6.00 6.20				
E1	3.80	3.90	4.00			
e	1.27BSC					
L	0.50 0.60 0.70					
L1	1.05REF					
θ	0°	0° 4° 8°				



#### DIP-8



	Dimensions in Millimeters				
Symbol	Min	Nom	Max		
А			4.31		
A1	3.15	3.30	3.65		
В	0.38	0.46	0.51		
B1	1.27	1.55	1.77		
С	0.20	0.25	0.30		
D	8.95	9.40	9.45		
Е	6.15	6.20	6.65		
E1		7.60			
e		2.54			
L	3.00	3.30	3.60		



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