

Product Specification

XBLW SN74AHC1G08

Single 2-input AND Gate

WEB | www.xinboleic.com



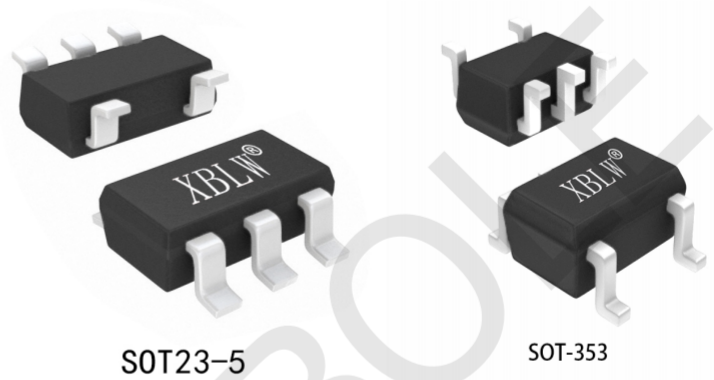
Description

The SN74AHC1G08 is a single 2-input AND gate. Inputs are overvoltage tolerant.

This feature allows the use of these devices as translators in mixed voltage environments.

Features

- Balanced propagation delays
- Low power dissipation
- Specified from -40°C to +125 °C
- Symmetrical output impedance
- Packaging information: SOT23-5/SOT353
- CMOS low power dissipation
- Wide supply voltage range from 2.0 V to 5.5 V



Applications

- Barcode Scanners
- Cable Solutions
- E-Books
- Embedded PCs
- Field Transmitter: Temperature or Pressure Sensors
- Fingerprint Biometrics
- HVAC: Heating, Ventilating, and Air Conditioning
- Network-Attached Storage (NAS)
- Server Motherboard and PSU
- Software Defined Radios (SDR)
- TV: High Definition (HDTV), LCD, and Digital
- Video Communications Systems
- Wireless Data Access Cards, Headsets, Keyboards, Mice, and LAN Cards

Ordering Information

Product Model	Package Type	Marking	Packing	Packing Qty
XBLW SN74AHC1G08T235	SOT-23-5	CLXX	Tape	3000Pcs/Reel
XBLW SN74AHC1G08T353	SOT-353	CLXX	Tape	3000Pcs/Reel

Block Diagram

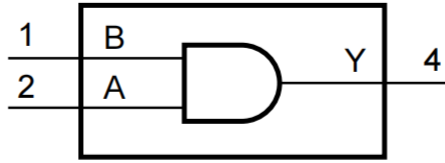


Figure 1. Logic symbol

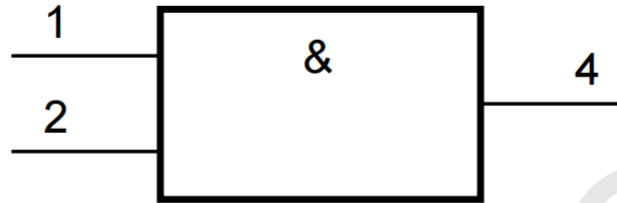


Figure 2. IEC logic symbol

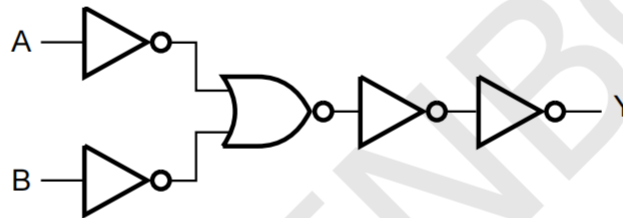
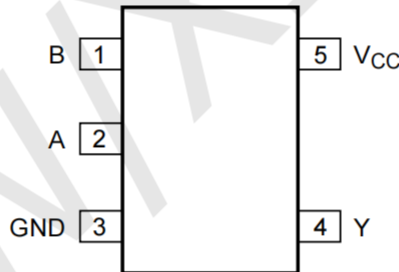


Figure 3. Logic diagram

Pin Configurations



Pin Description

Pin No.	Pin Name	Description
1	B	data input
2	A	data input
3	GND	ground (OV)
4	Y	data output
5	Vcc	supply voltage

Function Table

Input		Output
A	B	Y
L	L	L
L	H	L
H	L	L
H	H	H

Note: H=HIGH voltage level; L=LOW voltage level.

Electrical Parameter

Absolute Maximum Ratings

($T_{amb}=25^{\circ}\text{C}$, All voltage referenced to GND, unless otherwise specified)

Characteristic	Symbol	Conditions	Min.	Max.	Unit
supply voltage	V_{CC}	-	-0.5	+7.0	V
input voltage	V_I	-	-0.5	+7.0	V
input clamping current	I_{IK}	$V_I < -0.5\text{V}$	-20	-	mA
output clamping current	I_{OK}	$V_O < -0.5\text{V}$ or $V_O > V_{CC} + 0.5\text{V}$	-	± 20	mA
output current	I_O	$-0.5\text{V} < V_O < V_{CC} + 0.5\text{V}$	-	± 25	mA
supply current	I_{CC}	-	-	75	mA
ground current	I_{GND}	-	-75	-	mA
storage temperature	T_{stg}	-	-65	+150	$^{\circ}\text{C}$
total power dissipation	P_{tot}	-	-	250	mW
soldering temperature	T_L	10s	260		$^{\circ}\text{C}$

Recommended Operating Conditions

(Voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
supply voltage	V_{CC}	-	2.0	5.0	5.5	V
input voltage	V_I	-	0		5.5	v
output voltage	V_O	-	0		V_{CC}	v
ambient temperature	T_{amb}	-	-40		+125	$^{\circ}\text{C}$
input transition rise and fall rate	$\Delta t/\Delta V$	$V_{CC}=3.3\text{V}\pm 0.3\text{V}$	-		100	ns/V
		$V_{CC}=5.0\text{V}\pm 0.5\text{V}$	-		20	ns/V

ESD Ratings

Parameter	Defintion	Vaue	Unit
$V_{(ESD)}$ Electrostatic discharge	Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins ⁽¹⁾	± 2000	V
	Charged device model (CDM), per JEDEC specification JESD22-C101, all pins ⁽²⁾	± 1000	

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

DC Characteristics

 (T_{amb}=25°C, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
HIGH-level input voltage	V _{IH}	V _{CC} =2.0V	1.5		-	V	
		V _{CC} =3.0V	2.1		-	V	
		V _{CC} =5.5V	3.85		-	V	
LOW-level input voltage	V _{IL}	V _{CC} =2.0V	-		0.5	V	
		V _{CC} =3.0V	-		0.9	V	
		V _{CC} =5.5V	-		1.65	V	
HIGH-level output voltage	V _{OH}	V _I = V _{IH} or V _{IL}	I _O =-50μA; V _{CC} =2.0V	1.9			V
			I _O =-50μA; V _{CC} =3.0V	2.9			V
			I _O =-50μA; V _{CC} =4.5V	4.4			V
			I _O =-4mA; V _{CC} =3.0V	2.4			V
			I _O =-8mA; V _{CC} =4.5V	3.7			V
LOW-level output voltage	V _{OL}	V _I = V _{IH} or V _{IL}	I _O =50μA; V _{CC} =2.0V			0.1	V
			I _O =50μA; V _{CC} =3.0V			0.1	V
			I _O =50μA; V _{CC} =4.5V			0.1	V
			I _O =4mA; V _{CC} =3.0V			0.55	V
			I _O =8mA; V _{CC} =4.5V			0.55	V
input leakage current	I _I	V _I =5.5V or GND; V _{CC} =0V to 5.5V	-	-	2.0	μA	
supply current	I _{CC}	V _I =V _{CC} or GND; I _O =0A; V _{CC} =5.5V	-	-	40	μA	
input capacitance	C _I	-	-	-	10	pF	

DC Characteristics 2

 (T_{amb}=-40°C to +85°C, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
HIGH-level input voltage	V _{IH}	V _{CC} =2.0V	1.5		-	V	
		V _{CC} =3.0V	2.1		-	V	
		V _{CC} =5.5V	3.85		-	V	
LOW-level input voltage	V _{IL}	V _{CC} =2.0V	-		0.5	V	
		V _{CC} =3.0V	-		0.9	V	
		V _{CC} =5.5V	-		1.65	V	
HIGH-level output voltage	V _{OH}	V _I = V _{IH} or V _{IL}	I _O =-50μA; V _{CC} =2.0V	1.9			V
			I _O =-50μA; V _{CC} =3.0V	2.9			V
			I _O =-50μA; V _{CC} =4.5V	4.4			V
			I _O =-4mA; V _{CC} =3.0V	2.48			V
			I _O =-8mA; V _{CC} =4.5V	3.8			V
LOW-level output voltage	V _{OL}	V _I = V _{IH} or V _{IL}	I _O =50μA; V _{CC} =2.0V			0.1	V
			I _O =50μA; V _{CC} =3.0V			0.1	V
			I _O =50μA; V _{CC} =4.5V			0.1	V
			I _O =4mA; V _{CC} =3.0V			0.44	V
			I _O =8mA; V _{CC} =4.5V			0.44	V
input leakage current	I _I	V _I =5.5V or GND; V _{CC} =0V to 5.5V	-	-	1.0	μA	
supply current	I _{CC}	V _I =V _{CC} or GND; I _O =0A; V _{CC} =5.5V	-	-	10	μA	
input capacitance	C _I	-	-	-	10	pF	

DC Characteristics 3

 (T_{amb}=-40°C to +125°C, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
HIGH-level input voltage	V _{IH}	V _{CC} =2.0V	1.5		-	V	
		V _{CC} =3.0V	2.1		-	V	
		V _{CC} =5.5V	3.85		-	V	
LOW-level input voltage	V _{IL}	V _{CC} =2.0V	-		0.5	V	
		V _{CC} =3.0V	-		0.9	V	
		V _{CC} =5.5V	-		1.65	V	
HIGH-level output voltage	V _{OH}	V _i = V _{ih} or V _{il}	I _o =-50uA; V _{CC} =2.0V	1.9			V
			I _o =-50uA; V _{CC} =3.0V	2.9			V
			I _o =-50uA; V _{CC} =4.5V	4.4			V
			I _o =-4mA; V _{CC} =3.0V	2.4			V
			I _o =-8mA; V _{CC} =4.5V	3.7			V
LOW-level output voltage	V _{OL}	V _i = V _{ih} or V _{il}	I _o =50uA; V _{CC} =2.0V			0.1	V
			I _o =50uA; V _{CC} =3.0V			0.1	V
			I _o =50uA; V _{CC} =4.5V			0.1	V
			I _o =4mA; V _{CC} =3.0V			0.55	V
			I _o =8mA; V _{CC} =4.5V			0.55	V
input leakage current	I _I	V _i =5.5V or GND; V _{CC} =0V to 5.5V	-	-	2.0	uA	
supply current	I _{CC}	V _i =V _{CC} or GND; I _o =0A; V _{CC} =5.5V	-	-	40	uA	
input capacitance	C _I	-	-	-	10	pF	

AC Characteristics 1

 (T_{amb}=25°C, GND=0V, tr=tf=W3.0ns, unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
propagation delay	t _{pd}	A and B to Y; see Figure 5 ^[1]	V _{CC} =3.0V to 3.6V ^[2]				
			G=15pF	-	4.6	8.8	ns
			C _L =50PF	-	6.5	12.3	ns
			V _{CC} =4.5V to 5.5V ^[3]				
			C _L =15PF	-	3.2	5.9	ns
Power dissipation capacitance	C _{PD}	per buffer; C _L =50PF; f=1MHz; V _i = GND to V _{CC} ^[4]	-	17	-	pF	

Note:

- [1] t_{pd} is the same as t_{PLH} and t_{PHL}.
- [2] Typical values are measured at V_{CC}=3.3V.
- [3] Typical values are measured at V_{CC}=5.0V.
- [4] C_{PD} is used to determine the dynamic power dissipation (PD in uW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

 f_i=input frequency in MHz;

 f_o=output frequency in MHz;

 C_L=output load capacitance in pF;

 V_{CC}=supply voltage in Volts.

AC Characteristics 2

($T_{amb} = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $GND = 0\text{V}$, $t_r = t_f < 3.0\text{ns}$, unless otherwise specked.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
propagation delay	t_{pd}	A and B to Y; see Figure 5 ^[1]	$V_{cc} = 3.0\text{V}$ to 3.6V ^[2]				
			G=15pF	1.0	-	10.5	ns
			CL=50pF	1.0	-	14.0	ns
			$V_{cc} = 4.5\text{V}$ to 5.5V ^[3]				
			G=15pF	1.0	-	7.0	ns
			CL=50pF	1.0	-	9.0	ns

Note:

[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

[2] Typical values are measured at $V_{cc} = 3.3\text{V}$.

[3] Typical values are measured at $V_{cc} = 5\text{V}$.

AC Characteristics 3

($T_{amb} = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, $GND = 0\text{V}$, $t_r = t_f < 3.0\text{ns}$, unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
propagation delay	t_{pd}	A and B to Y; see Figure 5(1)	$V_{cc} = 3.0\text{V}$ to 3.6V ^[2]				
			G=15pF	1.0	-	12.0	ns
			CL=50pF	1.0	-	16.0	ns
			$V_{cc} = 4.5\text{V}$ to 5.5V ^[3]				
			G=15pF	1.0	-	8.0	ns
			CL=50pF	1.0	-	10.5	ns

Note:

[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

[2] Typical values are measured at $V_{cc} = 3.3\text{V}$.

[3] Typical values are measured at $V_{cc} = 5\text{V}$.

Testing Circuit

AC Testing Circuit

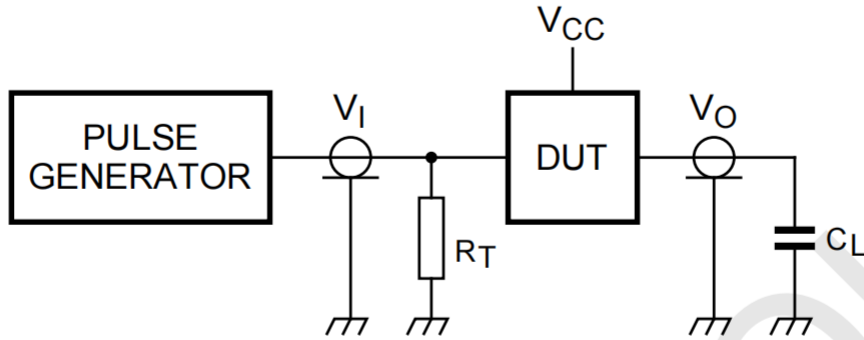


Figure 4. Test circuit for measuring switching times

Definitions for test circuit:

C_L =Load capacitance including jig and probe capacitance.

R_T =Termination resistance should be equal to output impedance Z_o of the pulse generator.

AC Testing Waveforms

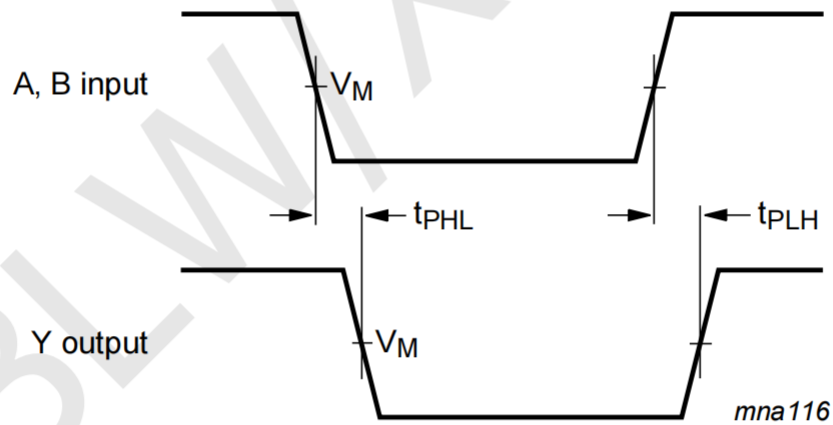


Figure 5. Input (A and B) to output (Y) propagation delays

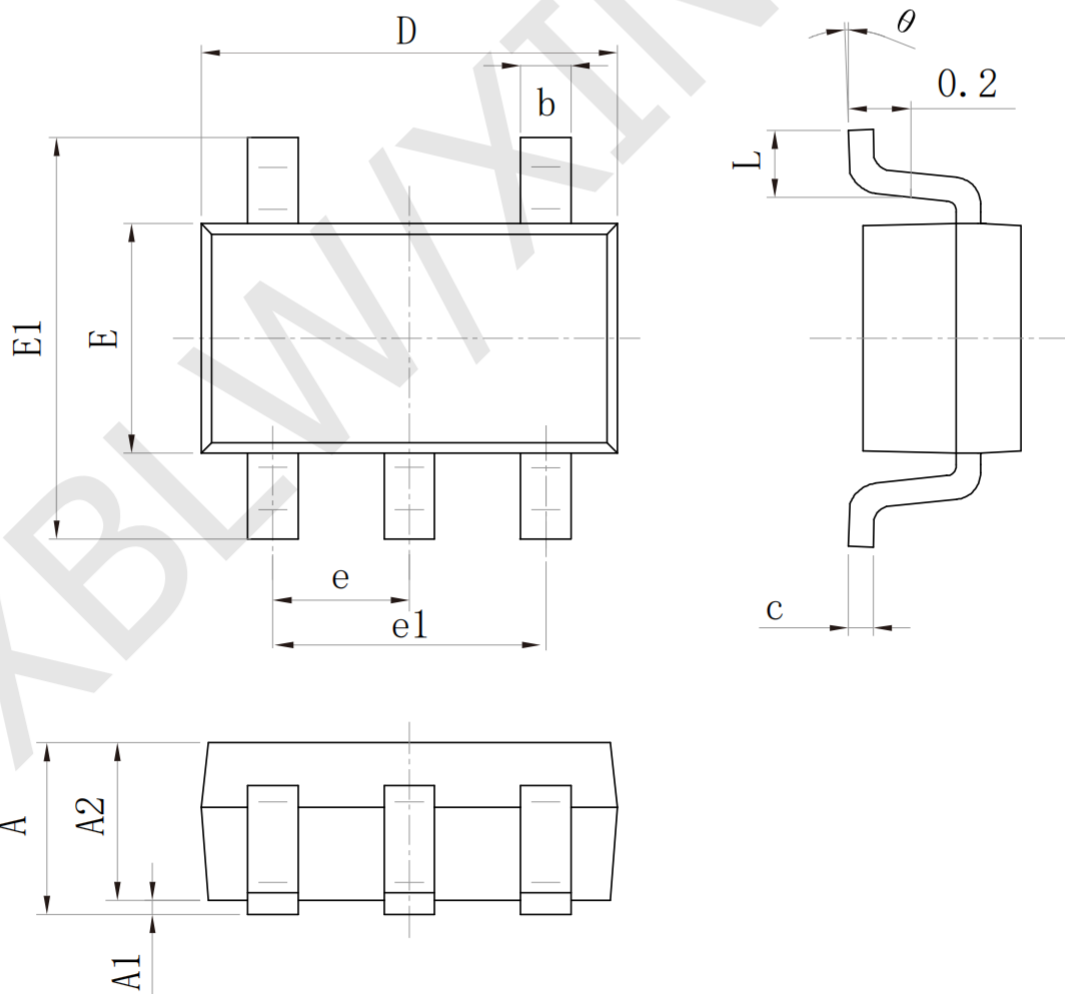
Measurement Points

Type	Input		Output
	V_I	V_M	V_M
SN74AHC1G08	GND to V_{CC}	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$

Package Information

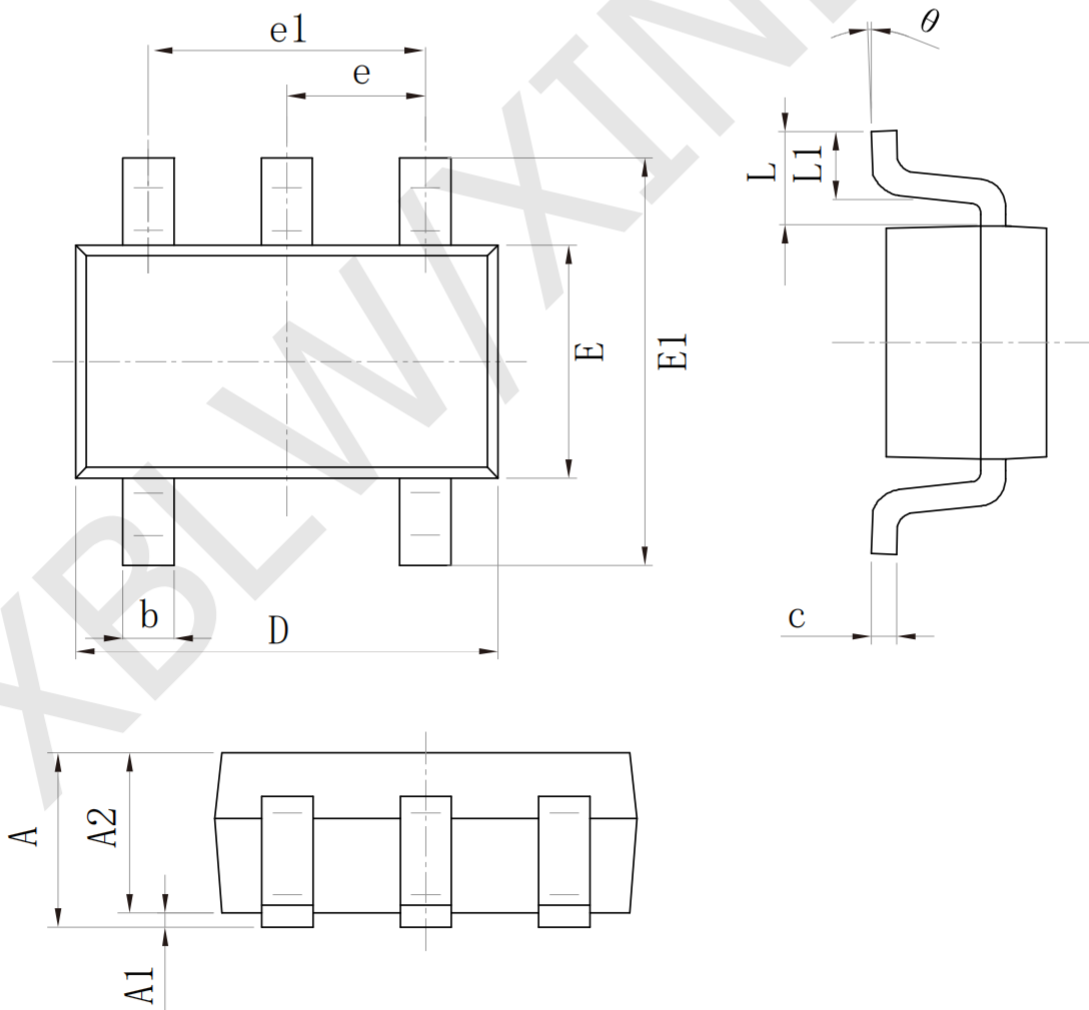
- SOT23-5

SIZE SYMBOL	Dimensions In Millimeters		SIZE SYMBOL	Dimensions In Inches	
	MIN (mm)	MAX (mm)		MIN (in)	MAX (in)
A	1.050	1.250	A	0.041	0.049
A1	0.000	0.100	A1	0.000	0.004
A2	1.050	1.150	A2	0.041	0.045
b	0.300	0.500	b	0.012	0.020
c	0.100	0.200	c	0.004	0.008
D	2.820	3.020	D	0.111	0.119
E	1.500	1.700	E	0.059	0.067
E1	2.650	2.950	E1	0.104	0.116
e	0.95 (BSC)		e	0.037 (BSC)	
e1	1.800	2.000	e1	0.071	0.079
L	0.300	0.600	L	0.012	0.024
θ	0°	8°	θ	0°	8°



• SOT-353

Size Symbol	Dimensions In Millimeters		Size Symbol	Dimensions In Inches	
	Min (mm)	Max (mm)		Min (in)	Max (in)
A	0.900	1.100	A	0.035	0.043
A1	0.000	0.100	A1	0.000	0.004
A2	0.900	1.000	A2	0.035	0.039
b	0.150	0.350	b	0.006	0.014
c	0.080	0.150	C	0.003	0.006
D	2.000	2.200	D	0.079	0.087
E	1.150	1.350	E	0.045	0.053
E1	2.150	2.450	E1	0.085	0.096
e	0.650 (TYP)		e	0.026 (TYP)	
e1	1.200	1.400	e1	0.047	0.055
L	0.525 (REF)		L	0.021 (REF)	
L1	0.260	0.460	L1	0.010	0.018
θ	0°	8°	θ	0°	8°



Statement:

- XBLW reserves the right to modify the product manual without prior notice! Before placing an order, customers need to confirm whether the obtained information is the latest version and verify the completeness of the relevant information.
- Any semi-guide product is subject to failure or malfunction under specified conditions. It is the buyer's responsibility to comply with safety standards when using XBLW products for system design and whole machine manufacturing. And take the appropriate safety measures to avoid the potential in the risk of loss of personal injury or loss of property situation!
- XBLW products have not been licensed for life support, military, and aerospace applications, and therefore XBLW is not responsible for any consequences arising from the use of this product in these areas.
- If any or all XBLW products (including technical data, services) described or contained in this document are subject to any applicable local export control laws and regulations, they may not be exported without an export license from the relevant authorities in accordance with such laws.
- The specifications of any and all XBLW products described or contained in this document specify the performance, characteristics, and functionality of said products in their standalone state, but do not guarantee the performance, characteristics, and functionality of said products installed in Customer's products or equipment. In order to verify symptoms and conditions that cannot be evaluated in a standalone device, the Customer should ultimately evaluate and test the device installed in the Customer's product device.
- XBLW documentation is only allowed to be copied without any alteration of the content and with the relevant authorization. XBLW assumes no responsibility or liability for altered documents.
- XBLW is committed to becoming the preferred semiconductor brand for customers, and XBLW will strive to provide customers with better performance and better quality products.

单击下面可查看定价，库存，交付和生命周期等信息

[>>XBLW\(芯伯乐\)](#)