

DATA SHEET

ULTRA LOW CAPACITANCE PROTECTION DEVICEUE0201

15V

RoHS compliant & Halogen free



YAGEO Phicomp

Downloaded From Oneyac.com



0201

SCOPE

This specification describes UE0201 has a typical capacitance of only 0.05pF (I/O to GND), and it can be used to meet the ESD immunity requirement of IEC61000-4-2 (15KV air, 8KV contact discharge).

YAGEO Phicomp

<u>APPLICATIONS</u>

- All general purpose applications
- High Definition Multi-Media Interface(HDMI)
- Digital Visual Interface (DVI)
- Display Port Interface (DP)
- Unified Display Interface (UDI)
- Mobile Display Digital Interface (MDDI)
- Gigabit Ethernet
- USB2.0 and USB3.0
- IEEE1394 interface

FEATURES

- Halogen Free Epoxy
- ESD protection for high speed data lines to
- IEC61000-4-2
- ESD contact discharge typical 8KV, max 15KV
- ESD air discharge typical 15KV, max 25KV
- Surface mount
- Extremely low capacitance
- Very low leakage current
- Fast response time
- Bi-directional ESD protection
- · Lead free solder termination
- The best ESD protection for high frequency,
- low voltage applications

ORDERING INFORMATION - GLOBAL PART NUMBER

Part number is identified by the series name, size, clamping voltage, trigger voltage, packaging type, capacitance, taping reel, working voltage.

GLOBAL PART NUMBER (PREFERRED)

UE0201 X X X - XX XX X

(1) (2) (3) (4) (5) (6) (7)

(I) CLAMPING VOLTAGE

F = 40V

(2) TRIGGER VOLTAGE

C = 350V

(3) PACKAGING TYPE

R = Paper taping reel

(4) CAPACITANCE

- = Base on spec

(5) TAPING REEL

07 = 7 inch dia Reel

7D = 7 inch reel with double quantity

(6) WORKING VOLTAGE

15V = 15V

(7) DEFAUL CODE

Letter L is system default code for ordering only

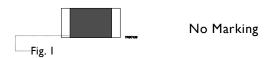
ORDERING EXAMPLE

The ordering code for a UE0201 clamping voltage 40V, trigger voltage 350V ESD suppressor working voltage 15V, supplied in 7-inch tape reel with IOKpcs quantify is: UE0201FCR-0715VL.



MARKING

UE0201



CONSTRUCTION

The ESD suppressors are constructed out of a highgrade ceramic body. Internal metal electrode is cut a kerf into two parts which are connected by ESD absorbent paste. The ESD absorbent layer is covered with protective coat. Finally, the two external terminations (matte tin) are added. See Fig. 2.

DIMENSIONS

Table I

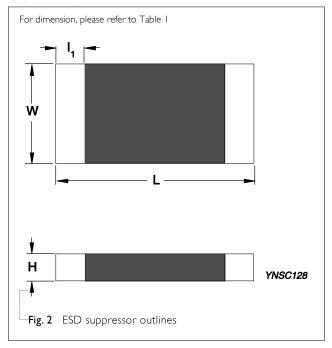
TYPE L (mm) W (mm) H (mm) I₁ (mm) **UE0201** 0.60 ±0.03 0.30 ±0.03 0.24 ±0.03 0.15 ±0.07

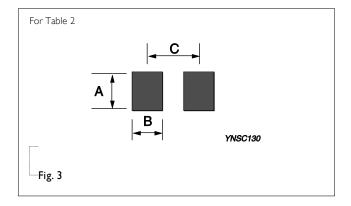
RECOMMENDED FOOTPRINT DIMENSIONS

Table 2

SIZE FOOTPRINT	Dimensions	code (unit: mm)	
UE0201	Α	В	С
OE0201	0.40	0.35	0.65

OUTLINES







Chip	Resistor	Surface	Mount

UE

SERIES 0201

ABSOLUTE MAXIMUM RATINGS

Table 3

PARAMETER	VALUE
Maximum Contact discharge voltage Per IEC61000-4-2	ISKV
Maximum Air discharge voltage Per IEC61000-4-2	25KV
Maximum Operating temperature(TOPER)	-55°C to +125°C
Maximum temperature for soldering during I0s (TL)	260°C

ELECTRICAL CHARACTERISTICS (TA=25°C)

Table 4

CHARACTERISTICS	UE0201
Rated Voltage (V _R)	I5V (Max)
Trigger Voltage (V₁)	350V (Typical)
Clamping Voltage (Vc)	40V (Typical)
Leakage current (IL)	I0nA (Typical)
Capacitance (C _P)	0.05pF(typical)

NOTE:

- 1. Trigger and clamping voltage are measured per IEC 61000-4-2, 8KV contact discharge method.
- 2. After reliability tests such as high temp storage, temp cycles, continuous ESD strike etc, the maximum leakage current is less than 10uA.
- 3. Capacitance is measured at VR=0V, f=IMHZ

PACKING STYLE AND PACKAGING QUANTITY

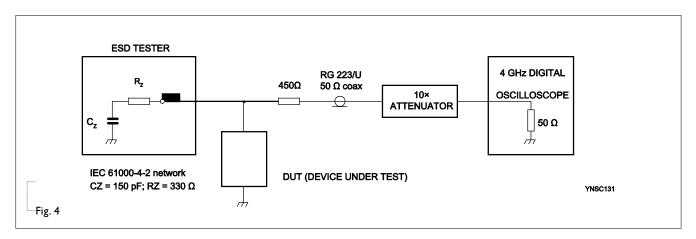
 Table 5
 Packing style and packaging quantity

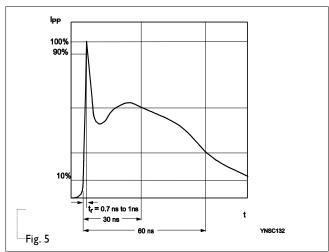
PACKING STYLE	REEL DIMENSION	UE0201	
Paper taping reel (R)	7" (178 mm)	10,000	

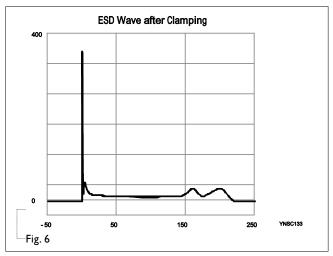


5 8

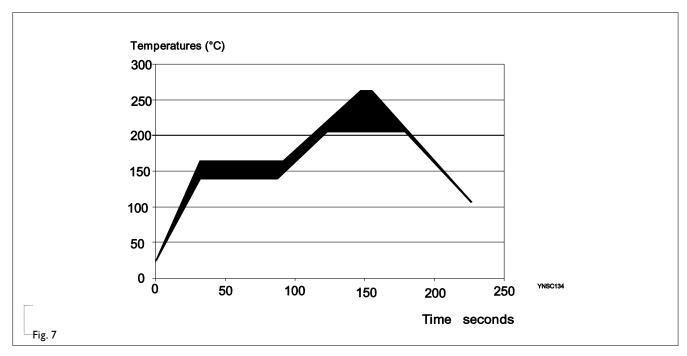
ESD CLAMPING TEST





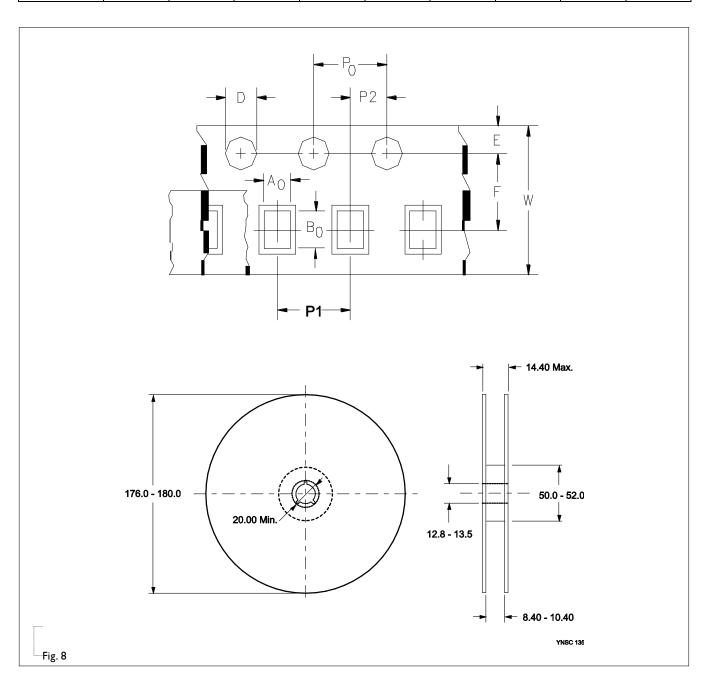


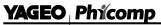
SOLDER REFLOW RECOMMENDATIONS



PACKAGE INFORMATION

Dimension	A0	В0	D	P0	PI	P2	Е	F	W
Typical (mm)	0.35	0.65	1.50	4.00	2.00	2.00	1.75	3.50	8.00



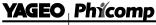


Chip Resistor Surface Mount UE SERIES 0201

TESTS AND REQUIREMENTS

Table 6 Test condition, procedure and requirements

TEST	REFERENCE STANDARD	TEST CONDITION	SPECIFICATION	
Operating temperature		-55 °C to 125°C	IL=I0µ A	
Bending	IEC 60068-2-21	3 mm deflection		
Resistance of solder heat	IEC 60068-2-58	$260 \pm 5^{\circ}$ C for 10 ± 1 sec		
Thermal shock	MIL-STD-202G Method-107G	-55°C to 125°C, 5 cycles		
Solderability	IPC/JEDEC J-STD-002B test B	245 \pm 5°C solder, 2 \pm 0.5 sec	95% coverage	
		dwell.		
		Solder: Sn96.5/Ag3.0/Cu0.5		
Capacitance		VR=0V, f=IMHZ	0.05pF (typical)	



Chip Resistor Surface Mount

UE

SERIES

0201

8

Product specification

REVISION HISTORY

REVISION DATE CHANGE NOTIFICATION DESCRIPTION

Version 0 Nov. 28, 2014 - - First issue of this specification

[&]quot;Yageo reserves all the rights for revising the content of this datasheet without further notification, as long as the products itself are unchanged. Any product change will be announced by PCN."



单击下面可查看定价,库存,交付和生命周期等信息

>>Yageo(国巨)