

DATA SHEET

THIN FILM CHIP RESISTORS
AUTOMOTIVE GRADE

NT series
0.1% TO 1%, TC25 TO TC50
sizes 0402/0603/0805/1206
RoHS compliant



YAGEO







Chip Resistor Surface Mount | NT |

NT SERIES

0402 to 1206

SCOPE

This specification describes NT0402 to NT1206 high precision-high stability chip resistors made by thin film process.

APPLICATIONS

- Automotive electronics
- Industrial and medical equipment
- Test and measuring equipment
- Telecommunications

FEATURES

- · AEC-Q200 qualified
- Pb free without RoHS exemption
- Halogen free epoxy
- Superior resistance against sulfur containing surroundings
- Moisture sensitivity level: MSL I
- Environmental hazards Reduction
- Non-forbidden materials used in products / production

ORDERING INFORMATION - GLOBAL PART NUMBER

Part number is identified by the series name, size, tolerance, packaging type, temperature coefficient, taping reel and resistance value.

GLOBAL PART NUMBER

NT XXXX X X X XX XXXXX L

(1) (2) (3) (4) (5) (6)

(I) SIZE

0402 / 0603 / 0805 / 1206

(2) TOLERANCE

 $B = \pm 0.1\%$

 $C = \pm 0.25\%$

 $D = \pm 0.5\%$

 $F = \pm 1\%$

(3) PACKAGING TYPE

R = Paper taping reel

(4) TEMPERATURE COEFFICIENT OF RESISTANCE

 $D = \pm 25 \text{ ppm/°C}$

 $E = \pm 50 \text{ ppm/}^{\circ}\text{C}$

(5) TAPING REEL

07 = 7 inch dia, Reel

(6) RESISTANCE VALUE

There are 2~4 digits indicated the resistor value.

Letter R/K/M is decimal point

Example: $100R = 100\Omega$

 $IK = 1,000\Omega$

(7) DEFAULT CODE

Letter L is the system default code for ordering only. (NOTE)

ORDERING EXAMPLE

The ordering code of a NT0402 chip resistor, TCR 50 value 100Ω with $\pm 0.5\%$ tolerance, supplied in 7-inch tape reel is: NT0402DRE07100RL.

NOTE

- I. All our Rchip products meet RoHS compliant and Halogen Free. "LFP" of the internal 2D reel label mentions "Lead Free Process".
- 2. On customized label, "LFP" or specific symbol can be printed.





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NT0402



No marking

NT0603



E-96 series: including values 10/11/13/15/20/75 of E-24 series, 3 digits



E-24 series: exception values 10/11/13/15/20/75 of E-24 series, one short bar under marking letter

NT0805 / NT1206



Fig. 4 Value = $10 \text{ K}\Omega$

Both E-24 and E-96 series: 4 digits

First three digits for significant figure and 4th digit for number of zeros

NOTE

For further marking information, please see special data sheet "Chip resistors marking".

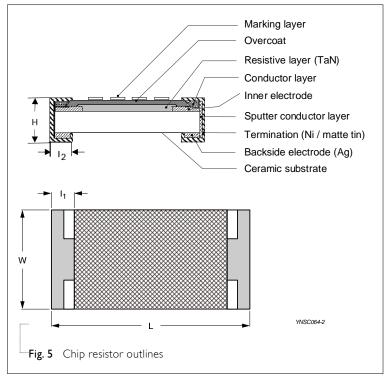
CONSTRUCTION

The resistors are constructed out of a high grade ceramic body. Internal metal electrodes are added at each end connected by a resistive layer.

This resistive layer is trimmed to its nominal value and on both ends a contact is made which will guarantee optimum solderability. This is achieved by applying several layers and for ease of soldering the outer layer consists of Ni/matte tin.

Adding a special protective coating, on this series to enhance moisture resistance of the environment.

OUTLINES







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DIMENSIONS

Table I

TYPE	L (mm)	W (mm)	H (mm)	I₁ (mm)	l ₂ (mm)
NT0402	1.00 ±0.10	0.50 ±0.05	0.30 ±0.05	0.20 ±0.10	0.25 ±0.10
NT0603	1.60 ±0.10	0.80 ±0.10	0.45 ±0.10	0.25 ±0.15	0.25 ±0.15
NT0805	2.00 ±0.10	1,25 ±0.10	0.50 ±0.10	0.35 ±0.20	0.35 ±0.20
NT1206	3.10 ±0.10	1.60 ±0.10	0.55 ±0.10	0.45 ±0.20	0.40 ±0.20

ELECTRICAL CHARACTERISTICS

Table 2

			Max.	Max.	Resistance Range (E-24/E-96 series)(Ω) & Tolerance(1)				
TYPE	Operating Temperature Range	Power Rating	Working Voltage	Overload Voltage	T.C.R. (ppm/°C) ⁽²⁾	±0.1% (B)	±0.25% (C)	±0.5% (D)	±1% (F)
NT0402		1/20W	75 V	150 V -	±50 (E)		100 ≤ R ≤ 63K4		
					±25 (D)				
NT0603 ————————————————————————————————————		3/20W	75V	150 V -	±50 (E)		100 < R	≤189K	
					±25 (D)	100 21/2 10/10			
	55 °C to +155 °C -	1/5\4/	±50 (E) 150 V 300 V ±25 (D)	200.1/	±50 (E)		100 ≤ R ≤ 370K		
		1/5W		100 2 10)0 2 N 2 3/0N				
NT1206		2/5\4/	200.1/	400 V -	±50 (E)		100 ≤ R ≤ 481K		
		2/5W	200 V		±25 (D)				

NOTE: I. Global part number (code 7) 2. Global part number (code 9)

FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet "Chip resistors mounting".

PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

PRODUCT TYPE	PATKING STYLE	REEL DIMENSION	QUANTITY PER REEL
NT0402	Paper taping reel	7" (178 mm)	10,000 Units
NT0603	Paper taping reel	7" (178 mm)	5,000 Units
NT0805	Paper taping reel	7" (178 mm)	5,000 Units
NT1206	Paper taping reel	7" (178 mm)	5,000 Units

NOTE: for paper tape and reel specification/dimensions, please see the special data sheet "packing" document.

FUNCTIONAL DESCRIPTION

OPERATING TEMPERATURE RANGE

Range: -55 °C to +155 °C

POWER RATING

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Each type rated power at 70 °C: NT0402=1/20W NT0603=3/20W NT0805=1/5W NT1206=2/5W

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

$$V = \sqrt{(P \times R)}$$

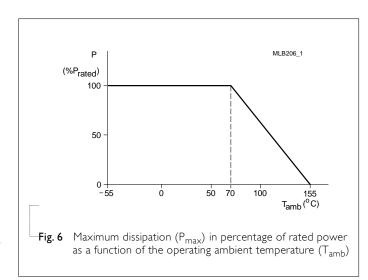
Or max. working voltage whichever is less

Where

V=Continuous rated DC or AC (rms) working voltage (v)

P=Rated power

R=Resistance value (Ω)





TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

EST	TEST METHOD	PROCEDURE	REQUIREMENTS
Short Time	IEC60115-1 4.13	2.5 times of rated voltage or maximum	±(0.05%+0.05Ω)
Overload		overload voltage, the less of the above, for 5 sec at room temperature	
High	AEC-Q200 Test 3	1,000 hours at Tamb = 155 °C, unpowered	±(0.3%+0.05Ω)
Temperature Exposure	MIL-STD-202 Method 108		
Moisture	AEC-Q200 Test 6	Each temperature / humidity cycle is defined at	±(0.1%+0.05Ω)
Resistance	MIL-STD-202 Method 106	8 hours (method 106F), 3 cycles / 24 hours for	
		10d. with 25 $^{\circ}$ C / 65 $^{\circ}$ C 95% R.H, without steps	
		7a & 7b, unpowered	
		Parts mounted on test-boards, without condensation on parts	
Biased	AEC-Q200 Test 7	1,000 hours; 85 °C / 85% RH	±(0.1%+0.05Ω)
Humidity	MIL-STD-202 Method 103	10% of operating power	
		Measurement at 24±4 hours after test conclusion	
Life	AEC-Q200 Test 8 MIL-STD-202 Method 108	1,000 hours at 70 ± 5 °C, RCWV applied for 1.5 hours on, 0.5 hour off, still air required	±(0.1%+0.05Ω)
Resistance to	AEC-Q200 Test 15	Condition B, no pre-heat of samples	±(0.05%+0.05Ω)
Soldering	MIL-STD-202 Method 210	Lead-free solder, 260 ± 5 °C, 10 ± 1 seconds	±(0.0370 · 0.0332)
Heat	THE OTB EGET TOUTOUT ET	immersion time	
		Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	
 Thermal	AEC-Q200 Test 16	-55/+125 °C	±(0.1%+0.05Ω)
Shock	MIL-STD-202 Method 107	Number of cycles is 300. Devices mounted	No visible damage
	THE STD 2021 redied 107	Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	No visione durinage
Solderability	AEC-Q200 Test 18	Electrical Test not required Magnification 50X	Well tinned
- Wetting	J-STD-002	SMD conditions:	(>95% covered)
	,	(a) Method B, aging 4 hours at 155 °C dry heat,	No visible damage
		dipping at 235±3 °C for 5±0.5 seconds. (b) Method B, steam aging 8 hours, dipping at	
		(b) Method B, steam aging 8 hours, dipping at 215±3 °C for 5±0.5 seconds.	
		(c) Method D, steam aging 8 hours, dipping at 260±3 °C for 7±0.5 seconds	



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TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Board Flex / Bending	AEC-Q200 Test 21 AEC-Q200-005	Chips mounted on a 90mm glass epoxy resin PCB (FR4) Bending for 0402: 5 mm 0603/0805: 3 mm 1206: 2mm Holding time: minimum 60 second	±(0.1%+0.05Ω)
Temperature Coefficient of Resistance (T.C.R.)	IEC 60115-1 4.8	At +25/–55 °C and +25/+125°C Formula: $T.C.R = \frac{R2 - R1}{R1(\mathbf{t2} - \mathbf{tl})} \times 10^{6}(\text{ppm/°C})$ Where $t1 = +25 \text{ °C or specified room temperature}$ $t2 = -55 \text{ °C or } +125 \text{ °C test temperature}$ $R1 = \text{resistance at reference temperature in ohms}$ $R2 = \text{resistance at test temperature in ohms}$	Refer to table 2
Flower of Sulfur	ASTM-B-809-95* * Modified	Sulfur 750 hours, 105°C, unpowered.	±(2.0%+0.05Ω)



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REVISION HISTORY

REVISION DATE CHANGE NOTIFICATION DESCRIPTION

Version 0 Oct. 31, 2023 - - First issue of this specification



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