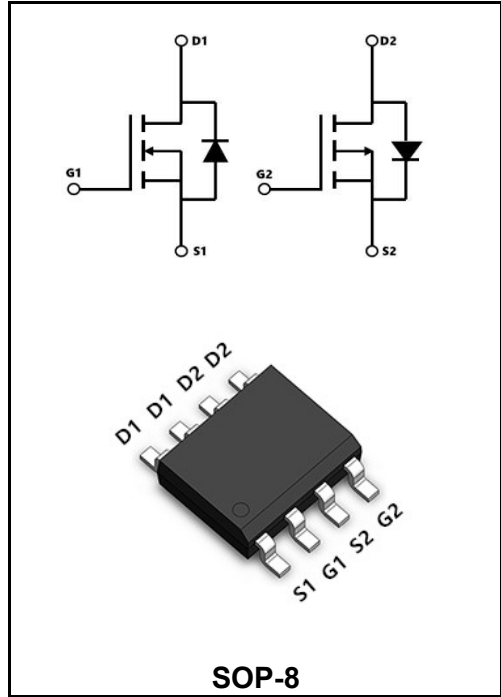


**30V N+P-CHANNEL ENHANCEMENT MODE MOSFET**

**MAIN CHARACTERISTICS**

$I_D$	10A
$V_{DSS}$	30V
$R_{DS(on)-typ}(@V_{GS}=10V)$	< 25mΩ (Type:20 mΩ)
$I_D$	-7.6A
$V_{DSS}$	-30V
$R_{DS(on)-typ}(@V_{GS}=-10V)$	< -48mΩ (Type:39 mΩ)



**Application**

- ◆ Battery protection
- ◆ Load switch
- ◆ Uninterruptible power supply

**Product Specification Classification**

Part Number	Package	Marking	Pack
YFW6G03S	SOP-8	YFW 6G03S XXXX	3000PCS/Tape

**Maximum Ratings at Tc=25°C unless otherwise specified**

Characteristics	Symbols	Value		Units
		N-Ch	P-Ch	
Drain-Source Voltage	<b>V<sub>DS</sub></b>	30	-30	<b>V</b>
Gate - Source Voltage	<b>V<sub>GS</sub></b>	±20	±20	<b>V</b>
Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup> @T <sub>C</sub> =25°C	<b>I<sub>D</sub></b>	10	-7.6	<b>A</b>
Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup> @T <sub>C</sub> =100°C	<b>I<sub>D</sub></b>	6	-5.9	<b>A</b>
Pulsed Drain Current <sup>2</sup>	<b>I<sub>DM</sub></b>	20	-15	<b>A</b>
Single Pulse Avalanche Energy <sup>3</sup>	<b>E<sub>AS</sub></b>	22	45	<b>mJ</b>
Avalanche Current	<b>I<sub>AS</sub></b>	21	-30	<b>A</b>
Total Power Dissipation <sup>4</sup> @T <sub>C</sub> =25°C	<b>P<sub>D</sub></b>	2.0	2.0	<b>W</b>
Storage Temperature Range	<b>T<sub>STG</sub></b>	-55 to +150	-55 to +150	<b>°C</b>
Operating Junction Temperature Range	<b>T<sub>J</sub></b>	-55 to +150	-55 to +150	<b>°C</b>
Thermal Resistance Junction-Ambient <sup>1</sup>	<b>R<sub>θJA</sub></b>	-	62	<b>°C/W</b>
Thermal Resistance Junction-Case <sup>1</sup>	<b>R<sub>θJC</sub></b>	-	5	<b>°C/W</b>

**N-Channel Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	<b>BV<sub>DSS</sub></b>	30	-	-	<b>V</b>
BVDSS Temperature Coefficient	Reference to 25 °C , I <sub>D</sub> =1mA	<b>ΔBV<sub>DSS</sub>/ΔT<sub>J</sub></b>	-	0.023	-	<b>V/°C</b>
Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =10A	<b>R<sub>DS(ON)</sub></b>	-	20	25	<b>mΩ</b>
	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A		-	30	38	<b>mΩ</b>
Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	<b>V<sub>GS(th)</sub></b>	1.0	1.7	2.5	<b>V</b>
V <sub>GS(th)</sub> Temperature Coefficient		<b>ΔV<sub>GS(th)</sub></b>	-	-5.2	-	<b>mV/°C</b>
Drain-Source Leakage Current	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V T <sub>J</sub> =25°C	<b>I<sub>DSS</sub></b>	-	-	1	<b>uA</b>
	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C		-	-	5	
Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	<b>I<sub>GSS</sub></b>	-	-	±100	<b>nA</b>
Forward Transconductance	V <sub>DS</sub> = 5V, I <sub>D</sub> = 10A	<b>g<sub>fs</sub></b>	-	16	-	<b>S</b>
Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	<b>R<sub>g</sub></b>	-	2.5	5	<b>Ω</b>
Total Gate Charge(4.5V)	V <sub>DS</sub> =20V V <sub>GS</sub> =4.5V I <sub>D</sub> =10A	<b>Q<sub>g</sub></b>	-	7.2	-	<b>nC</b>
Gate-Source Charge		<b>Q<sub>gs</sub></b>	-	1.4	-	
Gate-Drain Charge		<b>Q<sub>gd</sub></b>	-	2.2	-	
Turn-on delay time	V <sub>DD</sub> = 15V V <sub>GS</sub> = 10V R <sub>G</sub> = 3.3 I <sub>D</sub> = 5A	<b>t<sub>d(on)</sub></b>	-	4.1	-	<b>ns</b>
Rise Time		<b>T<sub>r</sub></b>	-	9.8	-	
Turn-Off Delay Time		<b>t<sub>d(OFF)</sub></b>	-	15.5	-	
Fall Time		<b>t<sub>f</sub></b>	-	6.0	-	
Input Capacitance	V <sub>DS</sub> =15V V <sub>GS</sub> =0V f=1MHz	<b>C<sub>iss</sub></b>	-	572	-	<b>pF</b>
Output Capacitance		<b>C<sub>oss</sub></b>	-	81	-	
Reverse Transfer Capacitance		<b>C<sub>rss</sub></b>	-	65	-	
Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	<b>I<sub>S</sub></b>	-	-	10	<b>A</b>
Pulsed Source Current <sup>2,5</sup>		<b>I<sub>SM</sub></b>	-	-	20	<b>A</b>
Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	<b>V<sub>SD</sub></b>	-	-	1.2	<b>V</b>

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V,V<sub>GS</sub>=10V,L=0.1mH,I<sub>AS</sub>=21A
- 4 .The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

**P-Channel Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

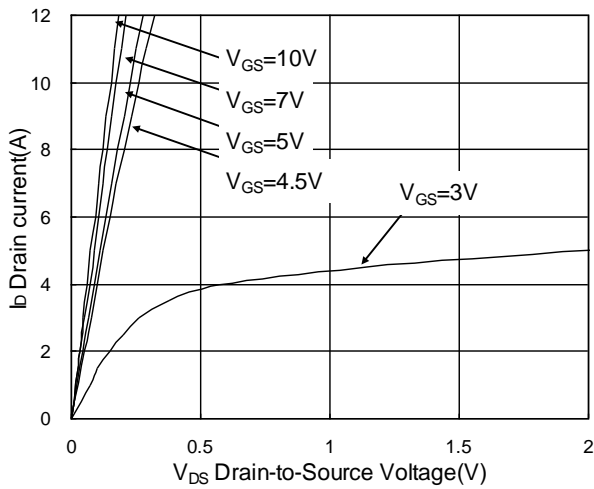
Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	<b>BV<sub>DSS</sub></b>	-30	-	-	<b>V</b>
BVDSS Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =-1mA	<b>ΔBV<sub>DSS</sub>/ΔT<sub>J</sub></b>	-	-0.021	-	<b>V/°C</b>
Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-7A	<b>R<sub>DS(ON)</sub></b>	-	39	48	<b>mΩ</b>
	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A		-	53	58	<b>mΩ</b>
Gate -Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	<b>V<sub>GS(th)</sub></b>	-1.0	-1.6	-2.5	<b>V</b>
V <sub>GS(th)</sub> Temperature Coefficient		<b>ΔV<sub>GS(th)</sub></b>	-	-4.2	-	<b>mV/°C</b>
Drain-Source Leakage Current	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	<b>I<sub>DSS</sub></b>	-	-	-1	<b>uA</b>
	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C		-	-	-5	
Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	<b>I<sub>GSS</sub></b>	-	-	±100	<b>nA</b>
Forward Transconductance	V <sub>DS</sub> = -5V, I <sub>D</sub> = -7A	<b>g<sub>fs</sub></b>	-	15	-	<b>S</b>
Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	<b>R<sub>g</sub></b>	-	15	30	<b>Ω</b>
Total Gate Charge(-4.5V)	V <sub>DS</sub> =-20V V <sub>GS</sub> =-4.5V I <sub>D</sub> =-7A	<b>Q<sub>g</sub></b>	-	9.8	-	<b>nC</b>
Gate-Source Charge		<b>Q<sub>gs</sub></b>	-	2.2	-	
Gate-Drain Charge		<b>Q<sub>gd</sub></b>	-	3.4	-	
Turn-on delay time	V <sub>DD</sub> =-15V V <sub>GS</sub> =-10V R <sub>G</sub> = 3.3 I <sub>D</sub> =-5A	<b>t<sub>d(on)</sub></b>	-	16.4	-	<b>ns</b>
Rise Time		<b>T<sub>r</sub></b>	-	20.2	-	
Turn-Off Delay Time		<b>t<sub>d(OFF)</sub></b>	-	55	-	
Fall Time		<b>t<sub>f</sub></b>	-	10	-	
Input Capacitance	V <sub>DS</sub> =-15V V <sub>GS</sub> =0V f=1MHz	<b>C<sub>iss</sub></b>	-	930	-	<b>pF</b>
Output Capacitance		<b>C<sub>oss</sub></b>	-	148	-	
Reverse Transfer Capacitance		<b>C<sub>rss</sub></b>	-	115	-	
Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	<b>I<sub>S</sub></b>	-	-	-7.6	<b>A</b>
Pulsed Source Current <sup>2,5</sup>		<b>I<sub>SM</sub></b>	-	-	-15	<b>A</b>
Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C	<b>V<sub>SD</sub></b>	-	-	-1.2	<b>V</b>

Note :

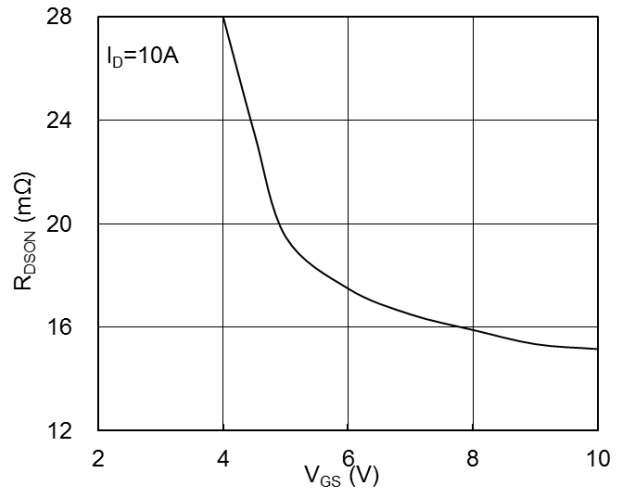
- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZcopper.
- 2.The data tested by pulsed , pulse width ≅ 300us , duty cycle ≅ 2%
- 3.The EAS data sh.The power dissipation is limited by ows Max. rating
4. The test condition is 150°C junction temperature V<sub>DD</sub>=-25V,V<sub>GS</sub>=-10V,L=0.1mH,I<sub>AS</sub>=-30A
- 5 .The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

**Ratings and Characteristic Curves**

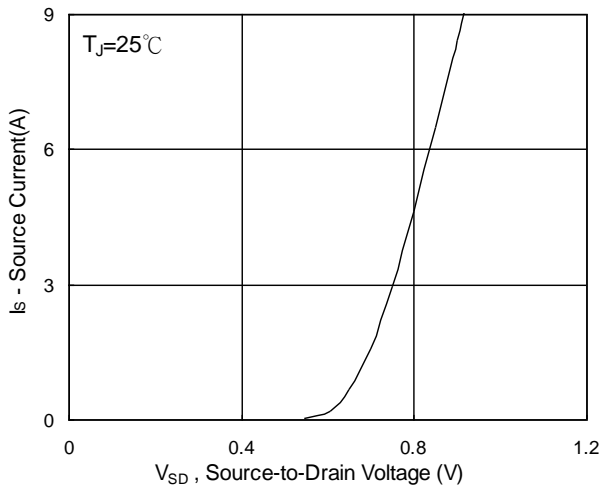
**N-Channel Typical Characteristics**



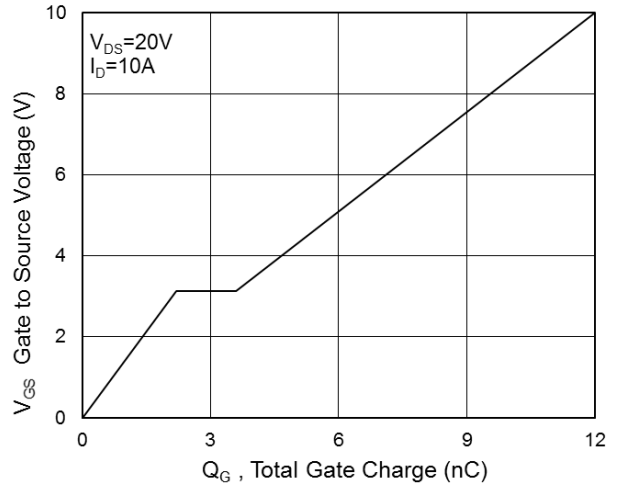
**Fig.1 Typical Output Characteristics**



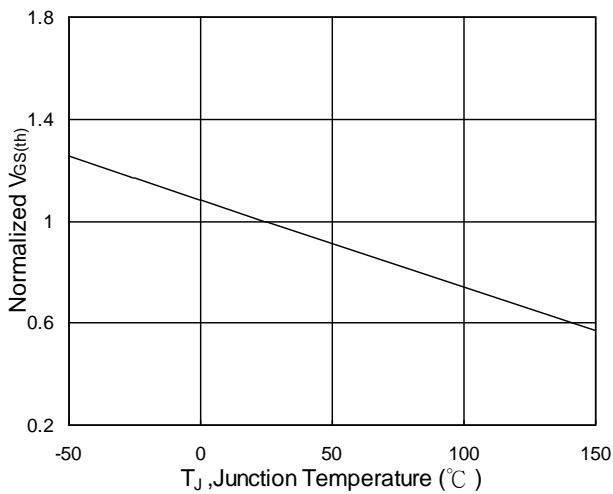
**Fig.2 On-Resistance vs Gate-Source Voltage**



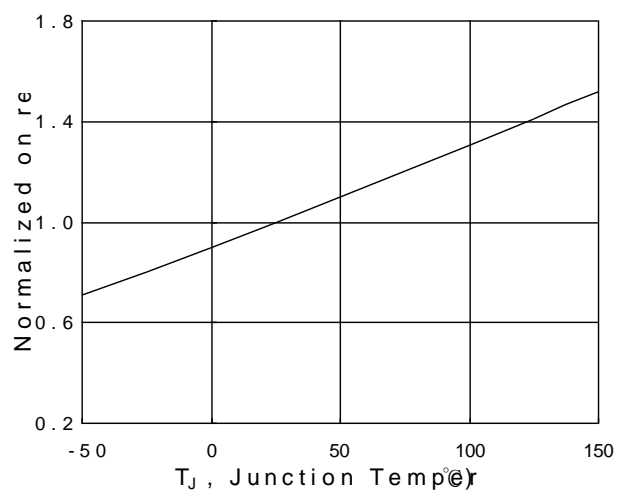
**Fig.3 Forward Characteristics of Reverse**



**Fig.4 Gate-Charge characteristics**

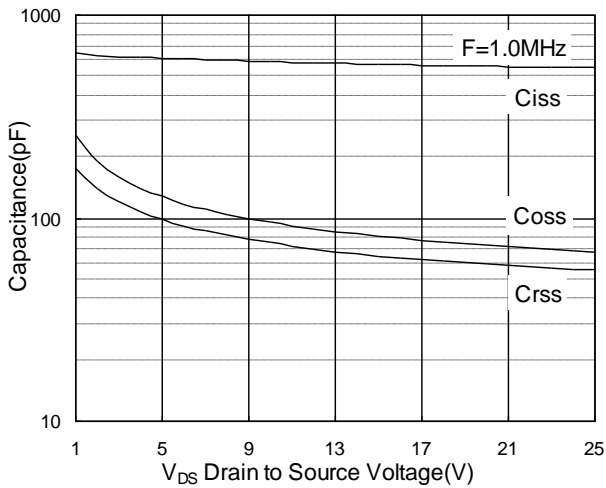


**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$**

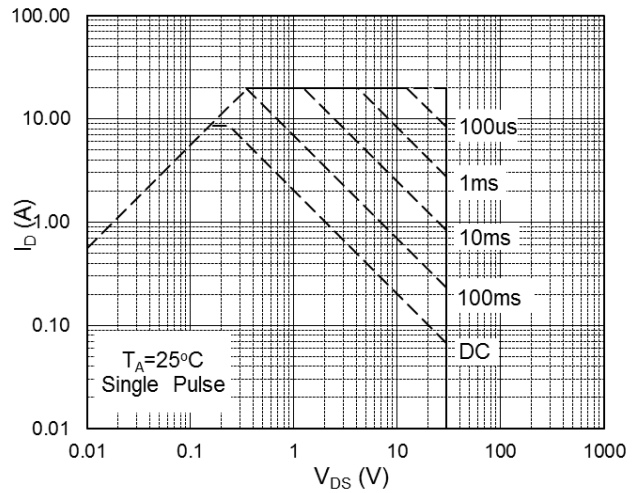


**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**

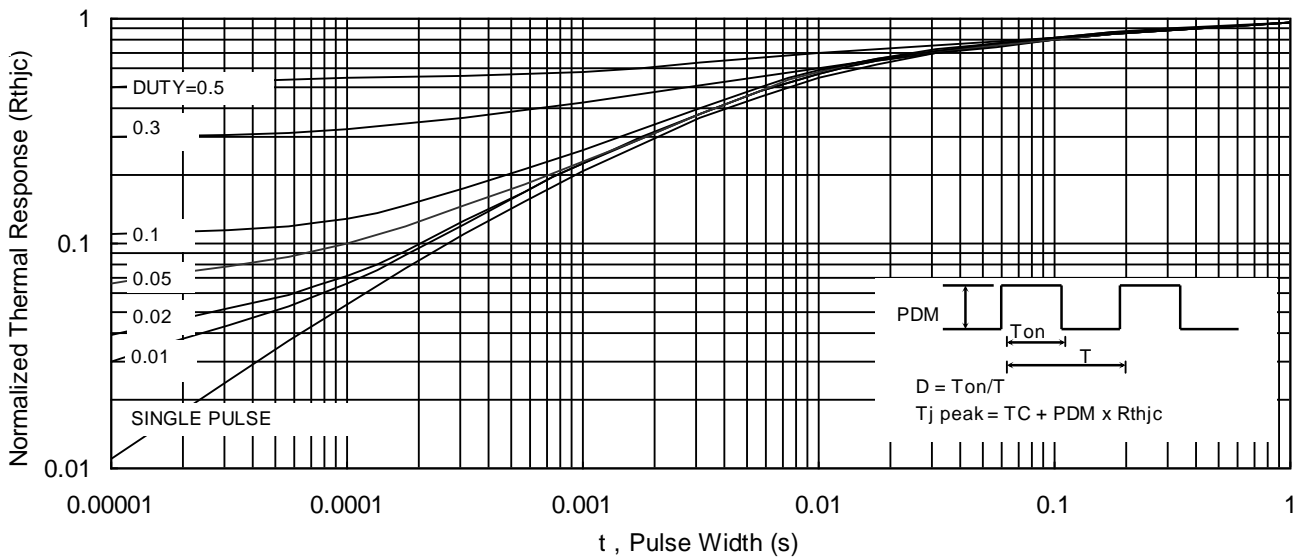
**Ratings and Characteristic Curves**



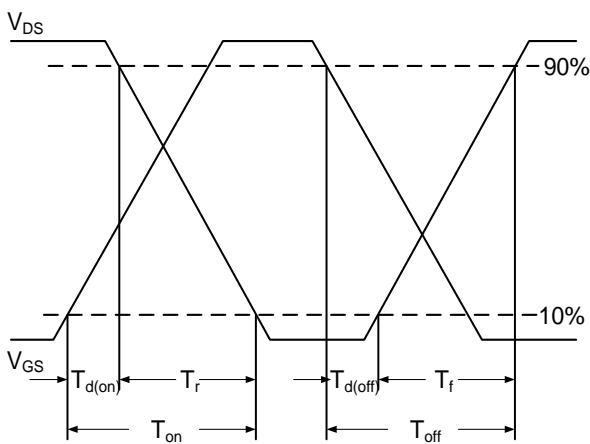
**Fig.7 Capacitance**



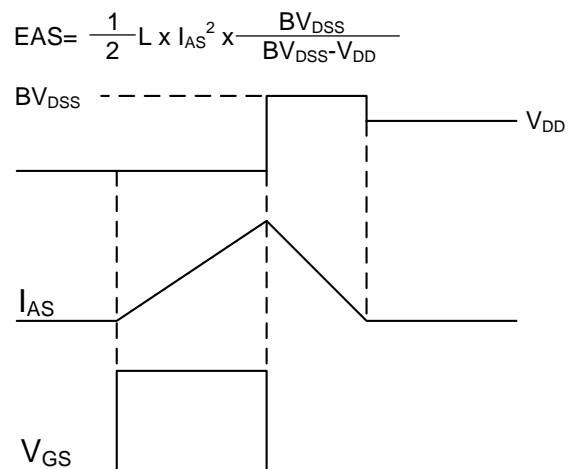
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



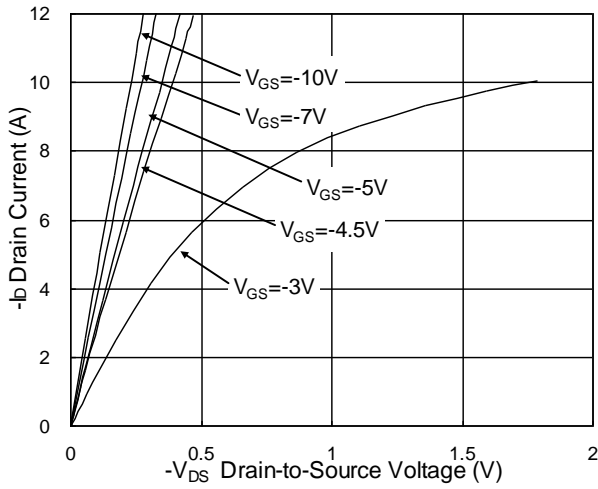
**Fig.10 Switching Time Waveform**



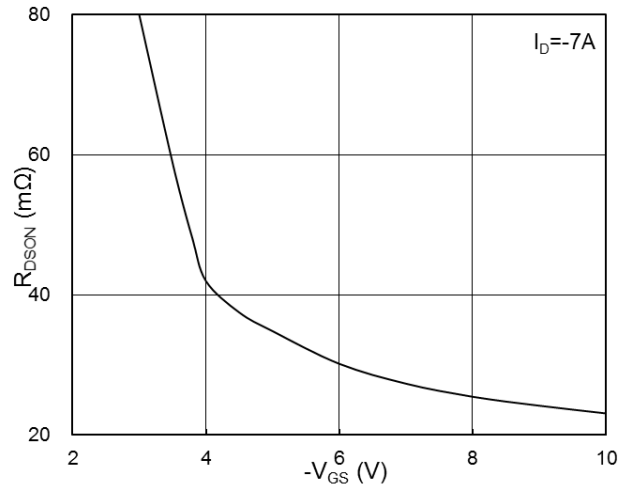
**Fig.11 Unclamped Inductive Waveform**

**Ratings and Characteristic Curves**

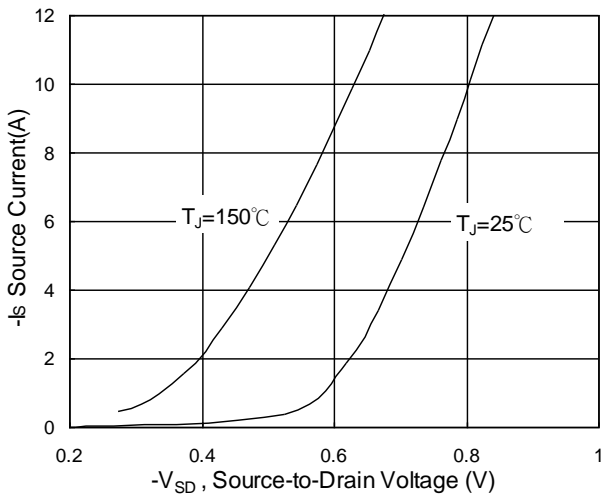
**P-Channel Typical Characteristics**



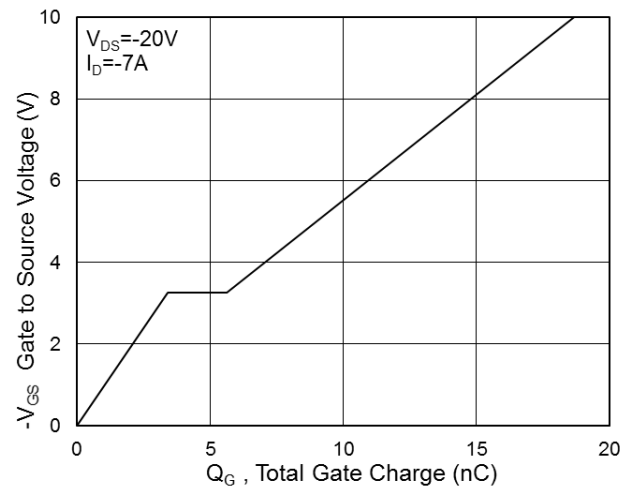
**Fig.1 Typical Output Characteristics**



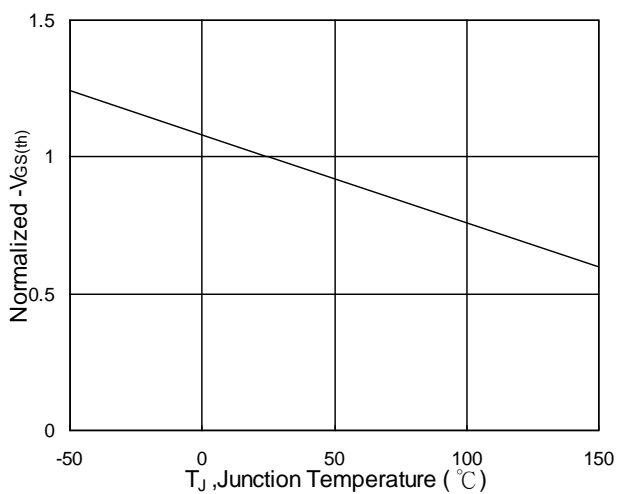
**Fig.2 On-Resistance vs Gate-Source Voltage**



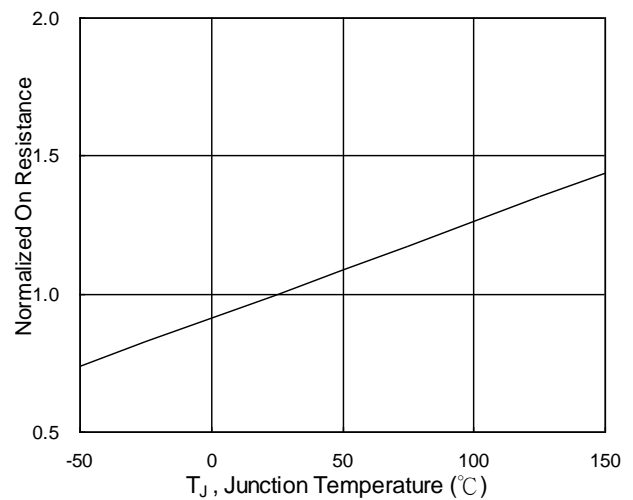
**Fig.3 Forward Characteristics of Reverse**



**Fig.4 Gate-Charge Characteristics**

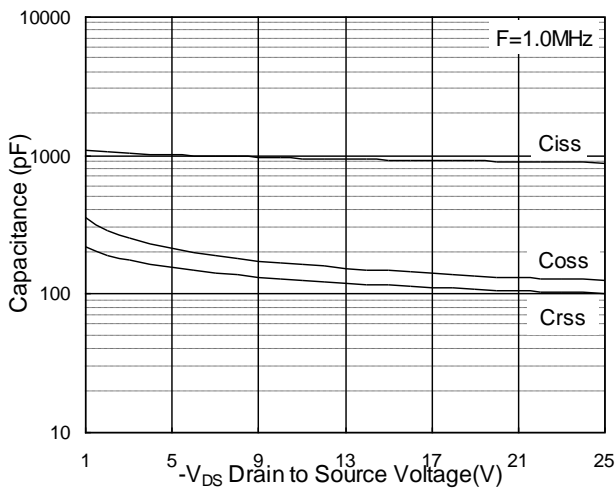


**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$**

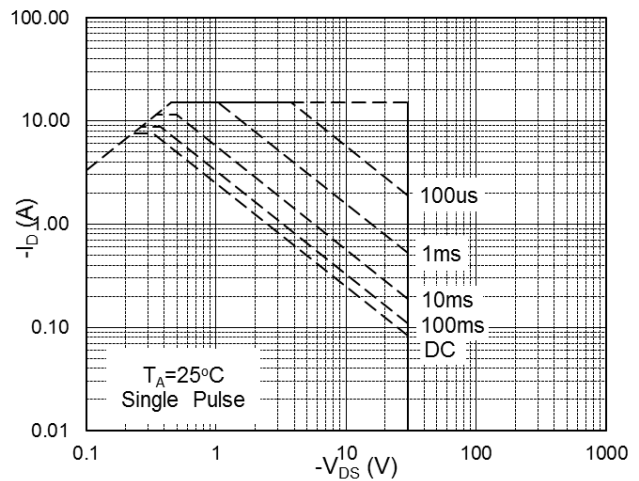


**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**

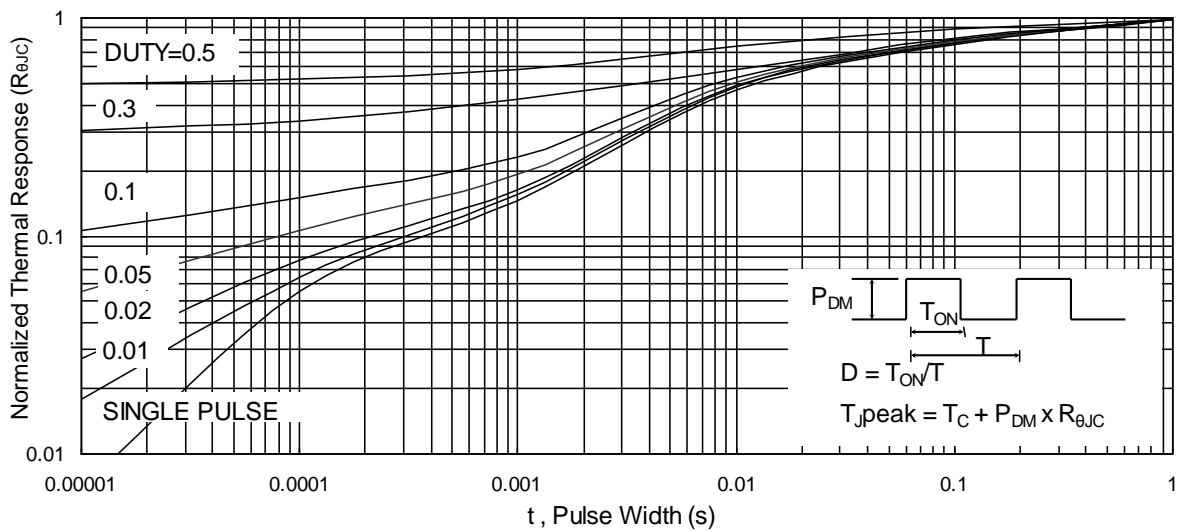
**Ratings and Characteristic Curves**



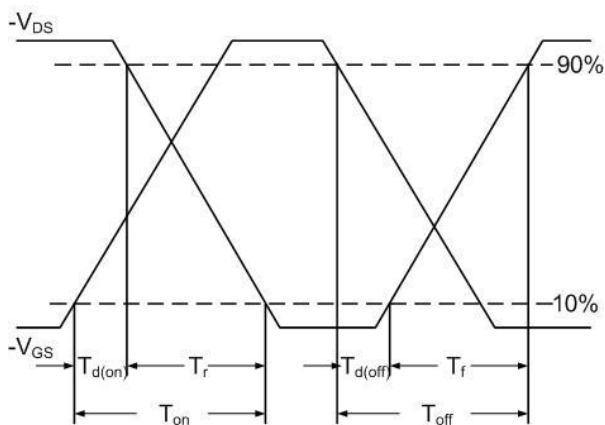
**Fig.7 Capacitance**



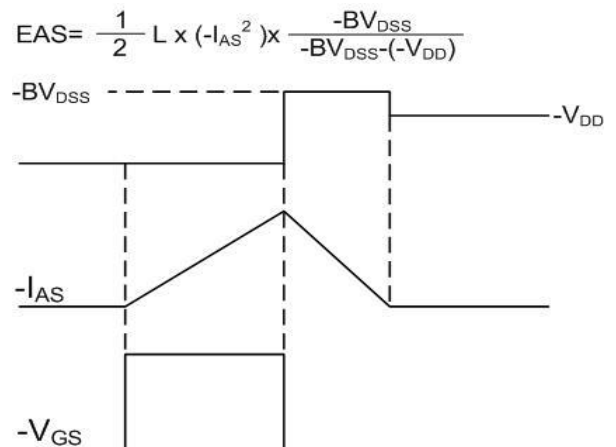
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

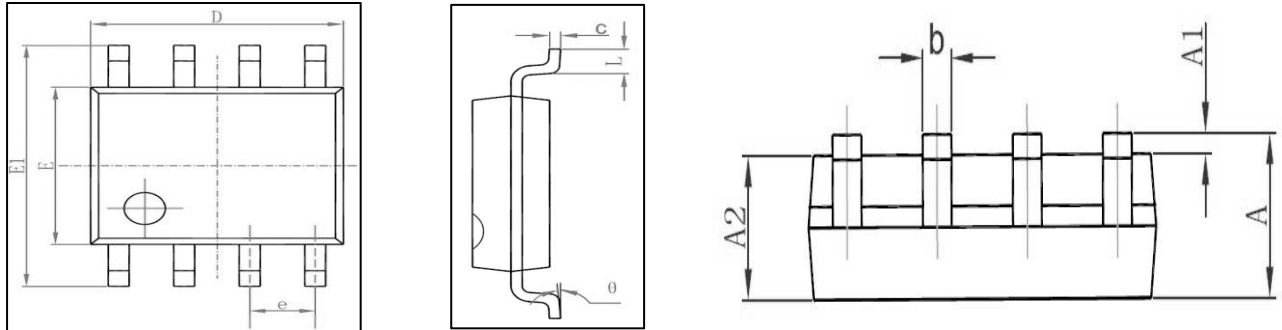


**Fig.10 Switching Time Waveform**

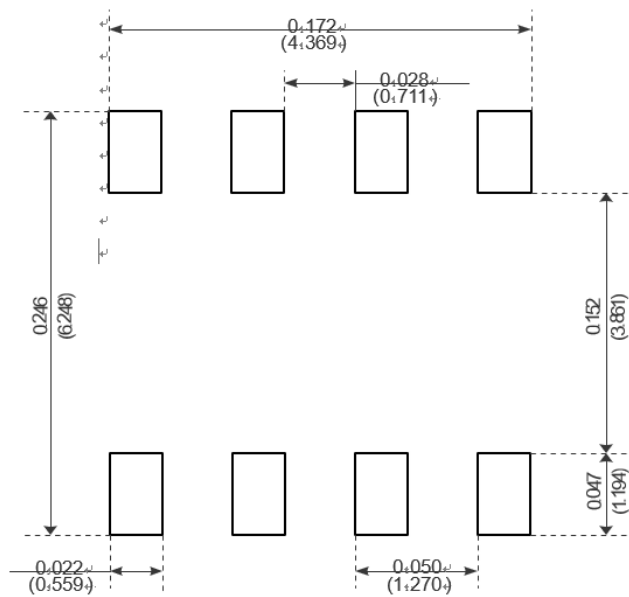


**Fig.11 Unclamped Inductive Waveform**

**SOP-8**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Recommended Minimum Pads



单击下面可查看定价，库存，交付和生命周期等信息

[>>YFW\(佑风微\)](#)