



### COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

## **Product Summary**

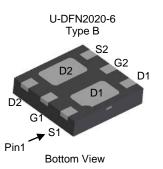
Device	V <sub>(BR)DSS</sub>	RDS(ON) Max	I <sub>D Max</sub> T <sub>A</sub> = +25°C
04		4.0Ω @ V <sub>GS</sub> = 10V	0.39A
Q1 N-Channel	60V	4.1Ω @ V <sub>GS</sub> = 5V	0.38A
		4.2Ω @ V <sub>GS</sub> = 4V	0.37A
	Q2 hannel -20V	$72m\Omega @ V_{GS} = -4.5 V$	-2.9A
Q2 P-Channel		$108m\Omega @ V_{GS} = -2.7V$	-2.3A
· c.i.diiiloi		$123m\Omega @ V_{GS} = -2.5V$	-2.2A

## Description

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

### Load Switch



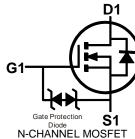


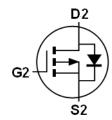
### Features

- Low On-Resistance
- Low Input Capacitance
- Low Profile, 0.6mm Maximum Height
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

## **Mechanical Data**

- Case: U-DFN2020-6 Type B
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)





P-CHANNEL MOSFET

Internal Schematic

## Ordering Information (Note 5)

Part Number	Case	Packaging	
DMC67D8UFDBQ-7	U-DFN2020-6 Type B	3000/Tape & Reel	
DMC67D8UFDBQ-13	U-DFN2020-6 Type B	10,000/Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

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### **Marking Information**

Date Code Key		7D YWX		YWX = Y = Ye W = We	roduct Type Ma Date Code Ma ear (ex: $9 = 20$ eek (ex: $a = we$ ernal code (ex:	arking 19) eek 27; z repres	sents week 52	2 and 53)	
Year	2017	2018	2019	2020	2021	2022	2023	2024	2025
Code	7	8	9	0	1	2	3	4	5
Week		1-26			27-52			53	
Code		A-Z			a-z			Z	
Internal Code	Sun	M	on	Tue	Wed	Thu		Fri	Sat

DMC67D8UFDBQ Document number: DS41258 Rev.3 – 2

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## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Q1 N-Channel	Q2 P-Channel	Unit
Drain-Source Voltage			V <sub>DSS</sub>	60	-20	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	±12	V
Continuous Drain Current (Note 7) N-Channel: $V_{GS} = 10V$ P-Channel: $V_{GS} = -4.5V$	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	0.39 0.31	-2.9 -2.3	A
Maximum Continuous Body Diode Forward Current (Note 7)			I <sub>S</sub>	0.39	-2.9	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	0.8	-20	A
Pulsed Source Current (10µs Pulse, Duty Cycle	= 1%)		I <sub>SM</sub>	-0.8	-20	А

# **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	PD	0.58	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>OJA</sub>	215	°C/W
Total Power Dissipation (Note 7)	T <sub>A</sub> = +25°C	PD	0.89	W
Thermal Resistance, Junction to Ambient (Note 7) Steady State		R <sub>OJA</sub>	140	°C/W
Thermal Resistance, Junction to Case (Note 7)	R <sub>eJC</sub>	35	C/W	
Operating and Storage Temperature Range		T <sub>J.</sub> T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics: Q1 N-Channel (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	T	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)	Symbol	WIIN	Тур	wax	Unit	Test Condition
Drain-Source Breakdown Voltage	<b>D</b> \/= aa	60		I	V	1/22 - 0/2 = -1000
0	BV <sub>DSS</sub>	00		1.0	uA	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_		- F	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	_		±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)					.,	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.0	_	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
			1.7	4.0		$V_{GS} = 10V, I_D = 0.5A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	1.6 1.8	4.1 4.2	Ω	$V_{GS} = 5V, I_D = 0.2A$
					1	$V_{GS} = 4V, I_D = 0.2A$
Diode Forward Voltage	V <sub>SD</sub>	_	0.8	1.1	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C <sub>iss</sub>		41	—	pF	
Output Capacitance	Coss	_	4.4	—	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	_	2.6	—	pF	1 = 1.000112
Gate Resistance	Rg	_	900	—	Ω	f = 1MHz, V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V
Total Gate Charge	Qg	—	0.4	—	рС	
Gate-Source Charge	Q <sub>gs</sub>	_	0.2	—	рС	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Q <sub>gd</sub>	—	0.1	—	рС	I <sub>D</sub> = 250mA
Turn-On Delay Time	t <sub>D(ON)</sub>		3.7	—	ns	
Turn-On Rise Time	t <sub>R</sub>	_	3.6		ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>		102	—	ns	$R_g = 25\Omega, I_D = 200 mA$
Turn-Off Fall Time	t <sub>F</sub>	—	22	—	ns	
Reverse Recovery Time	t <sub>RR</sub>	_	20	—	ns	I <sub>F</sub> = 1A, di/dt = 100A/μs
Reverse Recovery Charge	Q <sub>RR</sub>		7.9	—	nC	I <sub>F</sub> = 1A, di/dt = 100A/µs



# Electrical Characteristics: Q2 P-Channel (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

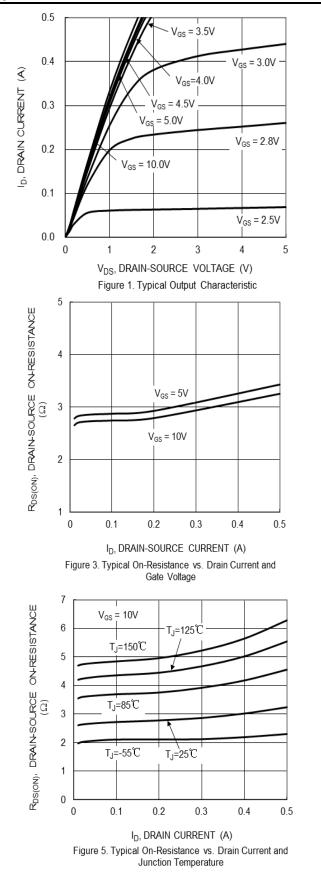
			1	1		
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	—	—	V	$I_D = -250 \mu A, V_{GS} = 0 V$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	—	-1	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Body Leakage Current	I <sub>GSS</sub>	_	—	±100	nA	$V_{DS} = 0V, V_{GS} = \pm 12V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.6	—	-1.25	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	51 87 99	72 108 123	mΩ	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -3.5A V <sub>GS</sub> = -2.7V, I <sub>D</sub> = -3.0A V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -2.6A
Diode Forward Voltage	V <sub>SD</sub>		-0.79	-1.26	V	I <sub>S</sub> = -1.7A, V <sub>GS</sub> = 0V
DYNAMIC PARAMETERS (Note 9)						
Total Gate Charge	Qg	_	7.3	—	nC	$V_{GS} = -4.5V, V_{DS} = -10V, I_{D} = -3.0A$
Gate-Source Charge	Q <sub>gs</sub>	_	2.0	—	nC	$V_{GS} = -4.5V, V_{DS} = -10V, I_{D} = -3.0A$
Gate-Drain Charge	Q <sub>gd</sub>	_	1.9	—	nC	$V_{GS} = -4.5V, V_{DS} = -10V, I_{D} = -3.0A$
Turn-On Delay Time	t <sub>D(on)</sub>	_	12	—	ns	
Turn-On Rise Time	tr	_	20	-	ns	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V,
Turn-Off Delay Time	t <sub>D(off)</sub>	_	38	—	ns	$R_L = 10\Omega, R_G = 6\Omega$
Turn-Off Fall Time	t <sub>f</sub>	_	41	—	ns	
Input Capacitance	C <sub>iss</sub>	_	443	—	pF	
Output Capacitance	Coss	_			V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		101	_	pF	

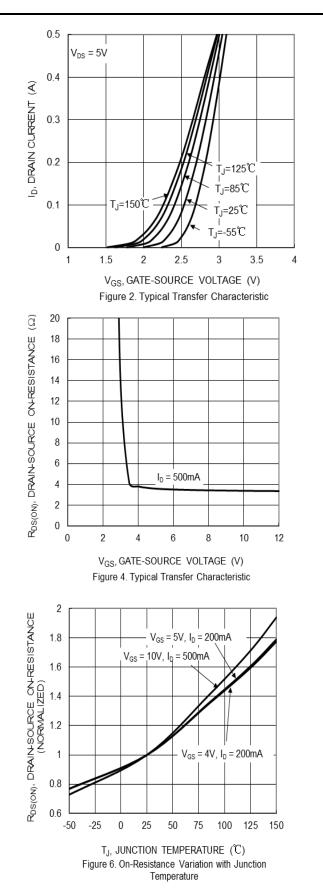
 Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect. Notes:

9. Guaranteed by design. Not subject to product testing.



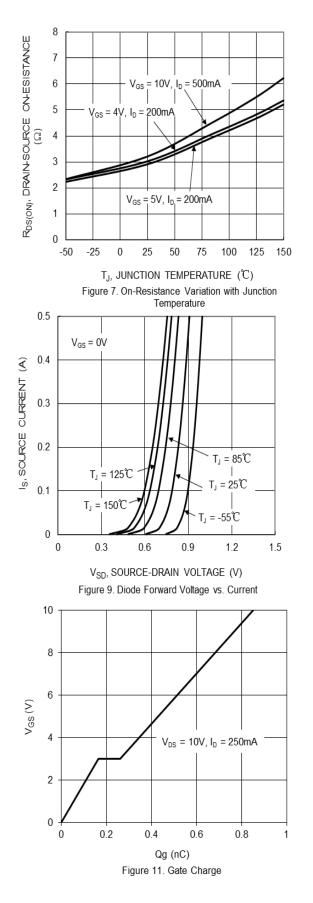
## **Typical Characteristics: N-Channel**

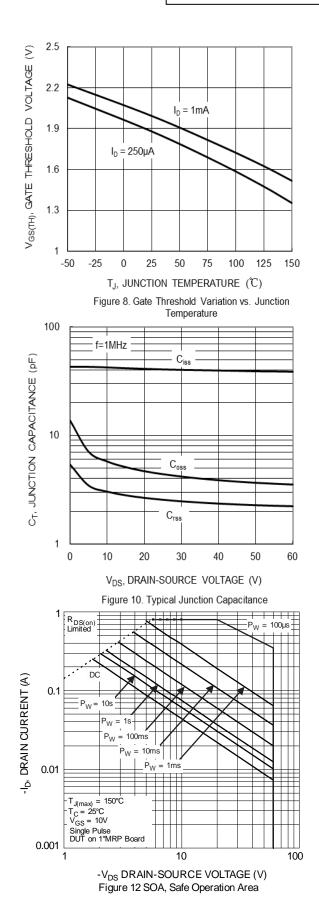




DMC67D8UFDBQ Document number: DS41258 Rev.3 – 2

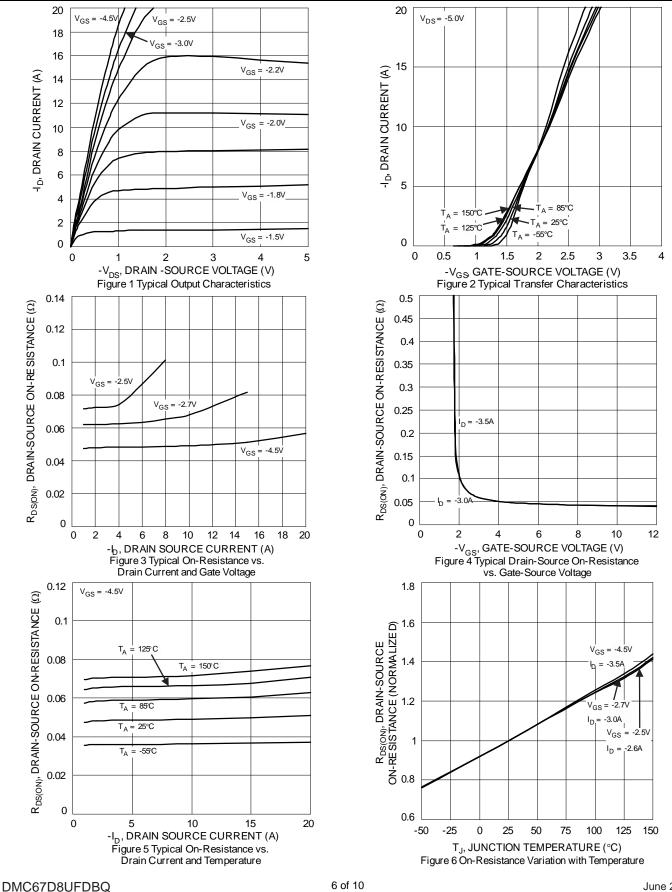








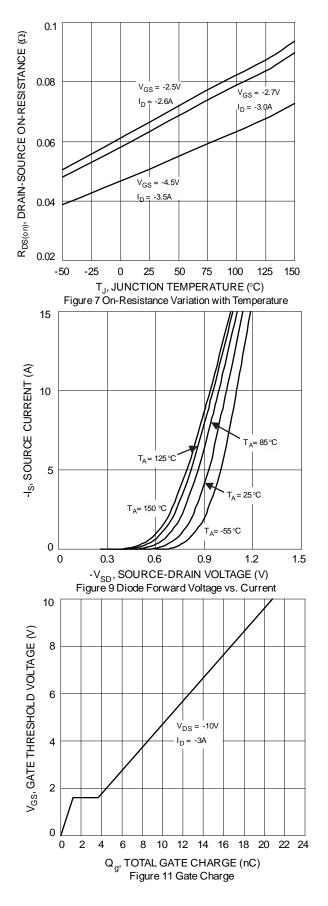
# **Typical Characteristics: P-Channel**

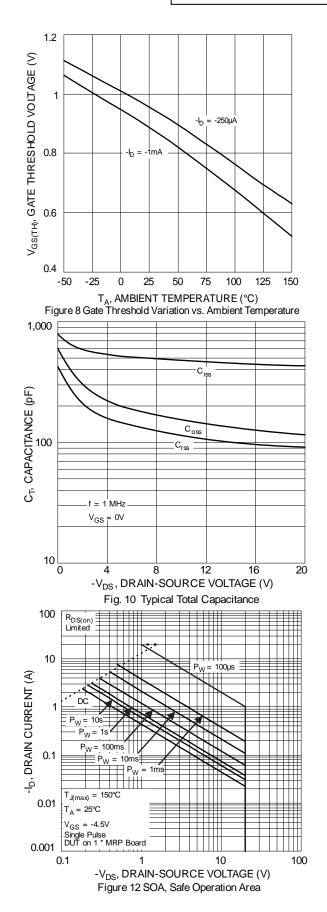


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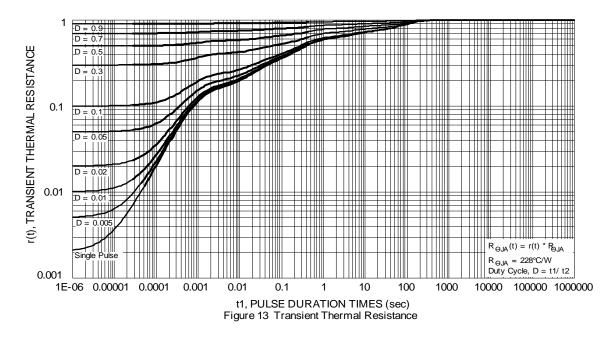






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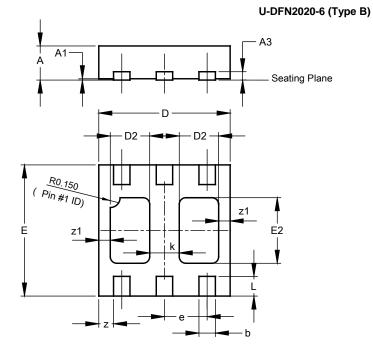






## **Package Outline Dimensions**

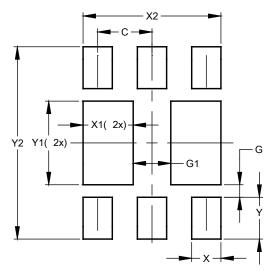
Please see http://www.diodes.com/package-outlines.html for the latest version.



1								
	U-DFN2020-6							
Туре В								
Dim	Min	Max	Тур					
Α	0.545	0.605	0.575					
A1	0.00	0.05	0.02					
A3	-	-	0.13					
b	0.20	0.30	0.25					
D	1.95	2.075	2.00					
D2	0.50	0.70	0.60					
е	-	-	0.65					
Е	1.95	2.075	2.00					
E2	0.90	1.10	1.00					
k	-	-	0.45					
L	0.25	0.35	0.30					
z	-	-	0.225					
z1	-	-	0.175					
All	Dimens	ions in	mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



### U-DFN2020-6 (Type B)

Dimensions	Value (in mm)
С	0.650
G	0.150
G1	0.450
Х	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
Y2	2.300

Document number: DS41258 Rev.3 - 2

DMC67D8UFDBQ



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