



#### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
60V	$2\Omega$ @ $V_{GS} = 4.5V$	380mA
000	$2.5\Omega$ @ $V_{GS} = 2.5V$	340mA

## **Description**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### **Applications**

- Motor Control
- Power Management Functions
- Backlighting

## **Features and Benefits**

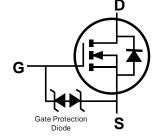
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 1kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

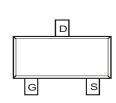
#### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42
   Leadframe. Solderable per MIL-STD-202, Method 208 <a>3</a>
- Weight: 0.008 grams (Approximate)









Top View

**Equivalent Circuit** 

Top View

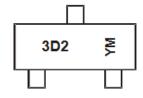
## **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN62D0U-7	SOT23	3000/Tape & Reel
DMN62D0U-13	SOT23	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



3D2 = Product Type Marking Code YM or  $\overline{Y}$ M = Date Code Marking Y or  $\overline{Y}$  = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Key

Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	D	Е	F	G	Н	ı	J	K	L	M	N	0
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

DMN62D0U

Document number: DS38023 Rev. 2 - 2

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# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	60	V		
Gate-Source Voltage	V <sub>GSS</sub>	±20	V		
Continuous Prain Correct (Note C) V	$T_A = +25$ °C $T_A = +70$ °C	ID	380 300	mA	
Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V	I <sub>D</sub>	430 340	mA		
Maximum Continuous Body Diode Forward Current	Is	0.4	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6) (Note 6)	)	I <sub>DM</sub>	1.2	А

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)		$P_{D}$	380	mW	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	٥	338	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<5s	$R_{\theta JA}$	292	C/VV	
Total Power Dissipation (Note 6)		$P_{D}$	590	mW	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	216	°C/W	
Themal Resistance, Junction to Ambient (Note 6)	t<5s	$R_{\theta JA}$	177	C/VV	
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C	

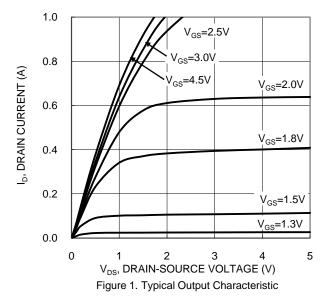
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	1		>	$V_{GS} = 0V$ , $I_D = 10\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5		1.0	<b>V</b>	$V_{DS} = 10V, I_D = 250\mu A$	
			1.2	2.0		$V_{GS} = 4.5V, I_D = 0.1A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	1.4	2.5	Ω	$V_{GS} = 2.5V, I_D = 0.05A$	
			1.8	3.0		$V_{GS} = 1.8V, I_D = 0.05A$	
Forward Transconductance	Y <sub>fs</sub>	_	1.8	_	S	$V_{DS} = 10V, I_D = 0.2A$	
Diode Forward Voltage	$V_{SD}$	_	0.8	1.3	V	$V_{GS} = 0V, I_S = 115mA$	
DYNAMIC CHARACTERISTICS (Note 8)	,						
Input Capacitance	C <sub>iss</sub>	_	32	_	рF	.,	
Output Capacitance	Coss	_	3.9		pF	$V_{DS} = 30V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	2.4	_	pF	1 = 1.0WI 12	
Gate Resistance	$R_{q}$	_	101	_	Ω	$f = 1MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$	
Total Gate Charge	Qg	_	0.5	_	nC	4.51/.1/	
Gate-Source Charge	Q <sub>qs</sub>	_	0.09	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Drain Charge	Q <sub>qd</sub>	_	0.09	_	nC	$I_D = 250 \text{mA}$	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	2.4	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	2.5	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	22.6	_	ns	$R_G = 25\Omega$ , $I_D = 200mA$	
Turn-Off Fall Time	t <sub>F</sub>	_	12.5	_	ns		

Notes:

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
  6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
  7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.





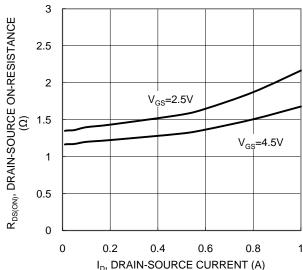


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

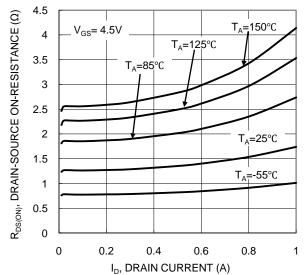
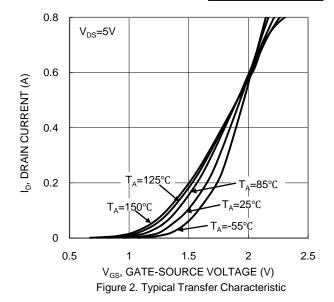
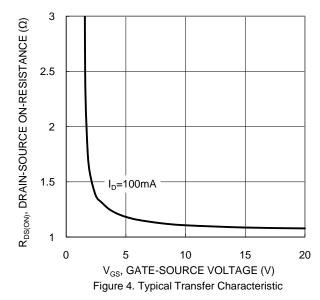


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





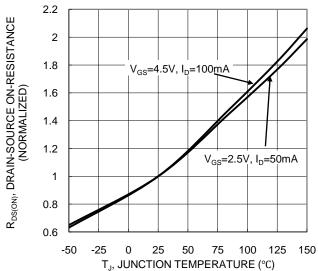
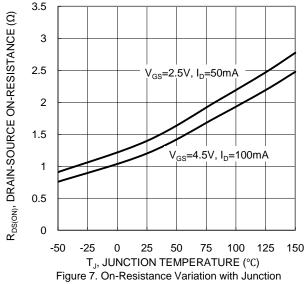


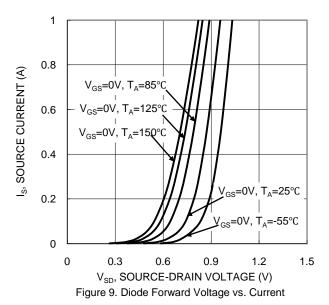
Figure 6. On-Resistance Variation with Junction Temperature

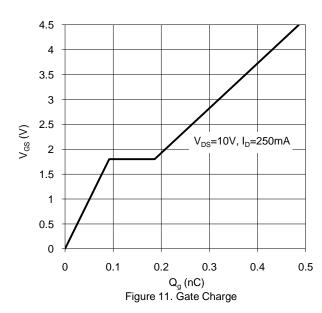


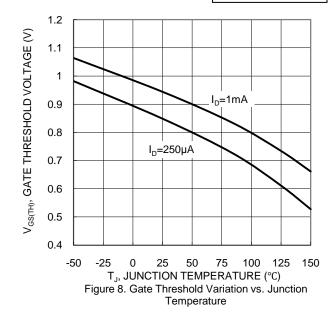


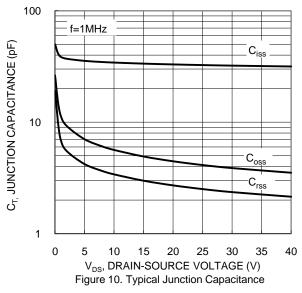


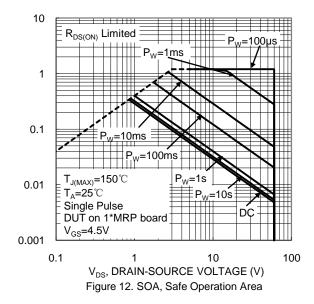
Temperature











ID, DRAIN CURRENT (A)



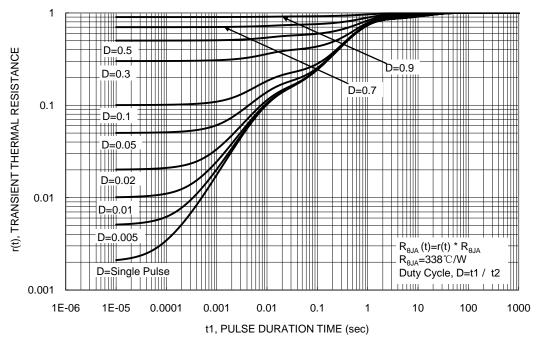


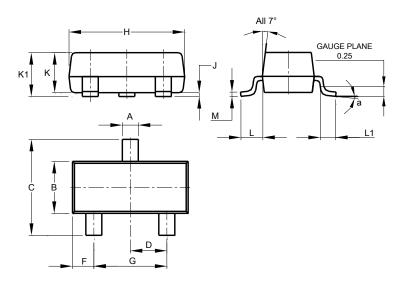
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT23

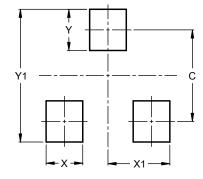


SOT23								
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	0°	8°						
All Dimensions in mm								

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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