

Dual Differential Comparators

LR393/LR2903

DESCRIPTION:

The LR393 and LR2903 consists of two voltage comparators with an offset voltage specification as low as 2.0mV max. for two comparators which were designed specifically to operation from a single power supply over a wide range of voltages. Operate from split power supplies is also possible, and the low power supply current drain is independent of the magnitude of the power supply voltage. These comparators also have a unique characteristic in that the input common-mode voltage range includes ground, even though operated from a single power supply voltage.



DIP-8

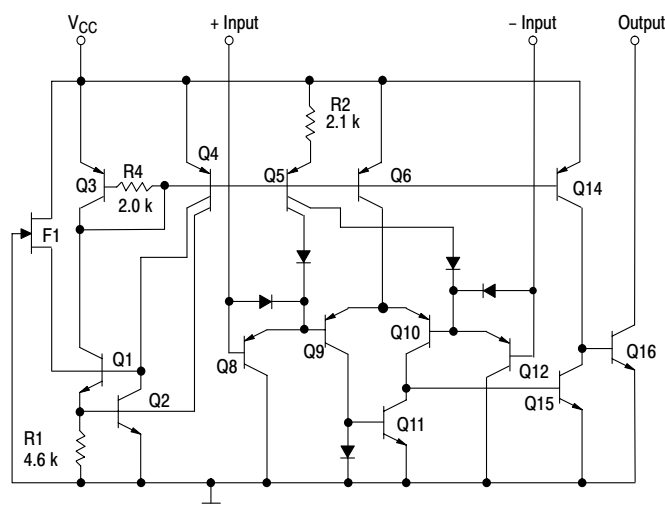


SOP-8

FEATURE:

- Wide supply voltage range: single supply operation: 2V to 36V, dual supply operation: $\pm 1V$ to $\pm 18V$
- Very low supply current drain (0.8mA) independent of supply voltage(2.0mW/comparator at 5.0 V_{DC})
- Low input biasing current: 25nA
- Low input offset current: 5.0nA; Low input offset voltage: 5.0mV
- Input common-mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- Output voltage compatible with TTL,DTL,ECL,MOS and CMOS logic systems.

BLOCK DIAGRAM

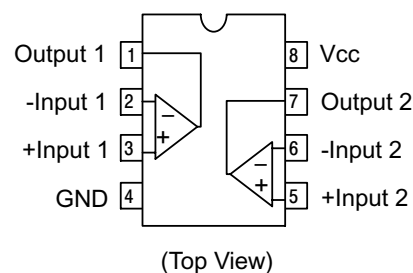


NOTE: Diagram shown is for 1 comparator

ORDERING INFORMATION

Device	Package
LR393	DIP-8
LR393D	SOP-8
LR2903	DIP-8
LR2903D	SOP-8

PIN CONFIGURATION



PIN DESCRIPTIONS

No	Description	Symbol	No	Description	Symbol
1	Output 1	OUT1	5	+Input2	IN2 (+)
2	-Input1	IN1 (-)	6	-Input2	IN2 (-)
3	+Input1	IN1 (+)	7	Output 2	OUT2
4	Ground	GND	8	Supply Voltage	Vcc

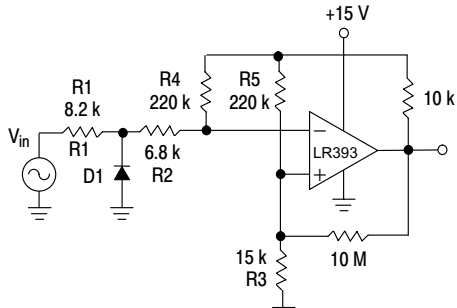
ABSOLUTE MAXIMUM RATINGS

Characteristics		Symbol	Value		Unit
			Min	Max	
Supply Voltage	Single supply voltage	Vcc		36	V
	Dual supplies voltage			±18	
Differential Input Voltage		VIDR		36	V
Input common-mode voltage		VICR	-0.3	36	V
Output short-circuit to ground		I _{OG}		20	mA
Input Current		I _{IN}		50	mA
Maximum junction temperature		T _J		125	°C
Power Dissipation		P _D		570	mW
Operating Temperature Range LR393		T _{amb}	-40	85	°C
Operating Temperature Range LR2903		T _{amb}	-40	105	°C
Storage Temperature Range		T _{stg}	-65	150	°C

ELECTRICAL CHARACTERISTICS (Un less otherwise specified :Vcc=5V, Tamb=25°C)

Characteristics	Test conditions	Symbo	Mi	Typ.	Max	Unit
Input Offset Voltage		V _{IO}		±1.0	±5.0	mV
	0°C ≤ Ta ≤ 70°C				±9.0	
Input Offset Current		I _{IO}		±5.0	±50	nA
	0°C ≤ Ta ≤ 70°C				±150	
Input Bias Current		I _{IB}		25	250	nA
	0°C ≤ Ta ≤ 70°C				400	
Input Common-mode Voltage Range		V _{VICR}	0		Vcc-1.5	V
	0°C ≤ Ta ≤ 70°C		0		Vcc-2.0	
Supply Current	R _L =∞ dual comparator	I _{CC}		0.4	1.0	mA
	R _L =∞, dual comparator Vcc=30V				2.5	
Voltage Gain	R _L ≥ 15KΩ, Vcc=15V	G _V	50	200		V/mV
Large Signal Response Time	V _{IN} =TTL Logic Swing, V _{REF} =1.4V, V _{RL} =5.0V, R _L =5.1KΩ	t _{RES}		0.3		us
Response Time	V _{RL} =5.0V, R _L =5.1KΩ	t _{RES}		1.3		us
Input Differential Voltage		V _{ID}			Vcc	V
Output Sink Current	V _{IN} (-) ≥ 1.0V, V _{IN} (+) =0V, V _O ≤ 1.5V	I _{SINK}	6.0	16		mA
output saturation voltage	V _{IN} (-) ≥ 1.0V, V _{IN} (+) =0V, I _{SINK} ≤ 4.0mA	V _{SAT}		150	400	mV
	V _{IN} (-) ≥ 1.0V, V _{IN} (+) =0V, I _{SINK} ≤ 4.0mA 0°C ≤ Ta ≤ 70°C				700	
Output Leakage Current	V _{IN} (+) ≥ 1.0V, V _{IN} (-) =0V, V _O =5.0V	I _{OL}		0.1		nA
	V _{IN} (+) ≥ 1.0V, V _{IN} (-) =0V, V _O =30V 0°C ≤ Ta ≤ 70°C				1000	

APPLICATION CIRCUIT

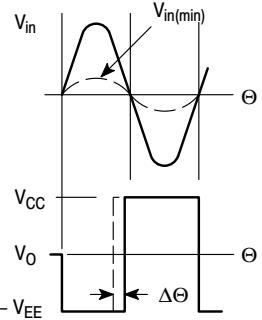
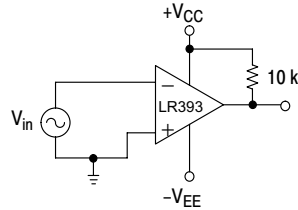


D1 prevents input from going negative by more than 0.6 V.

$$R1 + R2 = R3$$

$$R3 \leq \frac{R5}{10} \text{ for small error in zero crossing.}$$

Figure 1. Zero Crossing Detector (Single Supply)



$V_{in(min)} \approx 0.4 \text{ V peak for } 1\% \text{ phase distortion } (\Delta\Theta).$

Figure 2. Zero Crossing Detector (Split Supply)

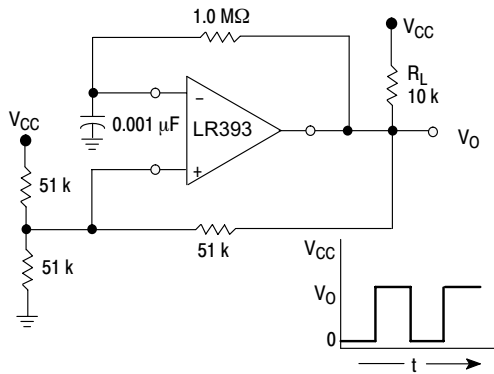
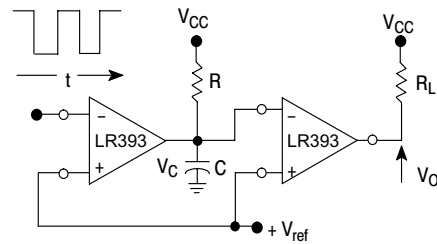


Figure 3. Free-Running Square-Wave Oscillator



"ON" for $t \geq t_0 + \Delta t$
where:

$$\Delta t = RC \ln \left(\frac{V_{ref}}{V_{CC}} \right)$$

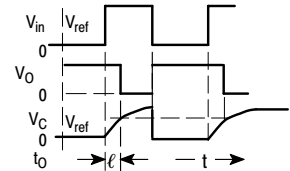


Figure 4. Time Delay Generator

TYPICAL CHARACTERISTICS CURVES

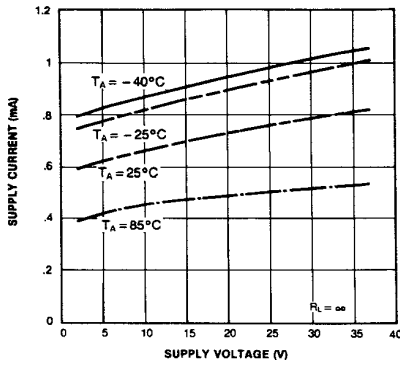


Figure 1. Supply Current vs Supply Voltage

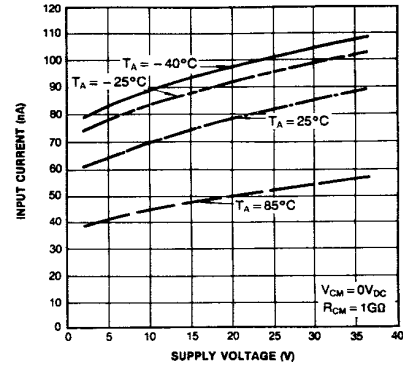


Figure 2. Input Current vs Supply Voltage

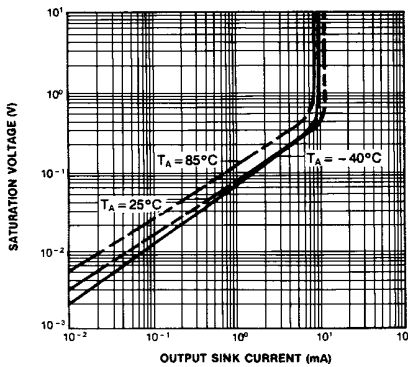


Figure 3. Output Saturation Voltage vs Sink Current

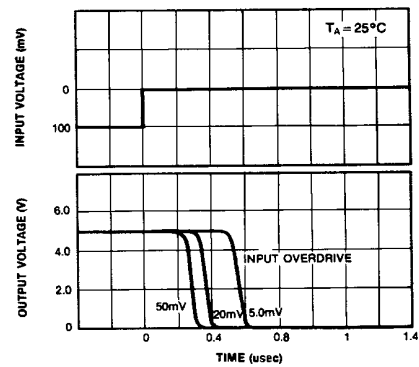


Figure 4. Response Time for Various Input Overdrive-Negative Transition

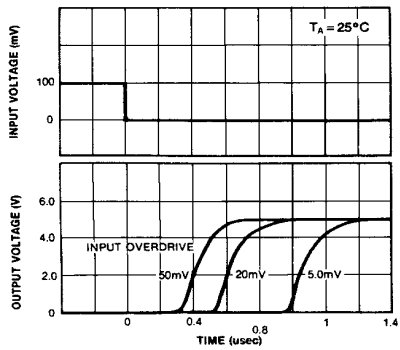
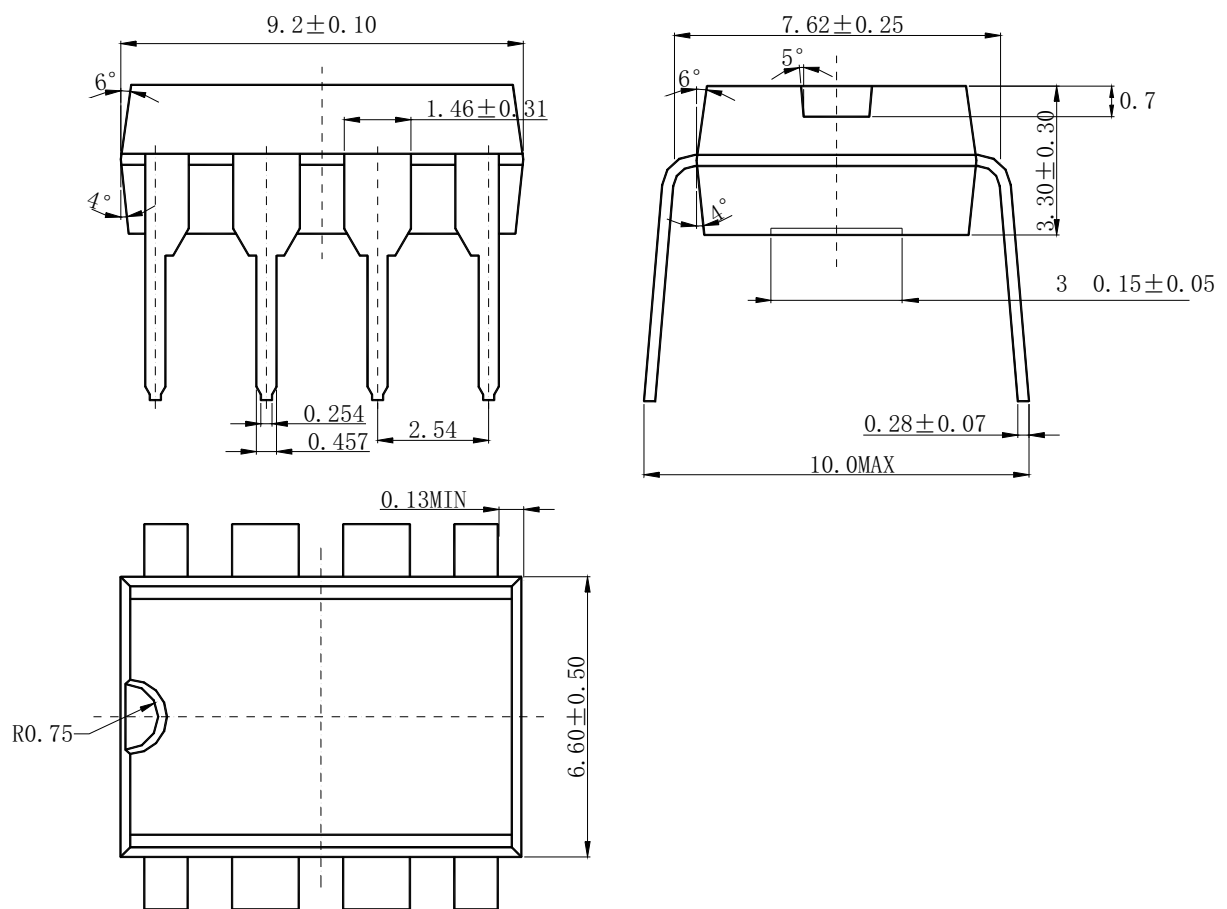


Figure 5. Response Time for Various Input Overdrive-Positive Transition

Mechanical Dimensions

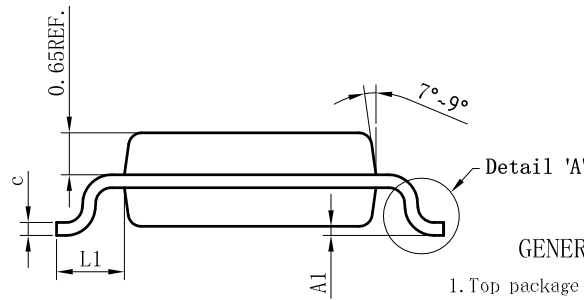
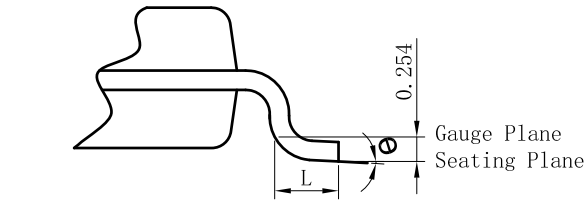
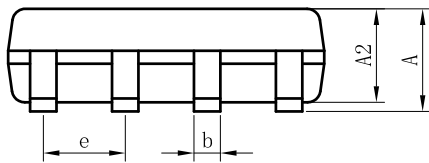
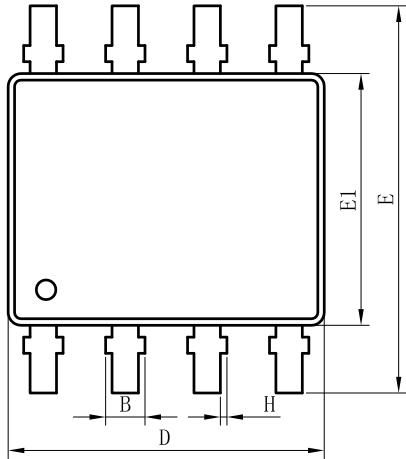
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Unit: mm



Mechanical Dimensions (Continued)

SOP-8

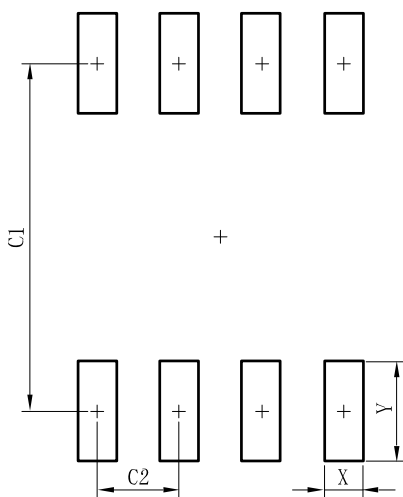


SOP8			
DIM	MIN	NOR	MAX
A	-	-	1.75
A1	0.10	0.15	0.20
A2	1.35	1.45	1.55
b	0.33	0.42	0.51
c	0.15	0.22	0.29
D	4.77	4.90	5.03
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27BSC		
L	0.46	0.66	0.86
L1	0.85	1.05	1.25
θ	0°	5°	8°
B	-	-	0.55
H	0	0.05	0.10
All Dimensions in mm			

GENERAL NOTES

1. Top package surface finish Ra0.4±0.2um
2. Bottom package surface finish Ra0.7±0.2um
3. Side package surface finish Ra0.4±0.2um
4. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
5. Dimension "b" Does Not Include Dambar Protrusion.

Suggested Pad layout



SOP8	
DIM	(mm)
X	0.60
Y	1.55
C1	5.40
C2	1.27

单击下面可查看定价，库存，交付和生命周期等信息

[>>LRC\(乐山无线电\)](#)