



#### **DUAL 20V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>ds(on)</sub>	R <sub>DS(ON)</sub> Package		
20V	$130m\Omega @ V_{GS} = 4.5V$	MSOP-8	2.5A	
200	150mΩ @ $V_{GS}$ = 2.7V	WISOP-8	2.3A	

### Description

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

# Applications

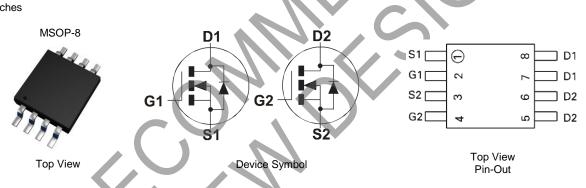
- **DC-DC Converters**
- **Power Management Functions**
- Motor Control
- **Disconnect Switches**

### **Features**

- Low On-Resistance
- Low Threshold
- Fast Switching Speed
- Low Gate Drive
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: MSOP-8
- Case Material: Molded Plastic, "Green" Molding Compound. . UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish @3
- Weight: 0.0277 grams (Approximate)



# Ordering Information (Note 4)

Part Number	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXMD63N02XTA	ZXM63N02	7	12	1,000
ZXMD63N02XTC	ZXM63N02	13	12	4,000
Notes: 1. No purposely adde	d lead. Fully EU Directive 2002	/95/EC (RoHS), 2011/65/EU (RoHS	2) & 2015/863/EU (RoHS 3) com	pliant.

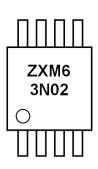
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



ZXM63N02 = Product Type Marking Code



ZXMD63N02X

#### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit	
Drain-Source Voltage Gate-Source Voltage				V <sub>DSS</sub>	20	V
				V <sub>GSS</sub>	±12	V
Continuous Drain Current Steady State $@V_{GS} = 10V; T_A = +25^{\circ}C (Notes 5 \& 6)$ $@V_{GS} = 10V; T_A = +70^{\circ}C (Notes 5 \& 6)$ $@V_{GS} = 10V; T_A = +70^{\circ}C (Notes 5 \& 6)$			ID	2.5 1.9 0.78	А	
Pulsed Drain Current (Notes 6 & 7)			I <sub>DM</sub>	19	А	
Continuous Source Current (Body Diode) (Note			(Notes 5 & 6)	ls	1.5	А
Pulsed Source Current (Body Diode)			(Notes 6 & 7)	I <sub>SM</sub>	19	А

#### Thermal Characteristics

Characteristic		Symbol	Value	Unit	
	(Notes 6 & 8)		0.87		
Power Dissipation	(Notes 5 & 6)	PD	1.25	W	
	(Notes 8 & 9)		1.04		
	(Notes 6 & 8)		143		
Thermal Resistance, Junction to Ambient	(Notes 5 & 6)	R <sub>0JA</sub>	100	°C/W	
	(Notes 8 & 9)		120		
Thermal Resistance, Junction to Leads	(Note 10)	R <sub>θJL</sub>	84.9	°C/W	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

5. For a device surface mounted on FR-4 PCB measured at t  $\leq$  10 sec.

6. For device with one active die.

Notes:

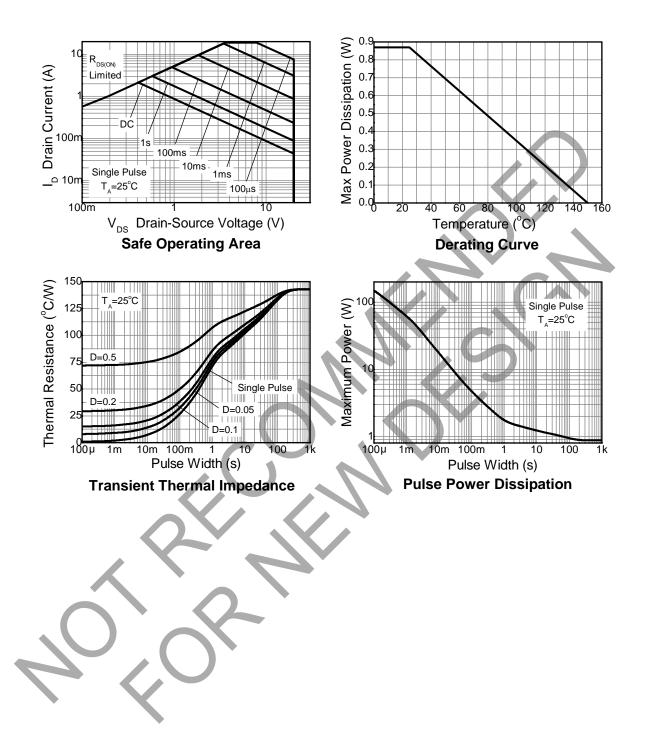
For a device surface mounted on 25mm FR-4 PCB, D = 0.02, pulse width 300µs – pulse width limited by maximum junction temperature.
 For a device surface mounted on 25mm x 25mm FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions.

For device with two active die running at equal power.
 Thermal resistance from junction to solder-point (at the end of the drain lead).

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## **Thermal Characteristics**





# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

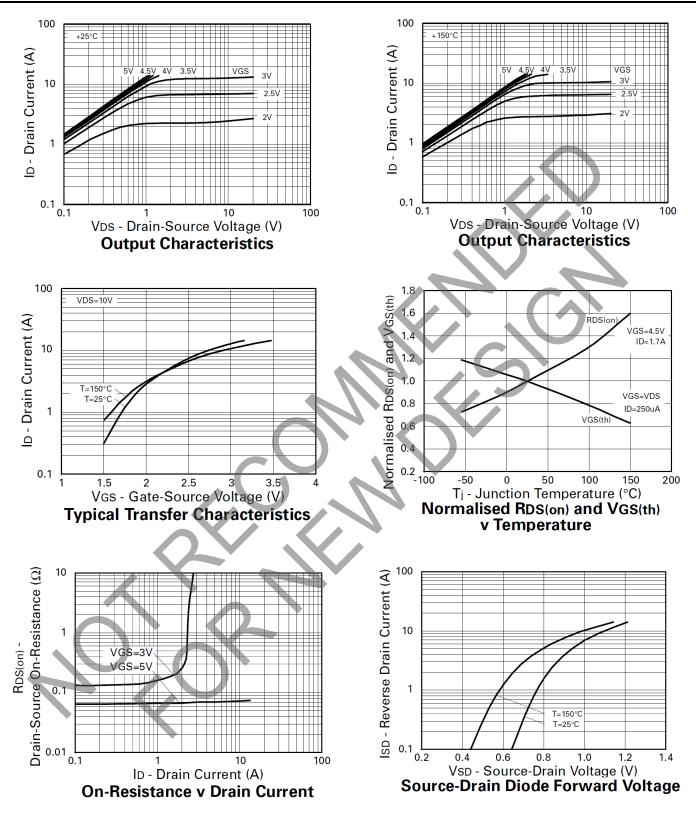
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	—		V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	_	—	100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.7	_	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Statia Drain Source On Begistenes (Note 11)			65	130	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 1.7A
Static Drain-Source On-Resistance (Note 11)	R <sub>DS(ON)</sub>	_	90	150	1112	V <sub>GS</sub> = 2.7V, I <sub>D</sub> = 0.85A
Forward Transconductance (Notes 11 & 13)	<b>g</b> fs	2.6	—	—	S	$V_{DS} = 10V, I_D = 0.85A$
Diodes Forward Voltage (Note 11)	V <sub>SD</sub>	_	0.85	0.95	V	T <sub>J</sub> = +25°C, I <sub>S</sub> = 1.7A, V <sub>GS</sub> = 0V
DYNAMIC CHARACTERISTICS						
Input Capacitance (Notes 12 & 13)	Ciss	—	350	700		
Output Capacitance (Notes 12 & 13)	Coss	_	120	250	рF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Reverse Transfer Capacitance (Notes 12 & 13)	Crss	_	50	100		T = T.OWI12
Gate Resistance (Notes 12 & 13)	Rg	_	3.8	7.6	Ω	$f = 1MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$
Total Gate Charge (Notes 12 & 13)	Qg	_	4.5	6		
Gate-Source Charge (Notes 12 & 13)	Q <sub>gs</sub>		0.5	0.65	nC	$V_{GS} = 4.5V, V_{DS} = 16V,$
Gate-Drain Charge (Notes 12 & 13)	Q <sub>gd</sub>	—	2	2.5		$I_D = 1.7A$
Reverse Recovery Time (Note 13)	t <sub>RR</sub>		15	30	ns	T <sub>J</sub> = +25°C, I <sub>F</sub> = 1.7A,
Reverse Recovery Charge (Note 13)	Q <sub>RR</sub>	—	5.9	-	nC	di/dt = 100A/µs
Turn-On Delay Time (Notes 12 & 13)	t <sub>D(ON)</sub>	-	3.4	_		
Turn-On Rise Time (Notes 12 & 13)	t <sub>R</sub>	—	8.1	—	ns	$V_{DD} = 10V, I_D = 1.7A,$
Turn-Off Delay Time (Notes 12 & 13)	t <sub>D(OFF)</sub>	—	13.5	—	ns	$R_G = 6\Omega, R_D = 5.7\Omega$
Turn-Off Fall Time (Notes 12 & 13)	tF		9.1	— (		

Notes: 11. Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤2%.
12. Switching characteristics are independent of operating junction temperature.
13. For design aid only, not subject to production testing.

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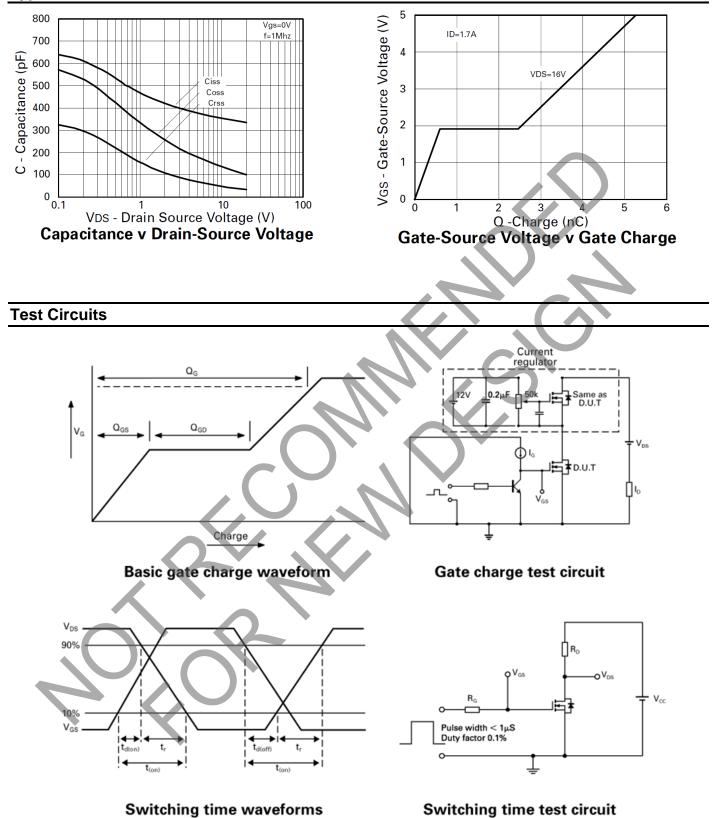


# **Typical Characteristics**





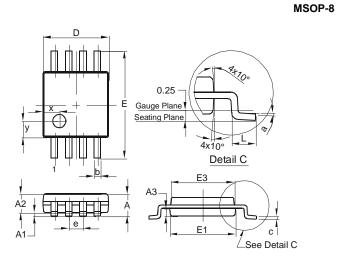
#### Typical Characteristics (Cont.)





#### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.



	MS	OP-8		
Dim	Min	Max	Тур	
Α	I	1.10	-	
A1	0.05	0.15	0.10	
A2	0.75	0.95	0.86	
A3	0.29	0.49	0.39	
b	0.22	0.38	0.30	
С	0.08	0.23	0.15	
D	2.90	3.10	3.00	
ш	4.70	5.10	4.90	
E1	2.90	3.10	3.00	
E3	2.85	3.05	2.95	
е	-	1	0.65	
L	0.40	0.80	0.60	
а	0°	8°	4°	
X	-	I	0.750	
У	-	I	0.750	
All E	Dimen	sions	in mm	

0.650

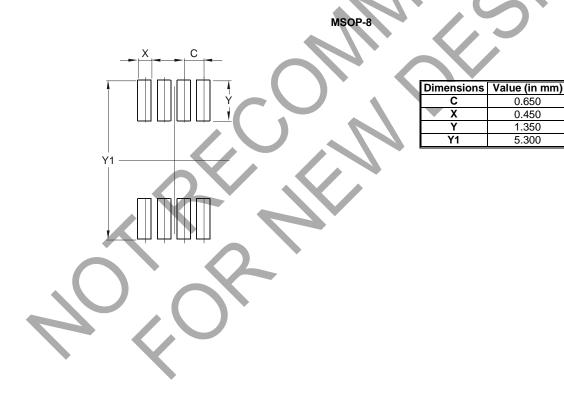
0.450

1.350

5.300

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.





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