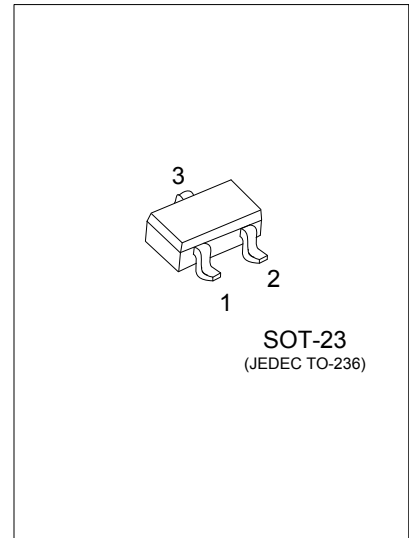




## 2SK508

## N-CHANNEL JFET

### HIGH FREQUENCY AMPLIFIER N-CHANNEL SILICON JUNCTION FIELD EFFECT TRANSISTOR



#### DESCRIPTION

The UTC **2SK508** is NPN transistor with High forward transfer admittance and low input capacitance.

It is suitable for cordless telephone, AM tuner and wireless installation, etc.

#### FEATURES

\* High forward transfer admittance

\* Low input capacitance

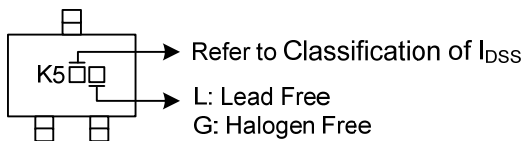
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
2SK508L-xxx-AE3-R	2SK508G-xxx-AE3-R	SOT-23	S	D	G	Tape Reel

Note: Pin Assignment: S: Source D: Drain G: Gate

<p>2SK508G-xxx-AE3-R</p>	<p>(1) R: Tape Reel</p> <p>(2) AE3: SOT-23</p> <p>(3) x: Refer to Classification of <math>I_{DSS}</math></p> <p>(4) G: Halogen Free and Lead Free, L: Lead Free</p>
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#### MARKING



■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Gate to Drain Voltage	V <sub>GDO</sub>	-15	V
Gate to Source Voltage	V <sub>GSO</sub>	-15	V
Drain to Source Voltage (V <sub>GS</sub> =-4.0 V)	V <sub>DSX</sub>	15	V
Drain Current (DC)	I <sub>D</sub>	50	mA
Gate Current (DC)	I <sub>G</sub>	5	mA
Power Dissipation	P <sub>D</sub>	200	mW
Junction Temperature	T <sub>J</sub>	+150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

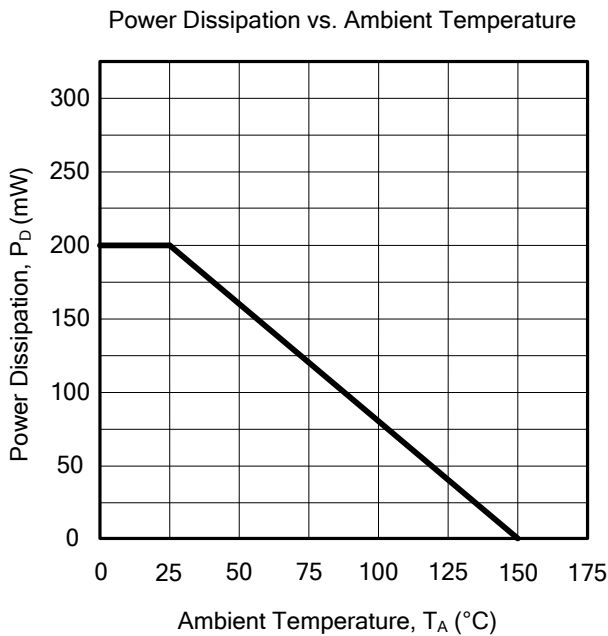
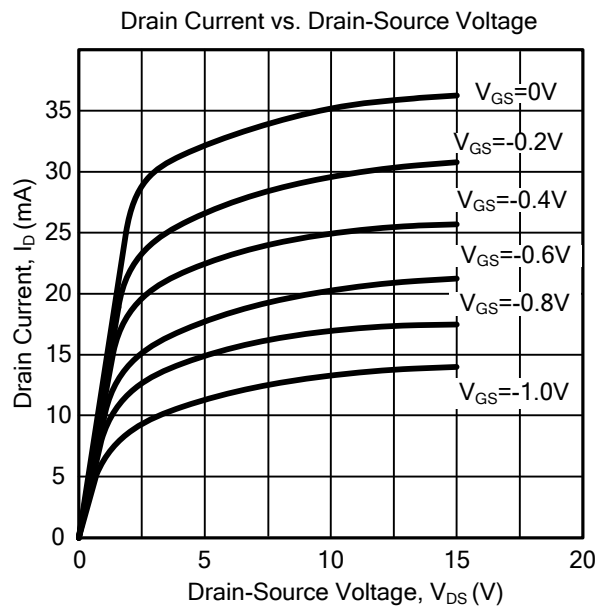
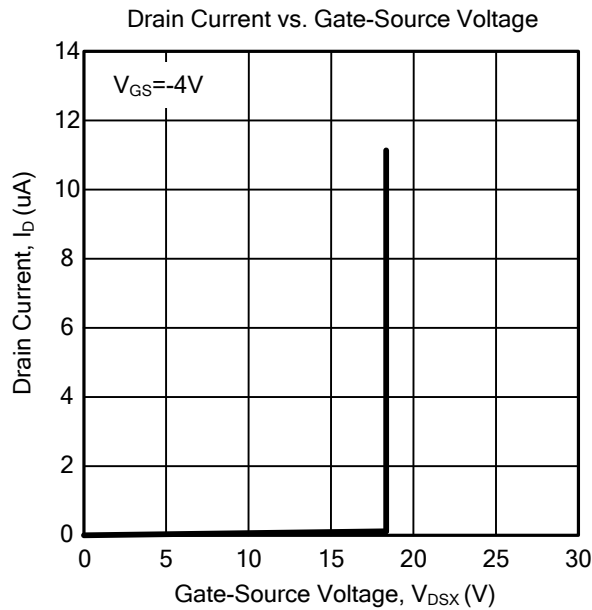
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Gate Cut-Off Current	I <sub>GSS</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =0V			-1.0	nA
Zero Gate Voltage Drain Current (Note)	I <sub>DSS</sub>	V <sub>DS</sub> =5.0V, V <sub>GS</sub> =0V	10	20	50	mA
Gate to Source Cut-Off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =5.0V, I <sub>D</sub> =10μA	-0.6	-1.4	-3.5	V
Forward Transfer Admittance (Note)	y <sub>Fs1</sub>	V <sub>DS</sub> =5.0V, I <sub>D</sub> =10mA, f=1.0kHz	14	19		mS
	y <sub>Fs2</sub>	V <sub>DS</sub> =5.0V, V <sub>GS</sub> =0V, f=1.0kHz	14	26		mS
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =5.0V, I <sub>D</sub> =10mA, f=1.0MHz		4.8		pF
Feedback Capacitance	C <sub>RSS</sub>	V <sub>DS</sub> =5.0V, I <sub>D</sub> =10mA, f=1.0MHz		1.6		pF

Note: Pulsed: P<sub>w</sub>≤1ms, Duty Cycle≤1%.

■ I<sub>DSS</sub> CLASSIFICATION

MARKING	K51	K52	K53
I <sub>DSS</sub> (mA)	10 ~ 20	15 ~ 30	25 ~ 50

■ TYPICAL CHARACTERISTICS



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