



## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON) max</sub>	Ι <sub>D</sub> T <sub>A</sub> = +25°C
2014	42.5mΩ @ $V_{GS}$ = -4.5V	-4.0A
-20V	71mΩ @ V <sub>GS</sub> = -1.8V	-2.0A

## Description

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

# Applications

- DC-DC Converters
- Power Management Functions

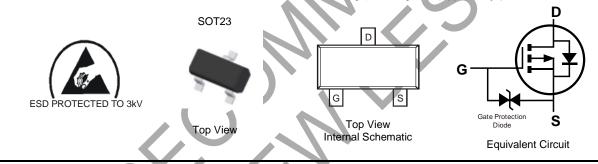
## P-CHANNEL ENHANCEMENT MODE MOSFET

### Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 3kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

## **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



## Ordering Information (Note 5)

Part Number	Compliance	Case	Packaging
DMG3415U-7	Standard	SOT23	3,000/Tape & Reel
DMG3415UQ-7	Automotive	SOT23	3,000/Tape & Reel
DMG3415U-13	Standard	SOT23	10,000/Tape & Reel

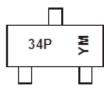
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/.

5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



34P = Product Type Marking Code $YM or <math>\overline{YM} = Date Code Marking$  $Y or <math>\overline{Y} = Year (ex: F = 2018)$ 

M = Month (ex: 9 = September)

Date	Code	Ke
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Year	201	8	2019		2020	20	)21	2022		2023	1	2024
Code	F		G		Н			J		K		L
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

#### DMG3415U Document number: DS31735 Rev. 13 - 3



DMG3415U

# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	-20	V
Gate-Source Voltage	V <sub>GSS</sub>	±8	V
Continuous Drain Current (Note 6) $V_{GS}$ = -4.5V	ID	-4.0 -3.5	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	-30	А

# **Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	PD	0.9	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>eja</sub>	139	°C/W
Thermal Resistance, Junction to Case (Note 6)	Rejc	32	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T <sub>A</sub> = +25°C, unless otherwise specified.)										
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition				
OFF CHARACTERISTICS (Note 7)										
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	-	-	V	$V_{GS} = 0V, I_D = -250\mu A$				
Zero Gate Voltage Drain Current	IDSS		—	-1	μA	$V_{DS} = -20V, V_{GS} = 0V$				
Gate-Source Leakage	Igss	-	T.	±10	μA	$V_{GS} = \pm 8.0V, V_{DS} = 0V$				
ON CHARACTERISTICS (Note 7)										
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.3	-0.55	-1.0	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$				
			31	42.5		$V_{GS} = -4.5V, I_D = -4.0A$				
Static Drain-Source On-Resistance	R <sub>DS</sub> (ON)	~	40	53	mΩ	$V_{GS} = -2.5V, I_D = -3.5A$				
			51	71		$V_{GS} = -1.8V, I_D = -2.0A$				
Forward Transfer Admittance	<b>G</b> FS		3	—	S	$V_{DS} = -5V, I_D = -4A$				
DYNAMIC CHARACTERISTICS (Note 8)										
Input Capacitance	C <sub>iss</sub>	—	294	—	pF					
Output Capacitance	C <sub>oss</sub>	—	104	—	pF	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f = 1.0MHz				
Reverse Transfer Capacitance	C <sub>rss</sub>	—	25	—	pF					
Gate Resistance	Rg	_	250	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$				
SWITCHING CHARACTERISTICS (Note 8)										
Total Gate Charge	Qg	—	9.1	—	nC					
Gate-Source Charge	Q <sub>gs</sub>	_	1.5	—	nC	V <sub>GS</sub> = -4.5V, V <sub>DS</sub> = -10V I <sub>D</sub> = -4A				
Gate-Drain Charge	$Q_{gd}$	-	1.7	—	nC					
Turn-On Delay Time	t <sub>D(ON)</sub>	_	71	—	ns					
Turn-On Rise Time	t <sub>R</sub>	—	117	—	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$				
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	795	—	ns	$R_D = 2.5\Omega, R_G = 3.0\Omega, I_D = -1A$				
Turn-Off Fall Time	t <sub>F</sub>	—	393	—	ns	7				

Notes: 6. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.

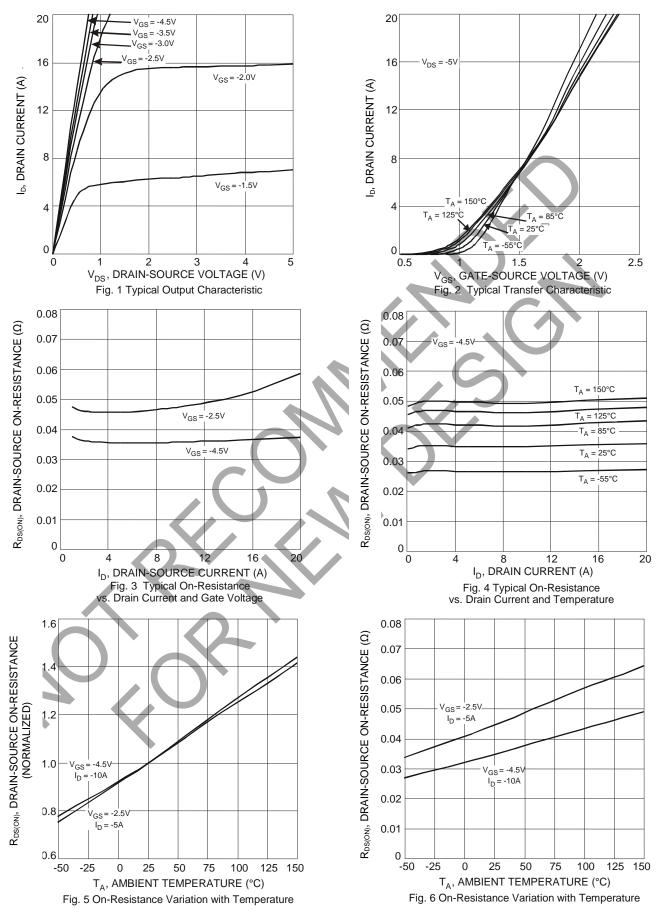
7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.



### NOT RECOMMENDED FOR NEW DESIGN USE <u>DMP2045U</u>

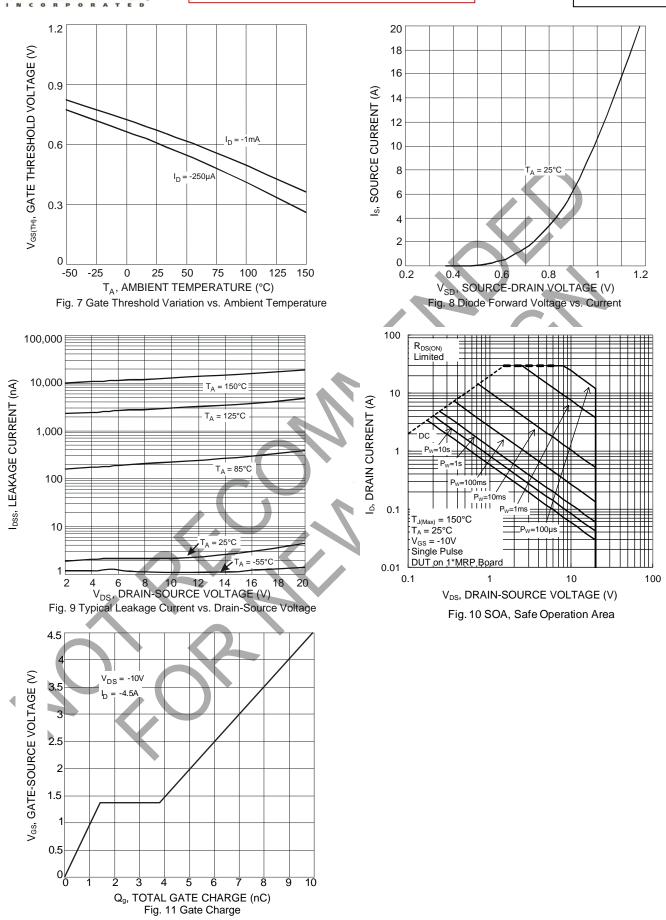
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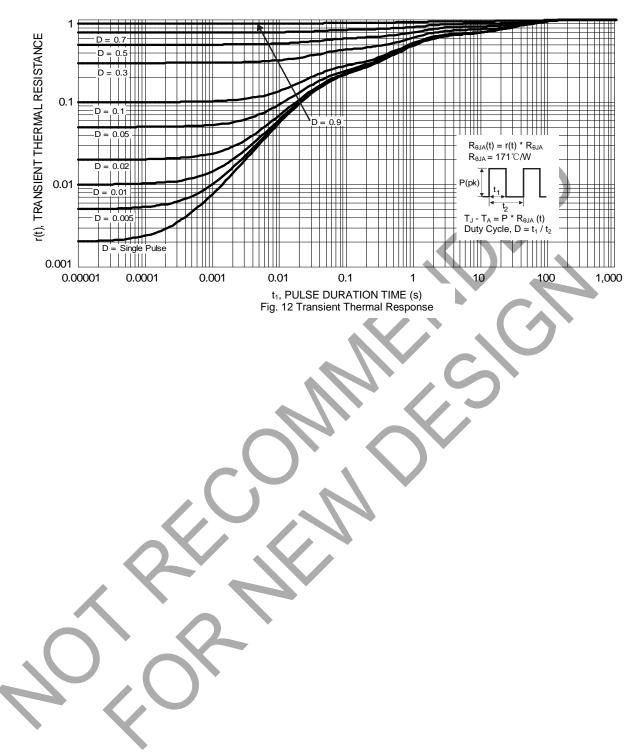






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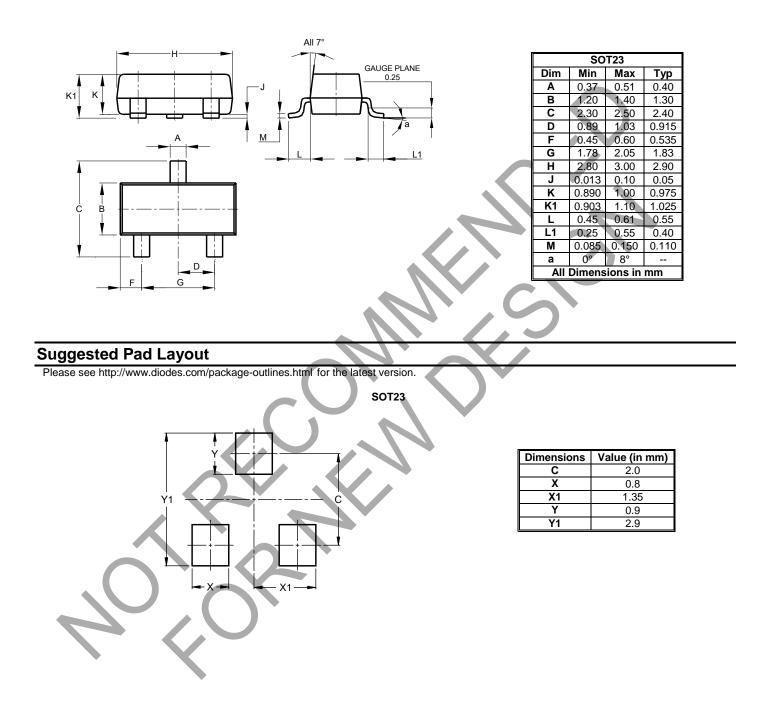




## Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23





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