

## Features

- Input voltage: 3.6V to 18V.
- Output voltage: 0.8V to V<sub>CC</sub>.
- Duty ratio: 0% to 100% PWM control
- Oscillation frequency: 300kHz typ.
- Current Limit, Enable function
- Thermal Shutdown function
- Built-in internal SW P-channel MOS
- Lead Free package: SOP-8L
- SOP-8L: Available in "Green" Molding Compound (No Br, Sb) (Note 1)
- Lead Free Finish/ RoHS Compliant (Note 2)

## General Description

AP1530 consists of step-down switching regulator with PWM control. These devices include a reference voltage source, oscillation circuit, error amplifier and internal PMOS.

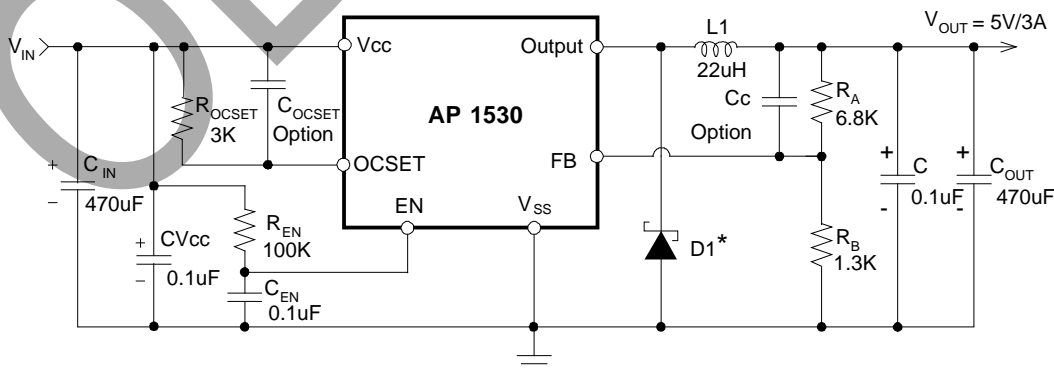
AP1530 provides low-ripple power, high efficiency, and excellent transient characteristics. The PWM control circuit is able to vary the duty ratio linearly from 0 up to 100%. This converter also contains an error amplifier circuit. An enable function, an over current protection and a short circuit protection are built inside, and when OCP or SCP happens, the operation frequency will be reduced from 300kHz to 50kHz. Also, an internal compensation block is built in to minimum external component count.

With the addition of an internal P-channel Power MOS, a coil, capacitors, and a diode connected externally, these ICs can function as step-down switching regulators. They serve as ideal power supply units for portable devices when coupled with the SOP-8L package, providing such outstanding features as low current consumption. Since this converter can accommodate an input voltage up to 18V, it is also suitable for the operation via an AC adapter.

## Applications

- PCD Motherboard
- LCD Monitor
- Graphic Card
- DVD-Video Player
- Telecom Equipment
- ADSL Modem
- Printer and other Peripheral Equipment
- Microprocessor core supply
- Networking power supply

## Typical Application Circuit

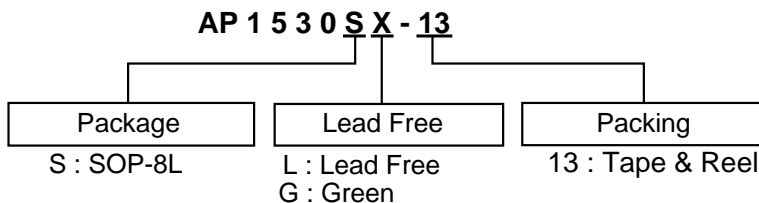


$$V_{OUT} = V_{FB} \times (1 + R_A/R_B)$$

$$R_B = 0.7K - 5K \text{ ohm}$$

\* Suggested DIODES Power Schottky P/N: B340 series or PDS340.

**Ordering Information**

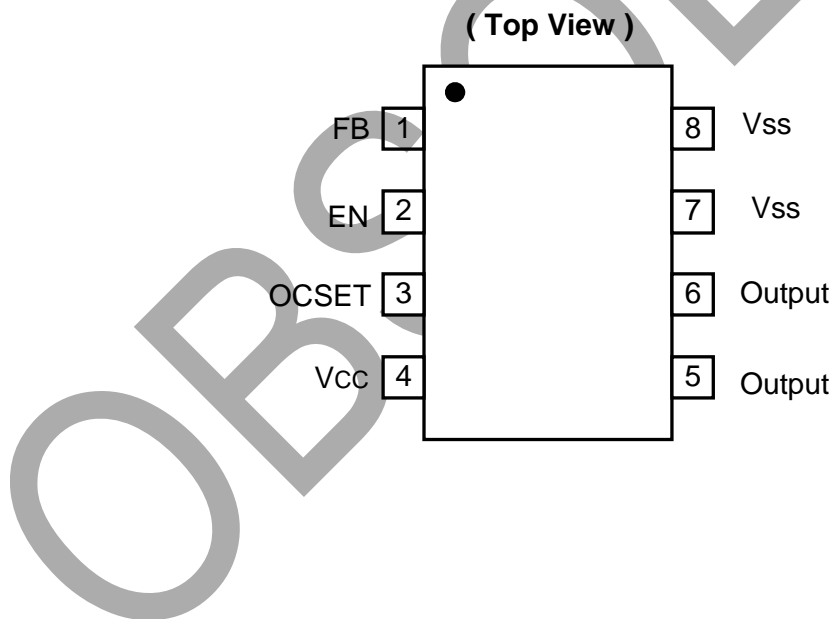


| Device      | Package Code | Packaging (Note 2) | 13" Tape and Reel |                    |
|-------------|--------------|--------------------|-------------------|--------------------|
|             |              |                    | Quantity          | Part Number Suffix |
| AP1530SL-13 | S            | SOP-8L             | 2500/Tape & Reel  | -13                |
| AP1530SG-13 | S            | SOP-8L             | 2500/Tape & Reel  | -13                |

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at [http://www.diodes.com/products/lead\\_free.html](http://www.diodes.com/products/lead_free.html).  
 2. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.

**Pin Assignments**

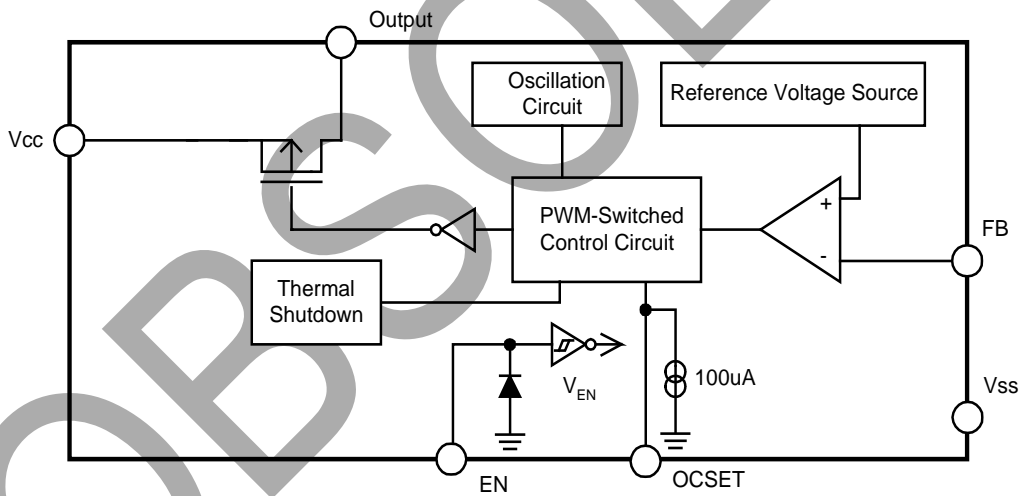
(1) SOP-8L



**Pin Descriptions**

| Pin Name        | Pin No. | Description   |
|-----------------|---------|---|
| FB              | 1       | Feedback pin  |
| EN              | 2       | Power-off pin<br>H: Normal operation (Step-down operation)<br>L: Step-down operation stopped (All circuits deactivated) |
| OCSET           | 3       | Add an external resistor to set max output current  |
| V <sub>CC</sub> | 4       | IC power supply pin   |
| Output          | 5, 6    | Switch Pin. Connect external inductor/diode here. Minimize trace area at this pin to reduce EMI                         |
| V <sub>SS</sub> | 7, 8    | GND Pin   |

**Block Diagram**



OBSOLETE - PART DISCONTINUED

### Absolute Maximum Ratings

| Symbol              | Parameter                            | Rating   | Unit |
|---------------------|--------------------------------------|--|------|
| ESD HBM             | Human Body Model ESD Protection      | 5  | KV   |
| ESD MM              | Machine Model ESD Protection         | 500  | V    |
| V <sub>CC</sub>     | V <sub>CC</sub> Pin Voltage          | V <sub>SS</sub> - 0.3 to V <sub>SS</sub> + 20  | V    |
| V <sub>FB</sub>     | Feedback Pin Voltage                 | V <sub>SS</sub> - 0.3 to V <sub>CC</sub>       | V    |
| V <sub>EN</sub>     | EN Pin Voltage                       | V <sub>SS</sub> - 0.3 to V <sub>IN</sub> + 0.3 | V    |
| V <sub>OUTPUT</sub> | Switch Pin Voltage                   | V <sub>SS</sub> - 0.3 to V <sub>IN</sub> + 0.3 | V    |
| P <sub>D</sub>      | Power Dissipation                    | Internally limited                             | mW   |
| T <sub>J</sub>      | Operating Junction Temperature Range | -40 to +125                                    | °C   |
| T <sub>ST</sub>     | Storage Temperature Range            | -65 to +150                                    | °C   |

Caution: The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

### Recommended Operating Conditions

| Symbol           | Parameter                     | Min | Max | Unit |
|------------------|-------------------------------|-----|-----|------|
| V <sub>IN</sub>  | Input Voltage                 | 3.6 | 18  | V    |
| I <sub>OUT</sub> | Output Current                | 0   | 3   | A    |
| T <sub>A</sub>   | Operating Ambient Temperature | -20 | 85  | °C   |

OBSOLETE

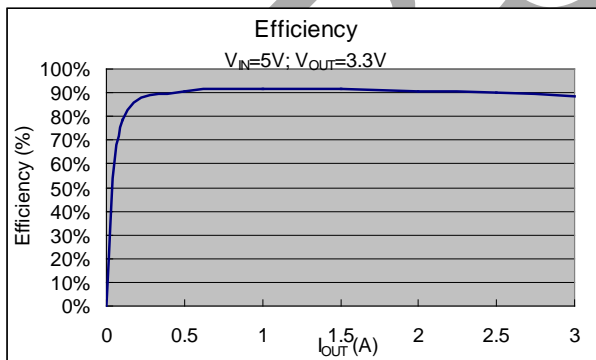
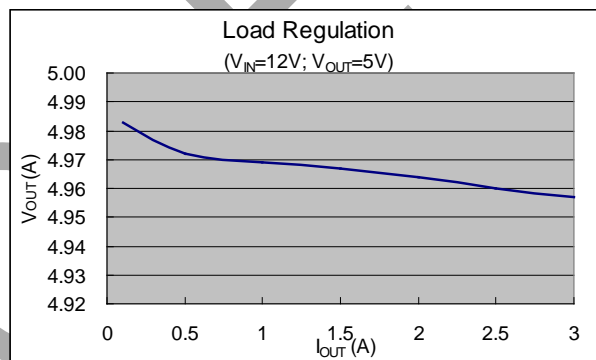
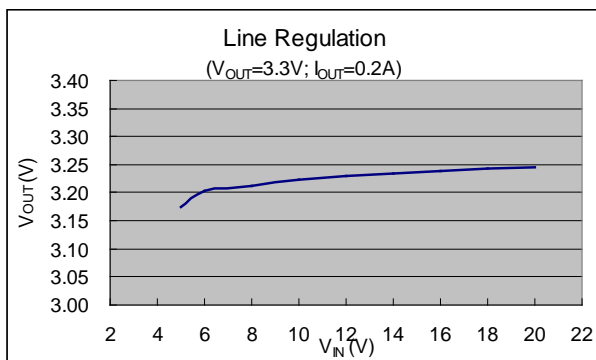
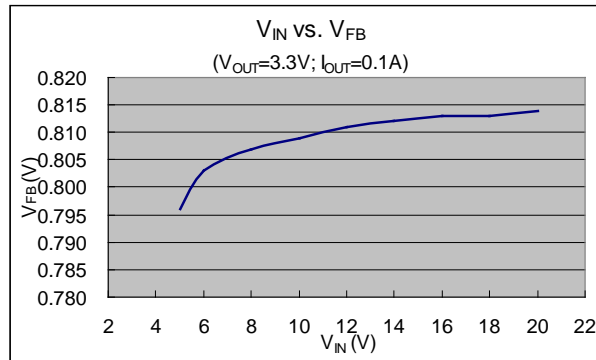
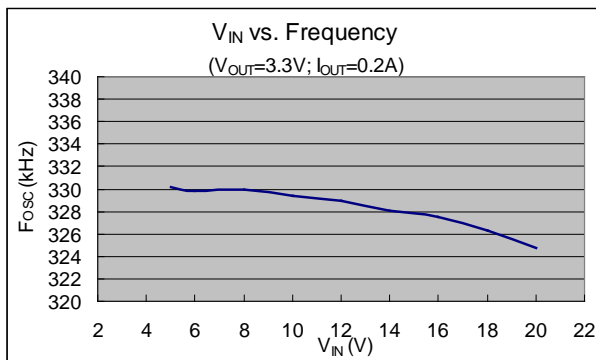
### Electrical Characteristics

( $V_{IN} = 12V$ ,  $T_A = 25^\circ C$ , unless otherwise specified)

| Symbol                           | Parameter  | Conditions  | Min      | Typ.      | Max      | Unit         |
|----------------------------------|--|---|----------|-----------|----------|--------------|
| $V_{FB}$                         | Feedback Voltage                                       | $I_{OUT} = 0.1A$  | 0.784    | 0.8       | 0.816    | V            |
| $I_{FB}$                         | Feedback Bias Current                                  | $I_{OUT} = 0.1A$  | -        | 0.1       | 0.5      | $\mu A$      |
| $I_{SW}$                         | Switch Current   | --  | 3.5      | -         | -        | A            |
| $I_{SHDN}$                       | Current Consumption During Power Off                   | $V_{EN} = 0V$   | -        | 10        | -        | $\mu A$      |
| $\frac{\Delta V_{OUT}}{V_{IN}}$  | Line Regulation  | $V_{IN} = 5V \sim 18V$  | -        | 1         | 2        | %            |
| $\frac{\Delta V_{OUT}}{I_{OUT}}$ | Load Regulation  | $I_{OUT} = 0.1$ to 3A   | -        | 0.2       | 0.5      | %            |
| $f_{OSC}$                        | Oscillation Frequency                                  | Measure waveform at SW pin  | 240      | 300       | 360      | kHz          |
| $f_{OSC1}$                       | Frequency of Current Limit or Short Circuit Protection | Measure waveform at SW pin  | -        | 50        | -        | kHz          |
| $V_{IH}$<br>$V_{IL}$             | EN Pin Input Voltage                                   | Evaluate oscillation at SW pin<br>Evaluate oscillation stop at SW pin | 2.0<br>- | -<br>-    | -<br>0.8 | V            |
| $I_{ENH}$<br>$I_{ENL}$           | EN Pin Input Leakage Current                           | --  | -<br>-   | 20<br>-10 | -<br>-   | $\mu A$      |
| $I_{OCSET}$                      | OCSET Pin Bias Current                                 | --  | 75       | 90        | 105      | $\mu A$      |
| $T_{SS}$                         | Soft-Start Time  | --  | 0.3      | 2         | 5        | ms           |
| $T_{SHDN}$                       | Thermal shutdown threshold                             |   | -        | 150       | -        | $^\circ C$   |
| $T_{HYS}$                        | Thermal shutdown hysteresis                            |   | -        | 55        | -        | $^\circ C$   |
| $R_{DS(ON)}$                     | Internal MOSFET $R_{DS(ON)}$                           | $V_{IN} = 5V, V_{FB} = 0V$  | -        | 110       | 150      | m $\Omega$   |
|                                  |  | $V_{IN} = 12V, V_{FB} = 0V$   | -        | 70        | 100      |              |
| EFFI                             | Efficiency   | $V_{IN} = 12V, V_{OUT} = 5V$<br>$I_{OUT} = 3A$                        | -        | 92        | -        | %            |
| $\theta_{JA}$                    | Thermal Resistance Junction-to-Ambient                 | SOP-8L (Note 3)   | -        | 134       | -        | $^\circ C/W$ |
| $\theta_{JC}$                    | Thermal Resistance Junction-to-Case                    | SOP-8L (Note 3)   | -        | 22        | -        | $^\circ C/W$ |

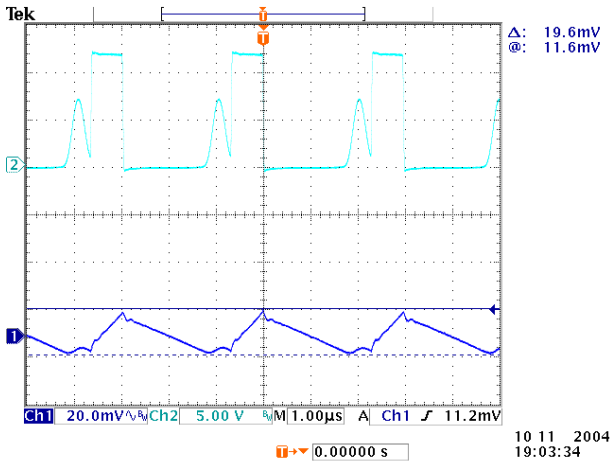
Notes: 3. Test condition SOP-8L: Device mounted on FR-4 substrate 2oz copper, minimum recommended pad layout, and single side. For better thermal performance, please arrange larger copper pad of layout for heatsink.

**Typical Performance Characteristics**

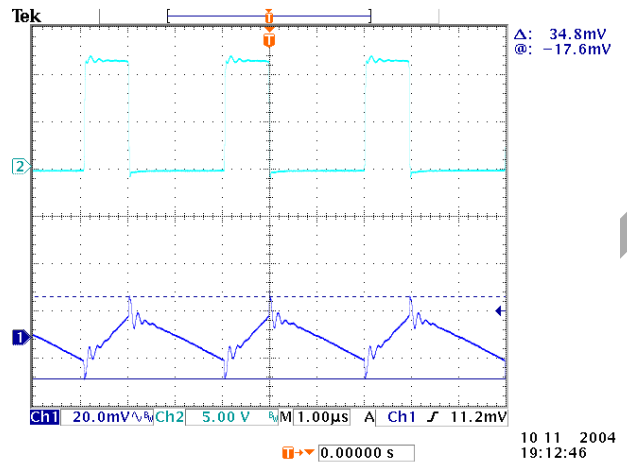


**Typical Performance Characteristics (Continued)**

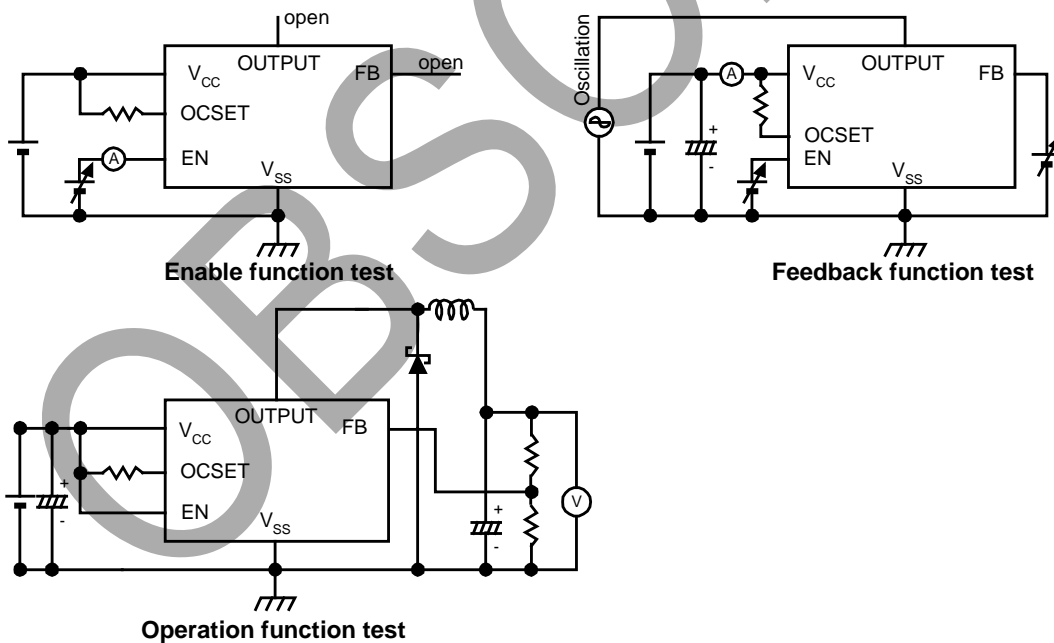
**AP1530 Vout Ripple  
(Vin= 12V; Vout= 3.3V; Iout= 0.1A)**



**AP1530 Vout Ripple  
(Vin= 12V; Vout= 3.3V; Iout= 3A)**



**Test Circuit**



### Functional Description

#### PWM Control

The AP1530 is a DC/DC converter that employs pulse width modulation (PWM) scheme. Its pulse width varies in the range of 0% to 99%, based on the output current loading. The output ripple voltage caused by the PWM high frequency switching can easily be reduced through an output filter. Therefore, this converter provides a low ripple output supply over a broad range of input voltage & output current loading

#### Under Voltage Lockout

The under voltage lockout circuit of the AP1530 assures that the high-side MOSFET driver remains in the off state whenever the supply voltage drops below 3.3V. Normal operation resumes once V<sub>CC</sub> rises above 3.5V.

#### Current Limit Protection

The current limit threshold is set by external resistor R<sub>OCSET</sub> connected from V<sub>CC</sub> supply to OCSET pin. The internal sink current I<sub>OCSET</sub> (90uA typical) across this resistor sets the voltage at OCSET pin. When the PWM voltage is less than the voltage at OCSET, an over-current condition is triggered.

The current limit threshold is given by the following equation:

$$I_{PEAK} \times R_{DS(ON)} = I_{OCSET} \times R_{OCSET}$$

$$I_{PEAK} > I_{OUT(MAX)} + \frac{(\Delta I)}{2}$$

where,

$$\Delta I = \frac{V_{IN} - V_{OUT}}{F_s \times L} \times \frac{V_{OUT}}{V_{IN}}$$

I<sub>PEAK</sub> is the output peak current; R<sub>DS(ON)</sub> is the MOSFET ON resistance; F<sub>s</sub> is the PWM frequency (300KHz typical). Also, the inductor value will affect the ripple current ΔI.

The above equation is recommended for input voltage range of 5V to 18V. For input voltage lower than 5V or ambient temperature over 100°C, higher R<sub>OCSET</sub> is recommended.

The recommended minimum R<sub>OCSET</sub> value is summarized below:

| V <sub>IN</sub> (V) | V <sub>OUT</sub> (V) | R <sub>OCSET</sub> (Ω) |
|---------------------|----------------------|------------------------|
| 4                   | 0.8                  | 3.9K                   |
| 5                   | 3.3                  | 3.0K                   |
| 12                  | 5                    | 3.0K                   |
| 18                  | 12                   | 3.0K                   |

#### Inductor Selection

For most designs, the operates with inductors of 22μH to 33μH. The inductor value can be derived from the following equation:

$$L = \frac{V_{IN} - V_{OUT}}{f_s \times \Delta I} \times \frac{V_{OUT}}{V_{IN}}$$

Where ΔI<sub>L</sub> is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple current. Choose inductor ripple current approximately 15% of the maximum load current 3A, ΔI<sub>L</sub>=0.45A. The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation (3A+0.225A).

#### Input Capacitor Selection

This capacitor should be located close to the IC using short leads and the voltage rating should be approximately 1.5 times the maximum input voltage. The RMS current rating requirement for the input capacitor of a buck regulator is approximately 1/2 the DC load current. A low ESR input capacitor sized for maximum RMS current must be used. A 470μF low ESR capacitor for most applications is sufficient.

#### Output Capacitor Selection

The output capacitor is required to filter the output voltage and provides regulator loop stability. The important capacitor parameters are the 100KHz Equivalent Series Resistance (ESR), the RMS ripples current rating, voltage rating and capacitance value. For the output capacitor, the ESR value is the most important parameter. The output ripple can be calculated from the following formula.

$$V_{RIPPLE} = \Delta I_L \times ESR$$

The bulk capacitor's ESR will determine the output ripple voltage and the initial voltage drop after a high slew-rate transient.

An aluminum electrolytic capacitor's ESR value is related to the capacitance and its voltage rating. In most case, higher voltage electrolytic capacitors have lower ESR values. Most of the time, capacitors with much higher voltage ratings may be needed to provide the low ESR values required for low output ripple voltage.

#### PCB Layout Guide

If you need low T<sub>c</sub> & T<sub>j</sub> or large PD (Power Dissipation), The dual SW pins(5& 6) and V<sub>SS</sub> pins(7& 8)on the SOP-8L package are internally connected to die pad, The evaluation board should be allowed for maximum copper area at output (SW) pins.

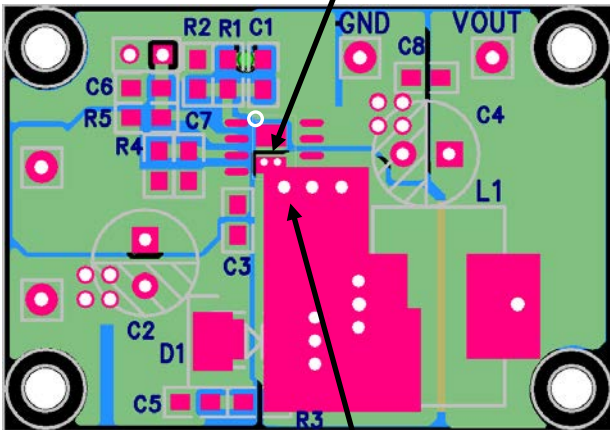
1. Connect FB circuits (R<sub>1</sub>, R<sub>2</sub>, C<sub>1</sub>) as closely as possible and keep away from inductor flux for pure V<sub>FB</sub>.
2. Connect C3 to V<sub>CC</sub> and V<sub>SS</sub> pin as closely as possible to get good power filter effect.
3. Connect R4 to V<sub>CC</sub> and OCSET pin as closely as possible.
4. Connect ground side of the C2 & D1 & C4 as closely as possible and use ground plane for best performance.



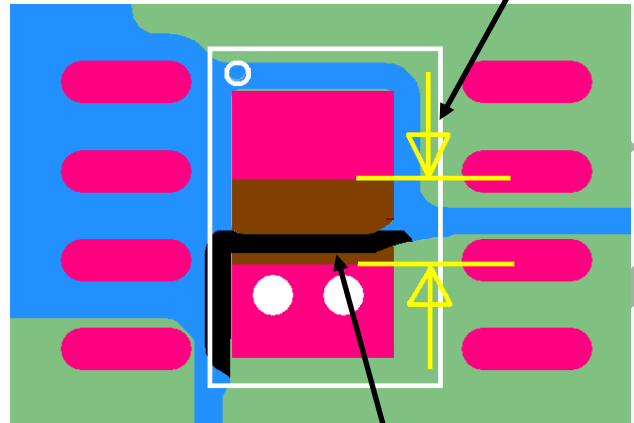
**Functional Description (Continued)**

Keep the gap of exposed pads from short circuit.

Top Side Layout Guide



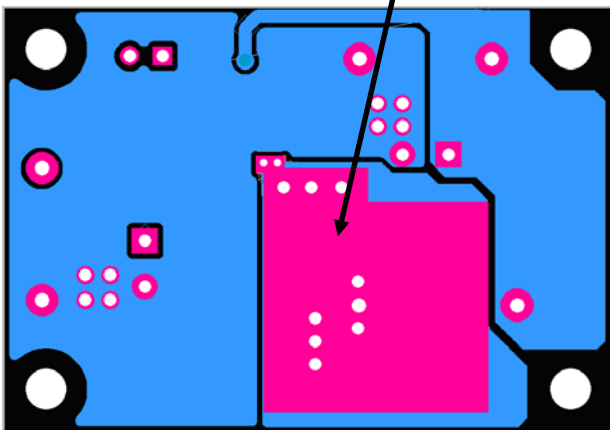
Recommended exposed-pads gap: 30~40mil (0.75~1mm)



Use vias to conduct the heat into the backside of PCB layer. The heat sink at output (SW) pins should be allowed for maximum solder-painted area.

Brown: IC exposed pads.  
Red: recommended layout.  
Reference pads layout dimension:  
Output: 90 x 50 mil  
Vss: 90 x 60 mil

Bottom Side Layout Guide



**Functional Description (Continued)**

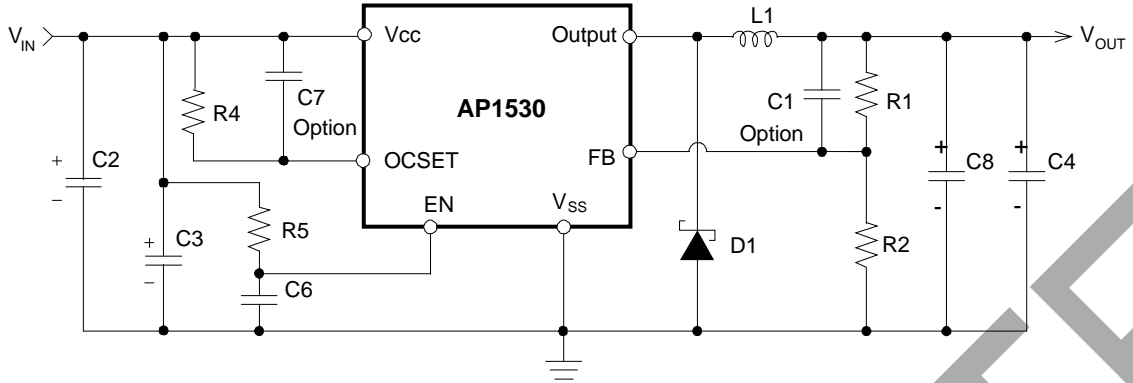
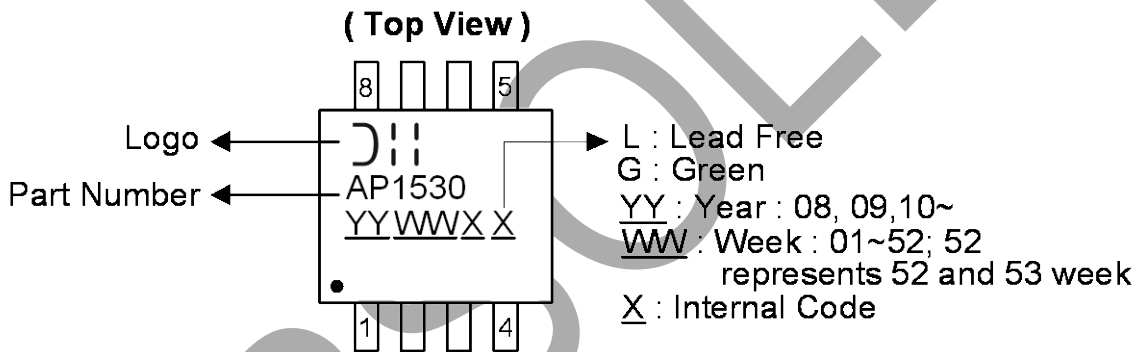


Figure: Layout numbering comparison

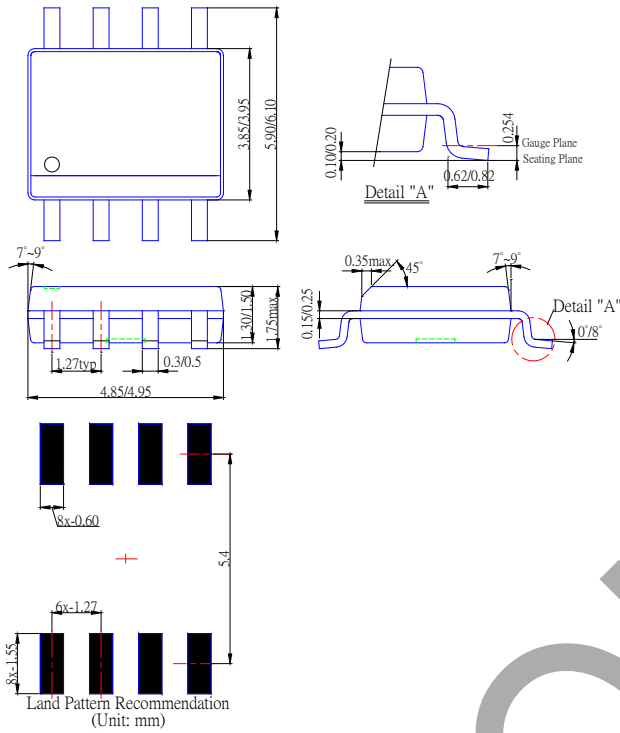
**Marking Information**

(1) SOP-8L



**Package Information (All Dimensions in mm)**

**(1) Package Type: SOP-8L**



OBSOLETE

**IMPORTANT NOTICE**

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.

Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

**LIFE SUPPORT**

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2019, Diodes Incorporated

[www.diodes.com](http://www.diodes.com)

单击下面可查看定价，库存，交付和生命周期等信息

[>>Diodes Incorporated\(达尔科技\)](#)