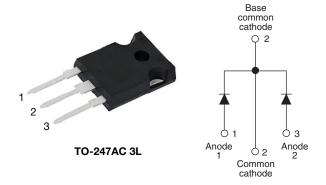


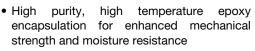
High Performance Schottky Rectifier, 2 x 40 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 40 A				
V_{R}	150 V				
V _F at I _F	0.71 V				
I _{RM} max.	26 mA at 125 °C				
T _J max.	175 °C				
E _{AS}	0.5 mJ				
Package	TO-247AC 3L				
Circuit configuration	Common cathode				

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- · High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-80CPQ150... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform	80	Α				
V _{RRM}		150	V				
I _{FSM}	$t_p = 5 \mu s sine$	1930	Α				
V _F	40 A _{pk} , T _J = 125 °C (per leg)	0.71	V				
TJ		-55 to +175	°C				

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-80CPQ150-N3	UNITS
Maximum DC reverse voltage	V_R	150	V
Maximum working peak reverse voltage	V_{RWM}	130	V

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDI	TIONS	VALUES	UNITS
Maximum average forward per leg		_	50 % d 1 d 50 %		40	
current, see fig. 5 per de	per device	I _{F(AV)}	50 % duty cycle at T _C = 150 °C, rectangular waveform		80	A
Maximum peak one cycle non-repetitive surge current per leg, see fig. 7		I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	1930	
			10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	500	
Non-repetitive avalanche energy per leg		E _{AS}	$T_J = 25 ^{\circ}\text{C}$, $I_{AS} = 1.0 \text{A}$, $L = 1 \text{mH}$		0.5	mJ
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	TYP.	MAX.	UNITS	
		40 A	T 05 °C	0.82	0.86	V	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	80 A	T _J = 25 °C	0.97	1.09		
See fig. 1	V _{FM} (1)	40 A	T _{.1} = 125 °C	0.67	0.71		
		80 A	1j = 125 C	0.80	0.85		
Maximum reverse	_	T _J = 25 °C		10	200	μA	
leakage current per leg See fig. 2	I _{RM}	T _J = 125 °C	V _R = Rated V _R	12	26	mA	
Typical junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		-	1100	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		-	7.5	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		=	10 000	V/µs	

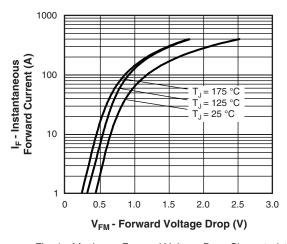
Note

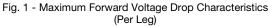
 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and temperature range	d storage	T _J , T _{Stg}		-55 to 175	°C
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package		Б	DC operation See fig. 4	0.6	
		- R _{thJC}	DC operation	0.3	°C/W
Typical thermal resista case to heatsink	ance,	R _{thCS}	Mounting surface, smooth and greased	0.24	
Aitil-t				6	g
Approximate weight				0.21	OZ.
Mounting torque	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf · in)
Marking device Case style TO-247AC 3L 80CPC		Q150			









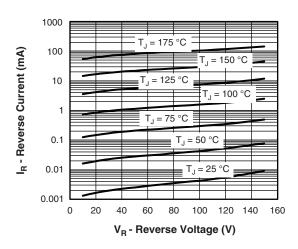


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

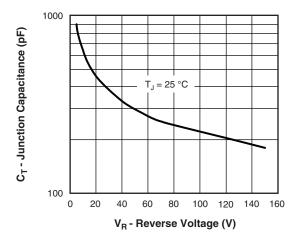


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

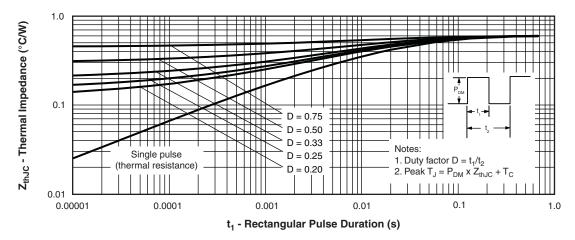
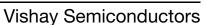


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)





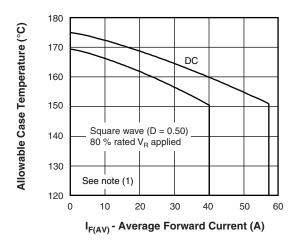


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

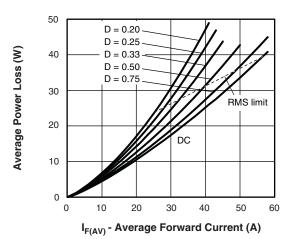


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

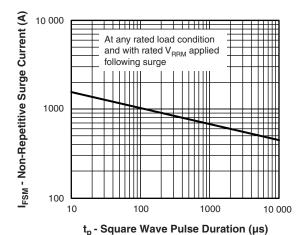


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

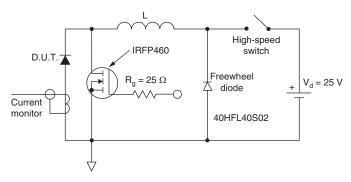


Fig. 8 - Unclamped Inductive Test Circuit

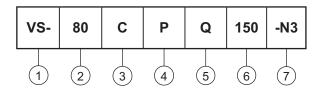
Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (80 = 80 A)

Circuit configuration:

C = common cathode

4 - Package:

P = TO-247

5 - Schottky "Q" series

6 - Voltage code (150 = 150 V)

7 - Environmental digit

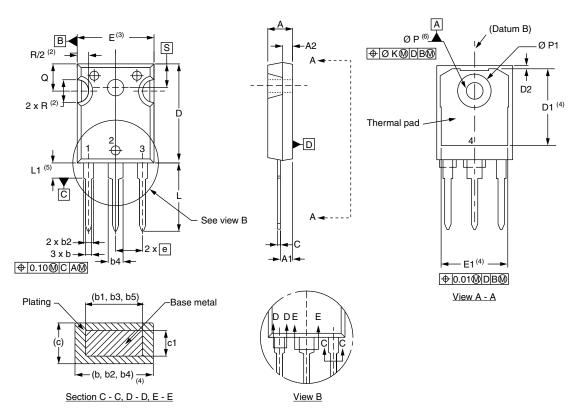
-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-80CPQ150-N3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?96138</u>				
Part marking information	www.vishay.com/doc?95007			

TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	TERS INCHES		NOTES
STINIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIMETERS		INC	NOTES	
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.35	0.020	0.053	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.254		0.0)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51	BSC	0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



Vishay

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