

N-Channel Enhancement Mode MOSFET

● DESCRIPTION

The VIC1221 is the n-channel logic enhancement mode power field effect transistor is produced using high cell density, advanced trench technology to provide excellent Rds(on).

This device is suitable for use as a load switch or in PWM applications.

These devices are particularly suited for low voltage application, and low in-line power loss are needed in a very small outline surface mount package.

● FEATURE

- ◆ VDS=20V; VGS=±8V; ID=2.2A
- ◆ RDS(ON)=75m Ω (TYP.)@VGS=4.5V
- ◆ RDS(ON)=90m Ω (TYP.)@VGS=2.5V

● APPLICATIONS

- ◆ Power Management in Notebook
- ◆ Portable Equipment
- ◆ Battery Powered System
- ◆ DC/DC Converter
- ◆ Load Switch, DSC LCD Display inverter

● PIN CONFIGURATION



● ABSOLUTE MAXIMUM RATINGS(TA=25°C Unless otherwise noted)

Symbol	Parameter	Rating		Unit
VDS	Drain-Source Voltage	20	±8	V
VGS	Gate-Source Voltage			
ID	Continuous Drain Current	VGS=4.5V	2.2	A
IDP	Power Dissipation	10	10	A
TJ	Maximum Junction Temperature	-55 to +150		°C
TSTG	Storage Temperature Range	-55 to +150		
PD	Maximum Power Dissipation (Ta=25°C)	1		W

**● ELECTRICAL CHARACTERISTICS(TA=25°C Unless otherwise noted)**

Symbol	Parameter	Test Conditions	VIC1221DI			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =10μA	20	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V	--	--	1	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =50μA	0.4	0.75	2.0	V
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±8V, V _{DS} =0V	--	--	±100	nA
R _{DSON} ^a	Drain-Source On-state Resistance	V _{GS} =4.5V, I _D =2.2A	--	70	85	mΩ
		V _{GS} =2.5V, I _D =2A	--	90	115	
Dynamic b						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =10V, Frequency=1.0MHz	--	450	--	pF
C _{oss}	Output Capacitance		--	70	--	
C _{rss}	Reverse Transfer Capacitance		--	43	--	
SWITCHING CHARACTERISTICS						
t _{d(ON)}	Turn-on Delay Time	V _{DD} =-6V, R _L =6Ω, I _{DS} =-1.0A, V _{GEN} =-4.5V, R _G =6Ω	--	15	--	ns
t _{d(OFF)}	Turn-off Delay Time		--	60	--	
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
V _{sd} ^a	Diode Forward Voltage	I _s = 1.1A, V _{GS} = 0V	0.6	0.8	1.15	V

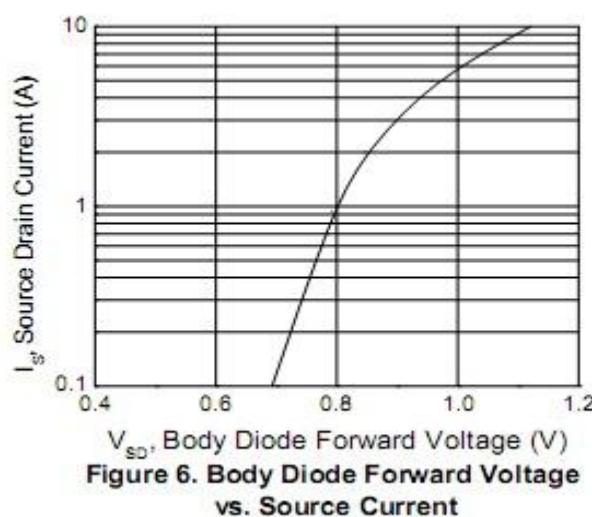
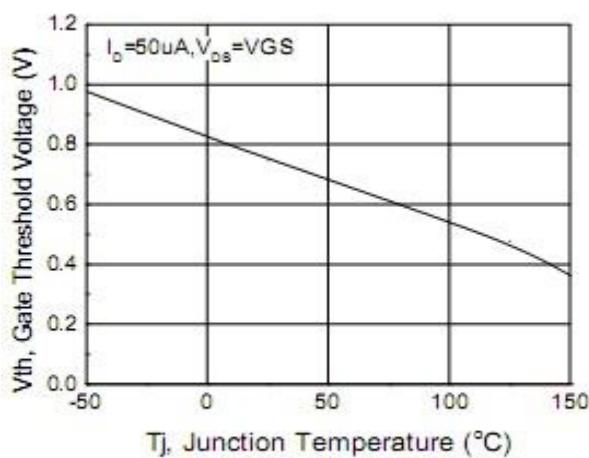
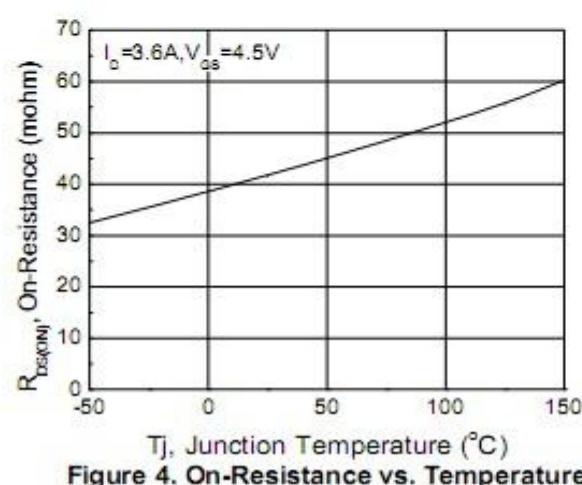
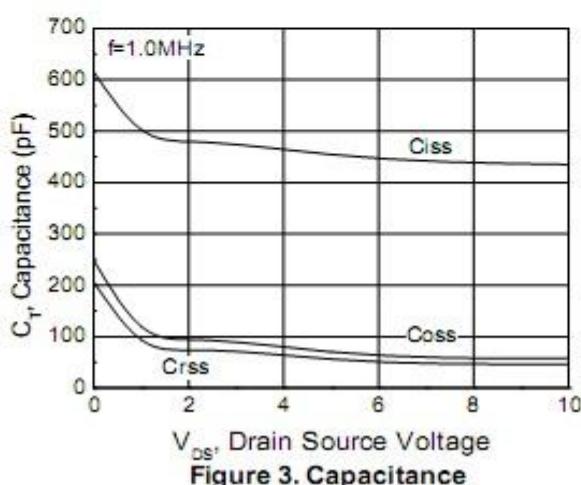
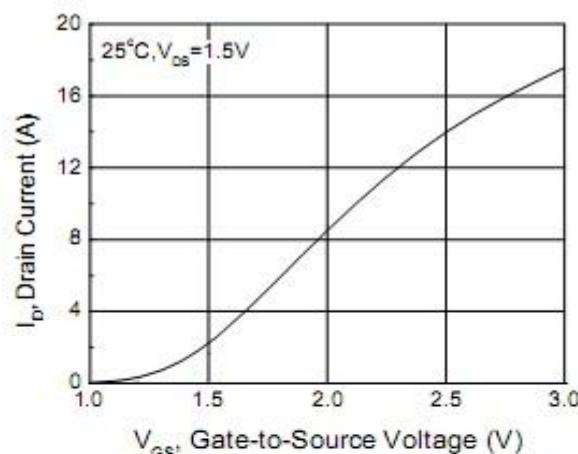
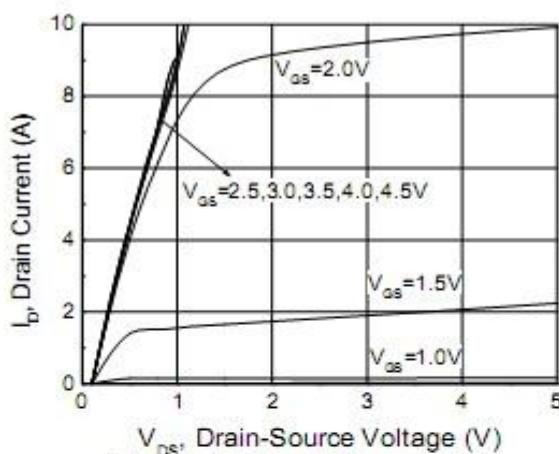
Notes:

- Surface Mounted on FR4 Board.T<10 sec.
- Pulse test:PW < 300μs,duty cycle < 2%.

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- **TYPICAL CHARACTERISTICS (TA=25°C Unless otherwise noted)**



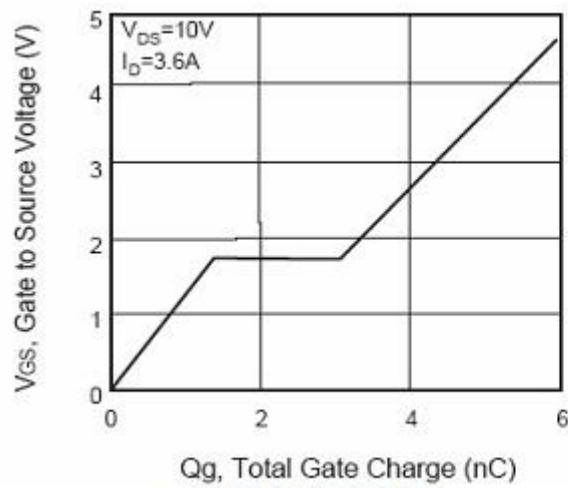
● **TYPICAL CHARACTERISTICS (TA=25°C Unless otherwise noted)**


Figure 7. Gate Charge

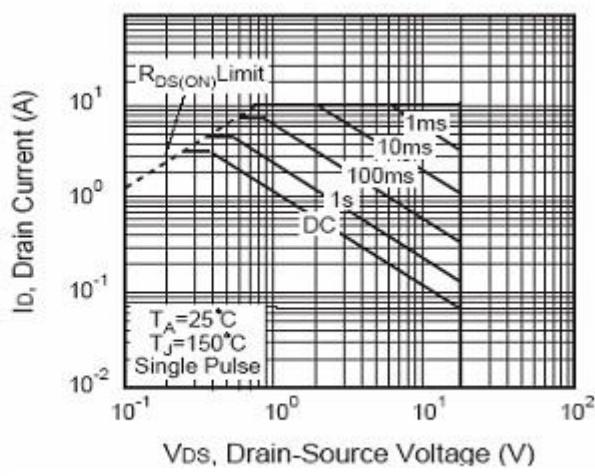
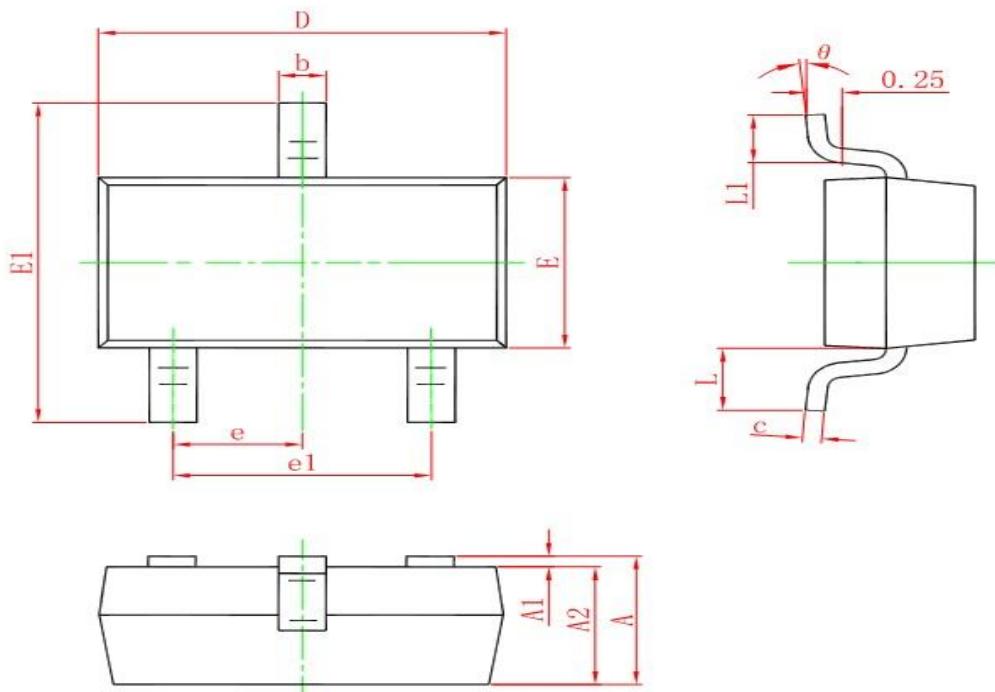


Figure 8. Maximum Safe Operating Area

● ORDERING INFORMATION

Part Number	Package code	Shipping
VIC1221DI	DI: SOT23	3000/Tape & Reel

● PACKAGE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°



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